

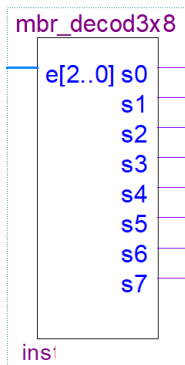
Aluno: Maximus Borges da Rosa

Cartão: 00342337

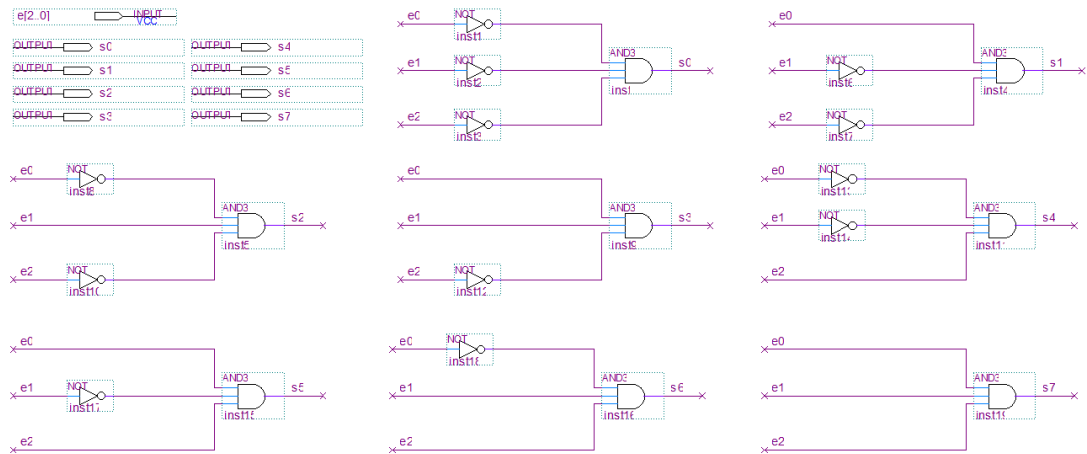
Nome do Projeto: mbr_decod3x8

Descrição: Circuito do Decodificador 3x8.

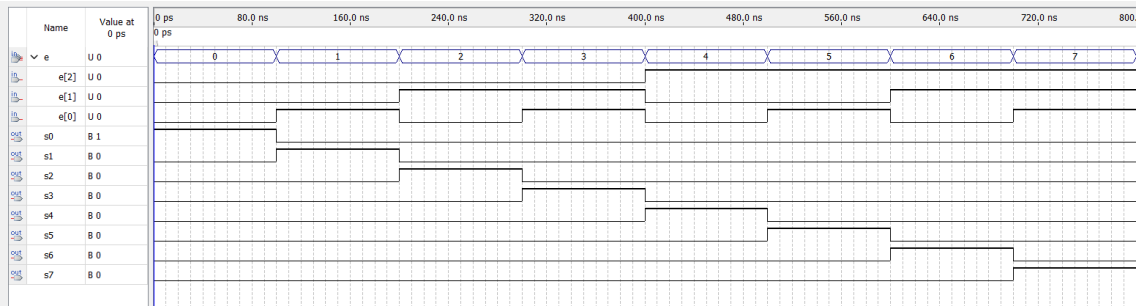
Símbolo:



Circuito:



Simulação:



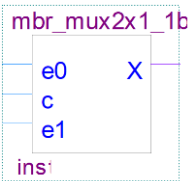
Aluno: Maximus Borges da Rosa

Cartão: 00342337

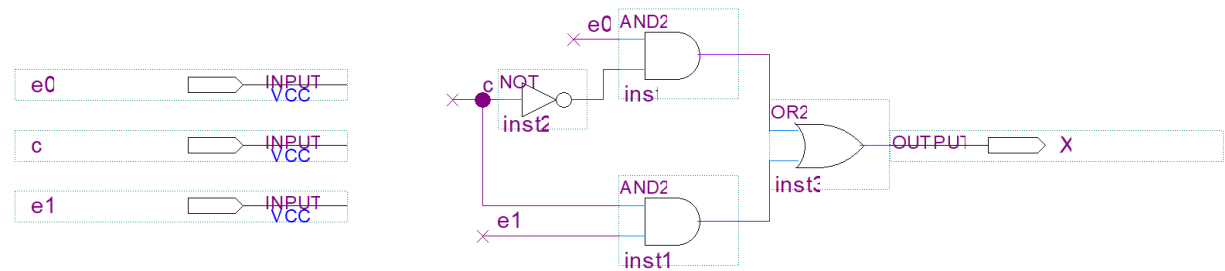
Nome do Projeto: mbr_mux2x1_1b

Descrição: Circuito do Multiplexador 2x1 de 1 bit.

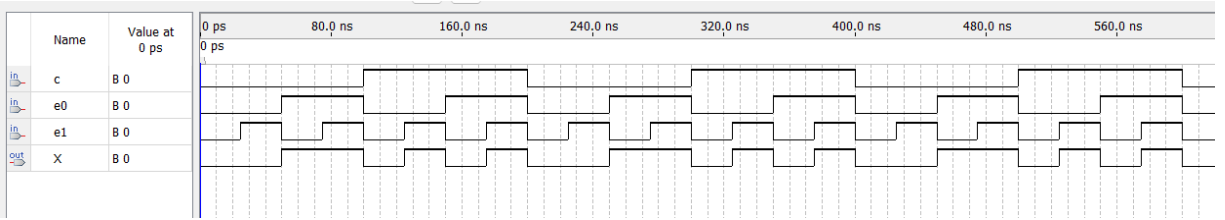
Símbolo:



Circuito:



Simulação:



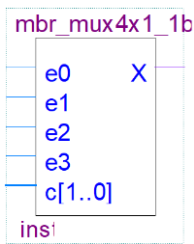
Aluno: Maximus Borges da Rosa

Cartão: 00342337

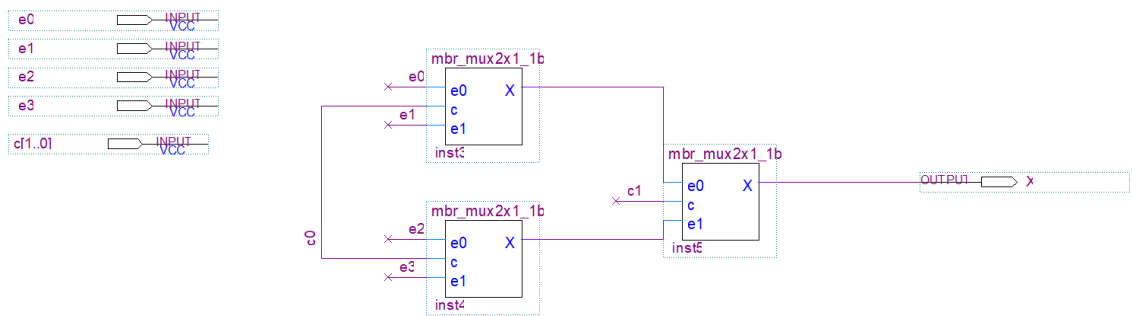
Nome do Projeto: mbr_mux4x1_1b

Descrição: Circuito do Multiplexador 4x1 de 1 bit.

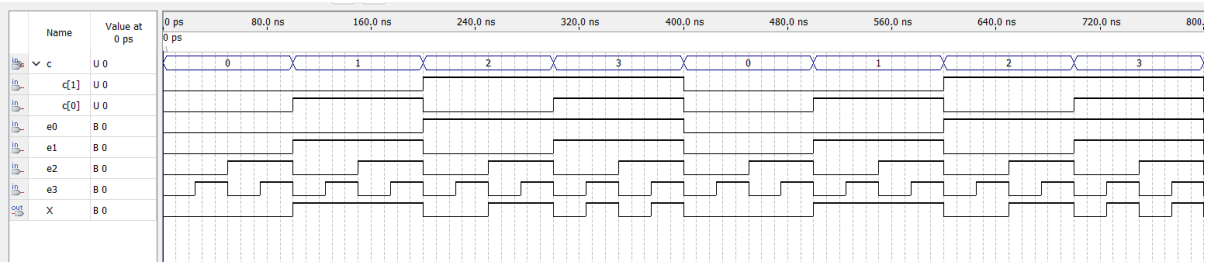
Símbolo:



Circuito:



Simulação:



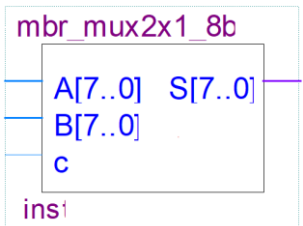
Aluno: Maximus Borges da Rosa

Cartão: 00342337

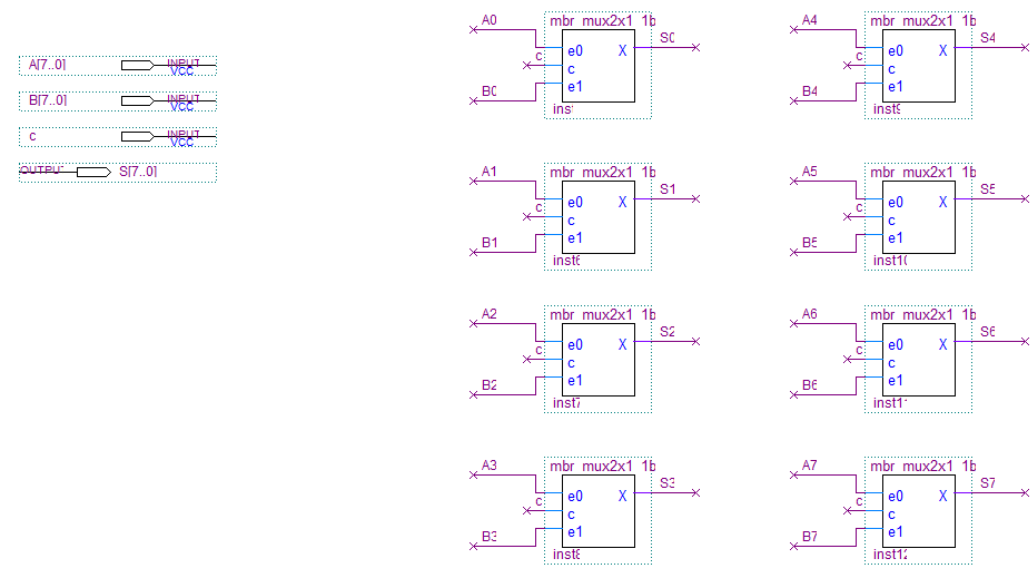
Nome do Projeto: mbr_mux2x1_8b

Descrição: Circuito do Multiplexador 2x1 de 8 bits.

Símbolo:



Circuito:



Simulação:

