

# Addition & Subtraction

Just like in grade school (carry/borrow 1s)

$$\begin{array}{r} 0111 \\ + 0110 \\ \hline \end{array}$$

$$\begin{array}{r} 0111 \\ - 0110 \\ \hline \end{array}$$

$$\begin{array}{r} 0110 \\ - 0101 \\ \hline \end{array}$$

# Addition & Subtraction

Two's complement operations easy

- subtraction using addition of negative numbers

$$\begin{array}{r} 0111 \quad (7) \\ + \underline{1010} \quad (-6) \end{array}$$

$$\begin{array}{r} 0111 \quad (7) \\ - \underline{0110} \quad (6) \end{array}$$

# Addition & Subtraction

Overflow (result too large for finite computer word):

- adding two n-bit numbers does not yield an n-bit number

$$\begin{array}{r} 1111 \\ + 0001 \\ \hline 10000 \end{array}$$

- How about  $-1 + -1$ ?

# Detecting Overflow

No overflow when adding positive to negative number

No overflow when signs are the same for subtraction

Overflow occurs when the value affects the sign:

- overflow when adding two positives yields a negative
- or, adding two negatives gives a positive
- or, subtract a negative from a positive and get a negative
- or, subtract a positive from a negative and get a positive

Consider the operations  $A + B$ , and  $A - B$

- Can overflow occur if  $B$  is 0 ?
- Can overflow occur if  $A$  is 0 ?

# Unsigned Multiplication

- Binary multiplication follows the same basic process as decimal multiplication
  - Multiplicand is multiplied by the current digit of the multiplier.

Partial-product terms are put in proper position:

$$\begin{array}{r} \phantom{00}0110 \\ \times 1011 \\ \hline \phantom{00}0110 \\ \phantom{00}0110 \\ \phantom{00}0000 \\ \phantom{00}0110 \\ \hline 1000010 \end{array}$$

$$\# A = 6$$

$$\# B = 11$$

$$\# \text{ Product} = 66$$

# Unsigned Multiplication

- For fixed-digit multiply (computers), all digits are present in multiplier, multiplicand, partial-product terms. We just don't show all the zeros when doing hand multiplication:

```
      00000110
x 00001011
-----
      00000110
      00001100
      00000000
      00110000
-----
     01000010
```

# A = 6

# B = 11

# Product = 66

# Partial Product Terms

- Partial product terms are either zero, or the multiplicand times a power of 2
  - Recall that each power of 2 is one left shift

00000110	# A = 6
x 00001011	# B = 11
-----	
00000110	# A x 2 <sup>0</sup>
00001100	# A x 2 <sup>1</sup>
00000000	# 0
00110000	# A x 2 <sup>3</sup>
-----	
01000010	# Product= 66

# Partial Product Terms (cont.)

- We can add the partial product terms to the product as they are generated:

00000110	# A = 6
x 00001011	# B = 11
-----	
00000000	
+00000110	
-----	
00000110	# Product = 6
+00001100	
-----	
00010010	# Product = 18
+00000000	
-----	
00010010	# Product = 18
+00110000	
-----	
01000010	# Product = 66



# Standard Multiply Algorithm

- Multiply can be done using a series of shift and add operations:

product = 0

while multiplier is non-zero

if multiplier LSB = 1

product = product + multiplicand

multiplier = multiplier >> 1                      # look at next bit

multiplicand = multiplicand << 1              # times 2

# Multiply Algorithm Example

```
    00000110  # A is the multiplicand
x   00001011  # B is the multiplier
-----
```

```
Cycle 1: A= 00000110, B = 00001011
          If condition is true, Product = 00000110
Cycle 2: A= 00001100, B = 00000101
          If condition is true, Product = 00010010
Cycle 3: A= 00011000, B = 00000010
          If condition is false, Product = 00010010
Cycle 4: A= 00110000, B = 00000001
          If condition is true, Product = 01000010
Cycle 5: A= 01100000, B = 00000000
          Loop ends, Product = 01000010
```

# Signed Multiplication

- Standard shift and add algorithm only works for positive numbers
- To include negative numbers with standard method must:
  - Save XOR of sign bits to get product sign bit
  - Convert multiplier/multiplicand to positive
  - Do shift and add algorithm
  - Negate result if product sign bit is 1

# Truth Table for Adder Bit Slice

3 inputs (A, B, Cin); 2 outputs (Sum, Cout)

A	B	Cin	Sum	Cout
0	0	0	0	0
0	0	1	1	0
0	1	0	1	0
0	1	1	0	1
1	0	0	1	0
1	0	1	0	1
1	1	0	0	1
1	1	1	1	1

# Adder Equations

$$\text{Sum} = (A \oplus B) \oplus \text{Cin}$$

$$\text{Carry} = AB + AC_{\text{in}} + BC_{\text{in}}$$

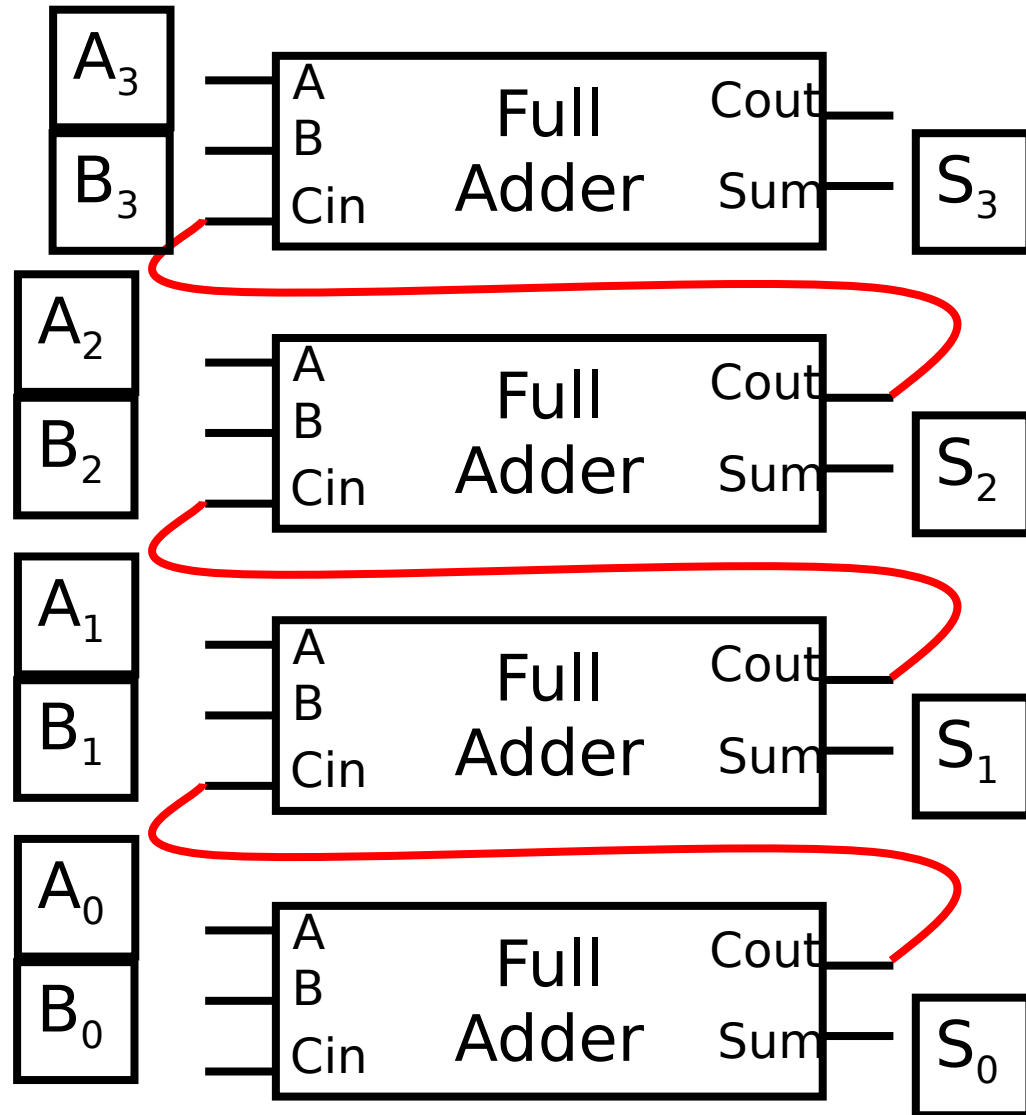
Abstract as “Full Adder”:



# Cascading Adders

Cascade Full Adders to make multi-bit adder:

$$A+B=S$$



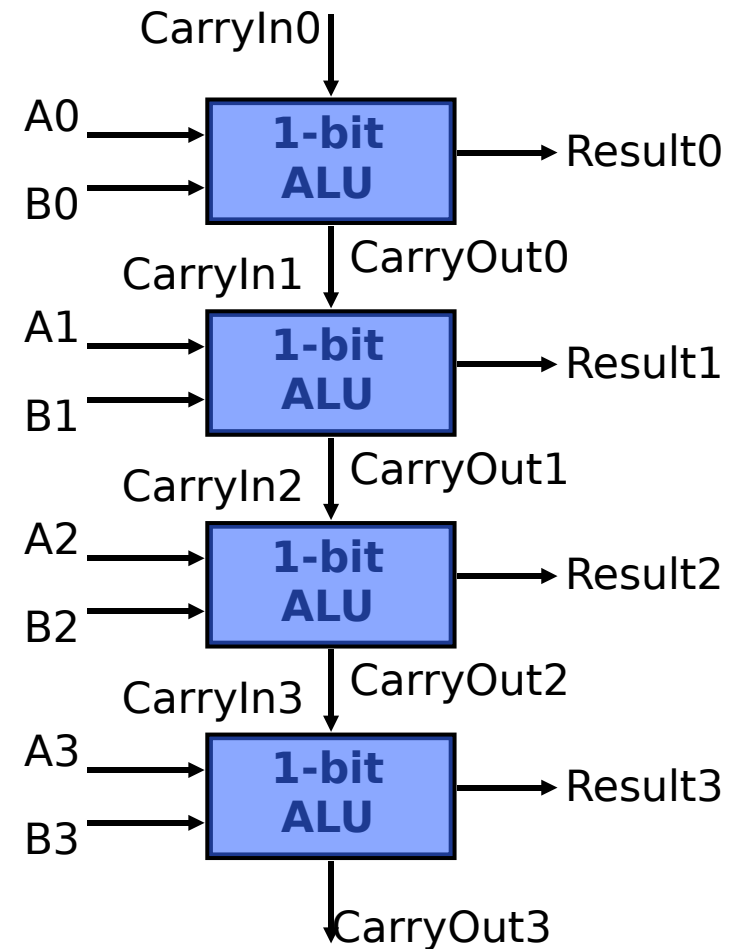
# But What About Performance?

Critical path of one bit-slice is CP

Critical path of n-bit  
rippled-carry adder is  
 $n \cdot \text{CP}$

Design Trick:

- Throw hardware at it



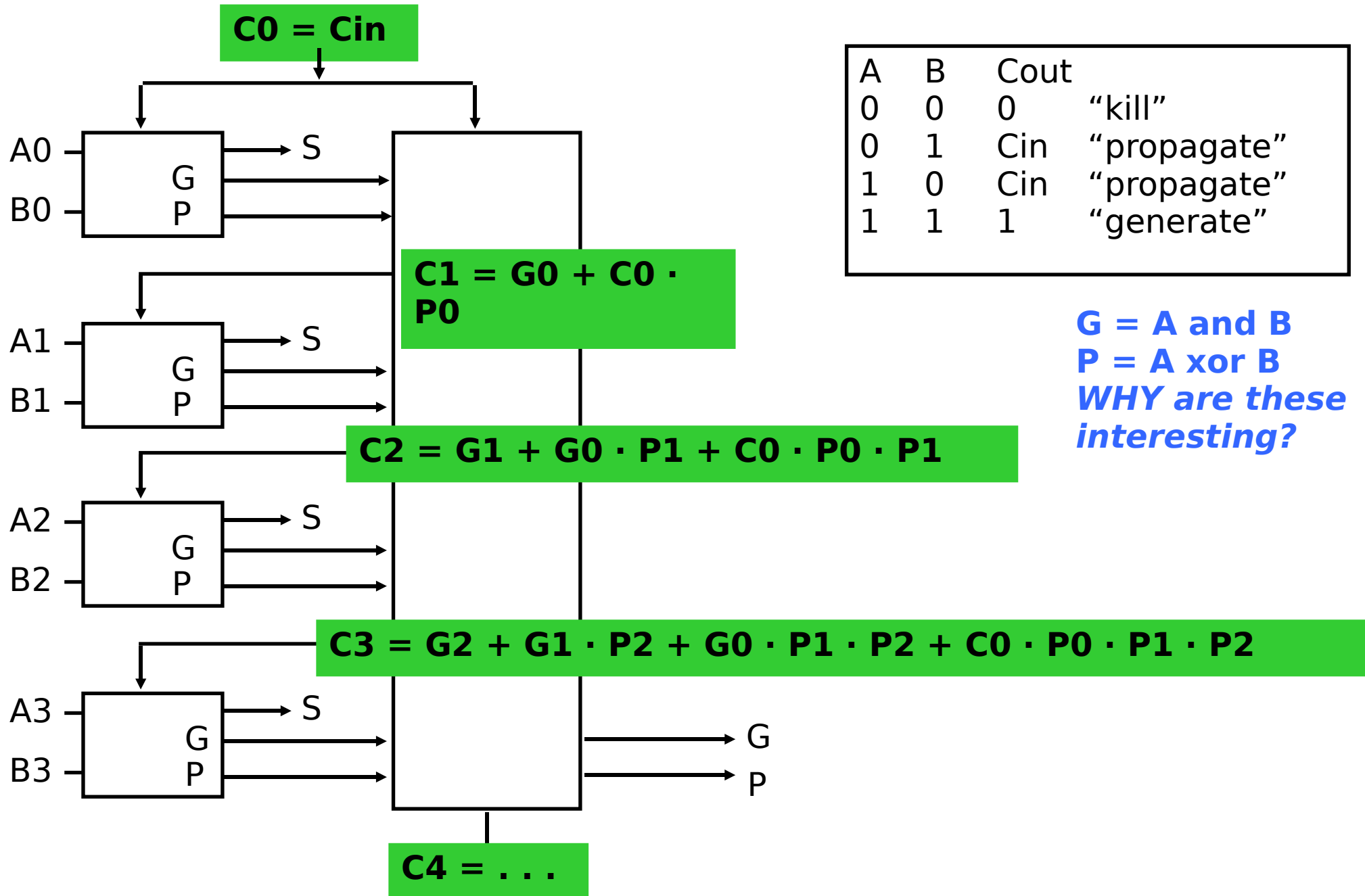
# Truth Table for Adder Bit Slice

3 inputs (A, B, Cin); 2 outputs (Sum, Cout)

A	B	Cin	Sum	Cout
0	0	0	0	0
0	0	1	1	0
0	1	0	1	0=Cin
0	1	1	0	1=Cin
1	0	0	1	0=Cin
1	0	1	0	1=Cin
1	1	0	0	1
1	1	1	1	1



# Carry Look Ahead (Design trick: peek)



# CLA vs. Ripple

