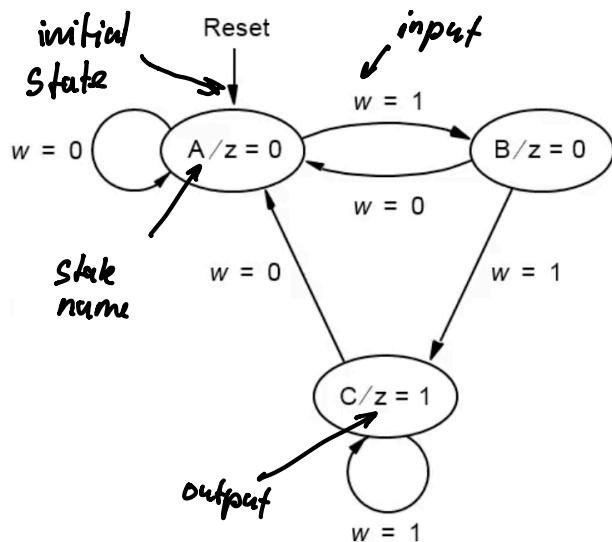
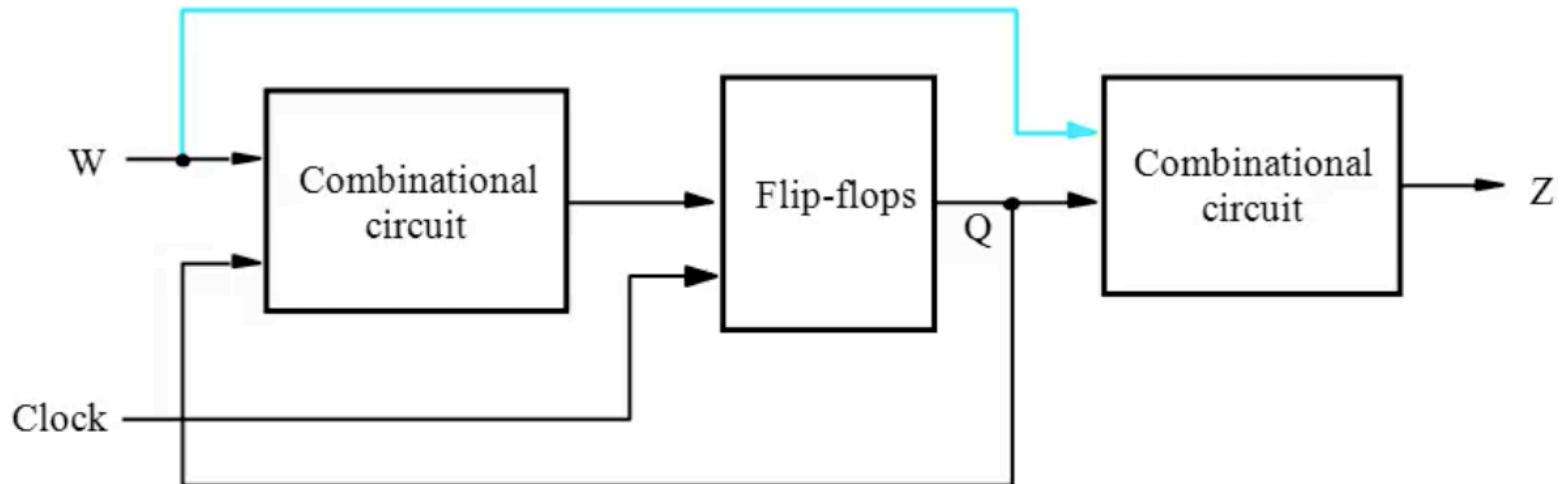


Finite state machine



1. Obtain the user specification of the circuit.
2. Create a state diagram.
3. Create a state table from the state diagram.
4. Perform state assignment.
5. Choose the type of flip-flops.
6. Design the next-state and output logic expressions.
7. Implement the circuit.

The change in state depends on:

- Current state (Q)
- Input (w)

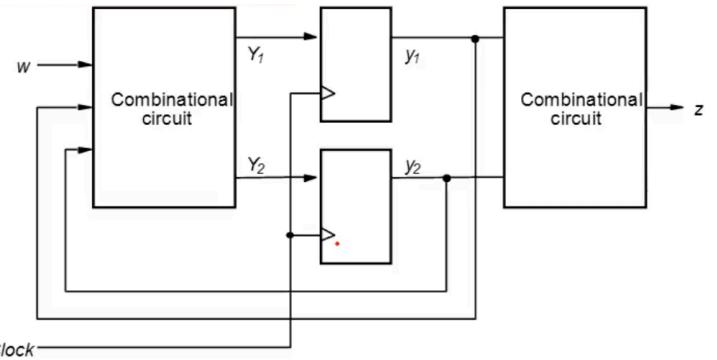
Output (Z) depends on:

- Q (in Moore FSM)
- Q and w (in Mealy FSM)

Present state	Next state		Output z
	$w = 0$	$w = 1$	
A	A	B	0
B	A	C	0
C	A	C	1

2^n states can be represented with n Flip-Flops

Present state	Next state		Output z
	$w = 0$	$w = 1$	
	$y_2 y_1$	$y_2 y_1$	
00	00	01	0
01	00	10	0
10	00	10	1
11	dd	dd	d

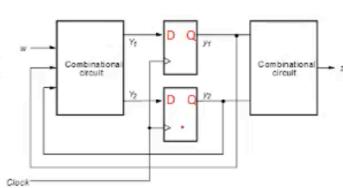


Logic Expressions (Section 8.1.4)

$$\begin{array}{ccccc} & y_2 y_1 & & & \\ w & \swarrow & \searrow & & \\ & 00 & 01 & 11 & 10 \\ \begin{array}{c} 0 \\ 1 \end{array} & \begin{array}{cccc} 0 & 0 & d & 0 \end{array} & \begin{array}{cccc} 0 & 0 & 0 & 0 \end{array} & \begin{array}{cccc} 0 & 1 & 0 & 0 \end{array} & \begin{array}{cccc} 0 & 0 & 0 & 0 \end{array} \\ & \text{Y}_1 = w y_1 \bar{y}_2 & & & \end{array}$$

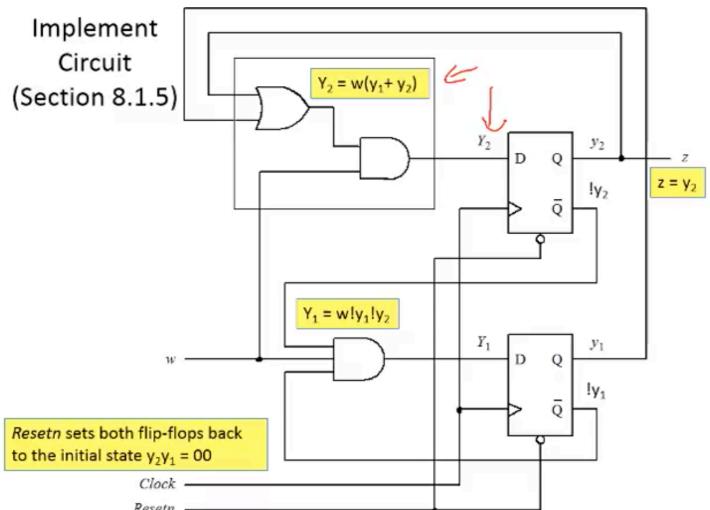
Present state	Next state		Output z
	$w = 0$	$w = 1$	
	$y_2 y_1$	$y_2 y_1$	
A	00	00	0
B	01	00	0
C	10	00	1
	11	dd	d

$$\begin{array}{ccccc} & y_2 y_1 & & & \\ w & \swarrow & \searrow & & \\ & 00 & 01 & 11 & 10 \\ \begin{array}{c} 0 \\ 1 \end{array} & \begin{array}{cccc} 0 & 0 & d & 0 \end{array} & \begin{array}{cccc} 0 & 0 & 0 & 0 \end{array} & \begin{array}{cccc} 0 & 1 & 0 & 0 \end{array} & \begin{array}{cccc} 0 & 0 & 0 & 0 \end{array} \\ & \text{Y}_2 = w y_1 + w y_2 = w(y_1 + y_2) & & & \end{array}$$

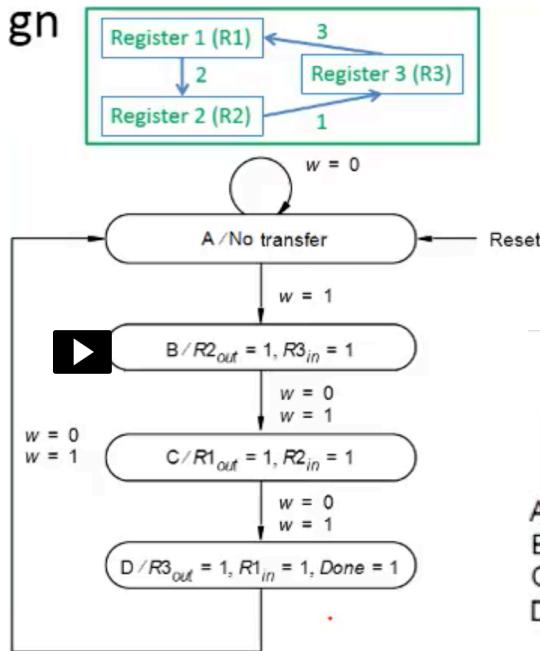


$$\begin{array}{ccccc} & y_1 & & & \\ y_2 & \swarrow & \searrow & & \\ & 0 & 1 & & \\ \begin{array}{c} 0 \\ 1 \end{array} & \begin{array}{cc} 0 & 0 \\ 1 & d \end{array} & & & \\ & z = y_2 & & & \end{array}$$

Implement Circuit (Section 8.1.5)

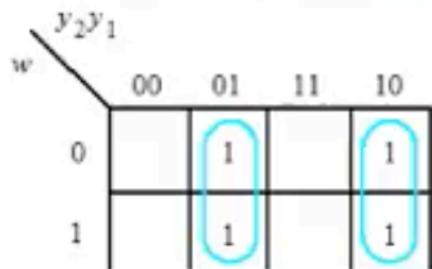
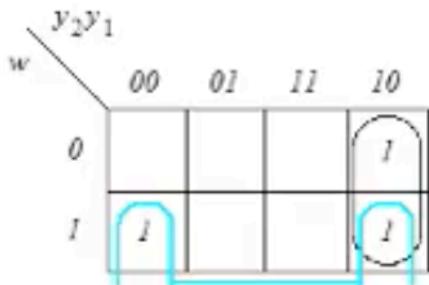


Swap Registers!



Present state	Next state		Outputs						
	w = 0	w = 1	R1 _{out}	R1 _{in}	R2 _{out}	R2 _{in}	R3 _{out}	R3 _{in}	Done
A	A	B	0	0	0	0	0	0	0
B	C	C	0	0	1	0	0	1	0
C	D	D	1	0	0	1	0	0	0
D	A	A	0	1	0	0	1	0	1

Present state	Next state		Outputs								
	w = 0	w = 1	Y ₂ Y ₁	Y ₂ Y ₁ '	R1 _{out}	R1 _{in}	R2 _{out}	R2 _{in}	R3 _{out}	R3 _{in}	Done
A	00	01	00	01	0	0	0	0	0	0	0
B	01	10	10	10	0	0	1	0	0	1	0
C	10	11	11	11	1	0	0	1	0	0	0
D	11	00	00	00	0	1	0	0	1	0	1



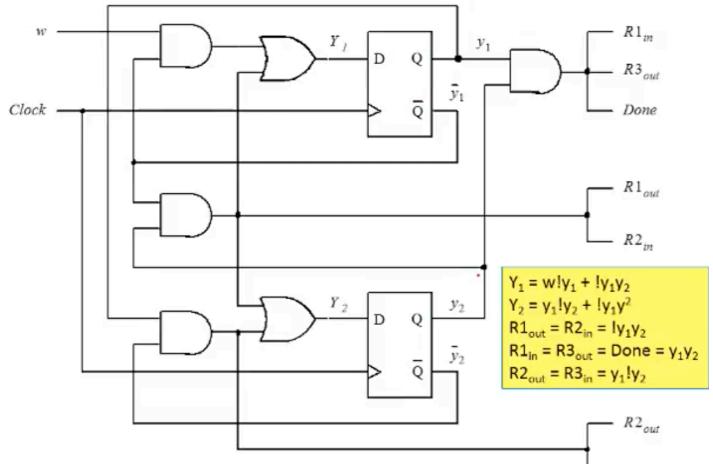
$$Y_1 = w'y_1 + \bar{y}_1y_2$$

$$Y_2 = y_1\bar{y}_2 + \bar{y}_1y_2$$

$$R1_{out} = R2_{in} = \bar{y}_1y_2$$

$$R1_{in} = R3_{out} = \text{Done} = y_1y_2$$

$$R2_{out} = R3_{in} = y_1\bar{y}_2$$



Module 75

David O. Johnson EECS 140/141

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Hot encoding : (faster, but more expensive)

Present state	Next state		Outputs						
	$w = 0 \quad w = 1$								
	y_2y_1	Y_2Y_1	$R1_{out}$	$R1_{in}$	$R2_{out}$	$R2_{in}$	$R3_{out}$	$R3_{in}$	Done
A	00	00	0	0	0	0	0	0	0
B	01	10	0	0	1	0	0	1	0
C	10	11	1	0	0	1	0	0	0
D	11	00	0	1	0	0	1	0	1

$$Y_1 = w!y_1 + !y_1y_2$$

$$Y_2 = y_1!y_2 + !y_1y^2$$

$$R1_{out} = R2_{in} = !y_1y_2$$

$$R1_{in} = R3_{out} = Done = y_1y_2$$

$$R2_{out} = R3_{in} = y_1!y_2$$

Present state	Nextstate		Outputs						
	$w = 0 \quad w = 1$								
	$y_4y_3y_2y_1$	$Y_4Y_3Y_2Y_1$	$R1_{out}$	$R1_{in}$	$R2_{out}$	$R2_{in}$	$R3_{out}$	$R3_{in}$	Done
A	0001	0001	0010	0	0	0	0	0	0
B	0010	0100	0100	0	0	1	0	0	1
C	0100	1000	1000	1	0	0	1	0	0
D	1000	0001	0001	0	1	0	0	1	0

$$Y_1 = !wy_1 + y_4$$

$$Y_2 = wy_1$$

$$Y_3 = y_2$$

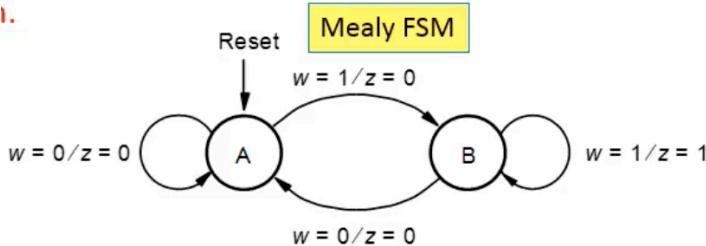
$$Y_4 = y_3$$

$$R1_{out} = R2_{in} = y_3$$

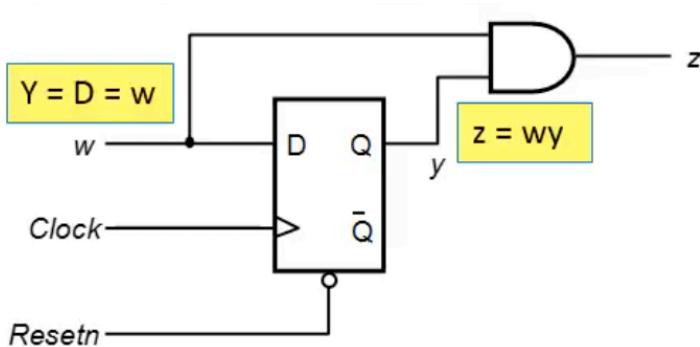
$$R1_{in} = R3_{out} = Done = y_4$$

$$R2_{out} = R3_{in} = y_2$$

1.

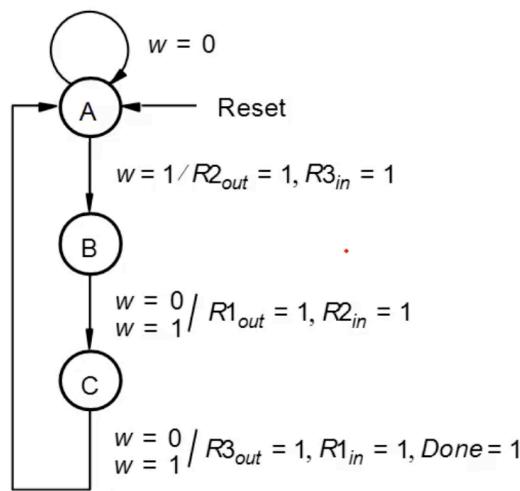
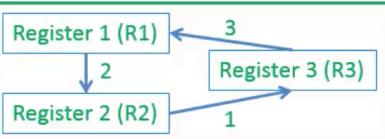


Present state	Next state		Output z	
	$w = 0$	$w = 1$	$w = 0$	$w = 1$
A	A	B	0	0
B	A	B	0	1



Present state	Next state		Output	
	$w = 0$	$w = 1$	$w = 0$	$w = 1$
y	Y	Y	z	z
A	0	1	0	0
B	1	0	1	0

on 8.3)



Next State	Outputs (w=0)							
	w=0	R1_out	R1_in	R2_out	R2_in	R3_out	R3_in	Done
A A	0	0	0	0	0	0	0	0
B C	1	0	0	1	0	0	0	0
C A	0	1	0	0	1	0	1	1

Next State	Outputs (w=1)							
	w=1	R1_out	R1_in	R2_out	R2_in	R3_out	R3_in	Done
A B	0	0	1	0	0	1	0	0
B C	1	0	0	1	0	0	0	0
C A	0	1	0	0	1	0	0	1

Pres State	Next State	Outputs						
		w=0						
A 00	00	0	0	0	0	0	0	0
B 01	10	1	0	0	1	0	0	0
C 10	00	0	1	0	0	1	0	1
	11	dd	d	d	d	d	d	d

Pres State	Next State	Outputs						
		w=1						
A 00	01	0	0	1	0	0	1	0
B 01	10	1	0	0	1	0	0	0
C 10	00	0	1	0	0	1	0	1
	11	dd	d	d	d	d	d	d

Y ₂ Y ₁	
00	01
00	11
00	10
00	00
10	00
10	01
10	11
10	10

Y ₁ Y ₂	
00	01
00	11
00	10
00	00
10	01
10	11
10	10
10	00

Y ₁ Y ₂	
00	01
00	11
00	10
00	00
10	01
10	11
10	10
10	00

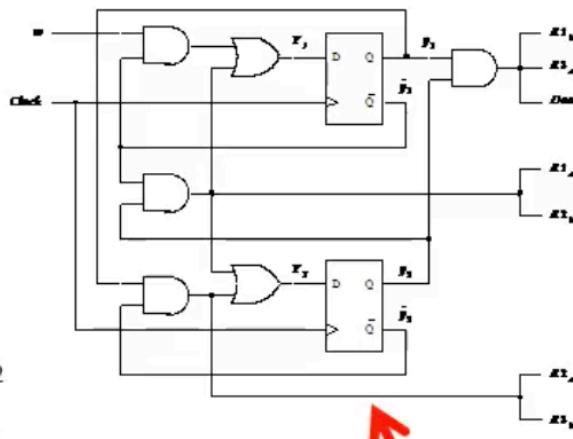
$$Y_1 = w!y_1!y_2$$

$$R2_{out} = R3_{in} = w!y_1!y_2$$

$$Y_2 = y_1$$

$$R1_{out} = R2_{in} = y_1$$

$$R1_{in} = R3_{out} = \text{Done} = y_2$$



	Mealy	Moore
Y ₁	1 gate + 3 inputs = 4	
Y ₂	0 gates + 0 inputs = 0	
R2 _{out} = R3 _{in}	Share with Y ₁ = 0	6 gates + 12 inputs
R1 _{out} = R2 _{in}	Share with Y ₂ = 0	
R1 _{in} = R3 _{out} = Done	0 gates + 0 inputs = 0	
Total Cost	4	18