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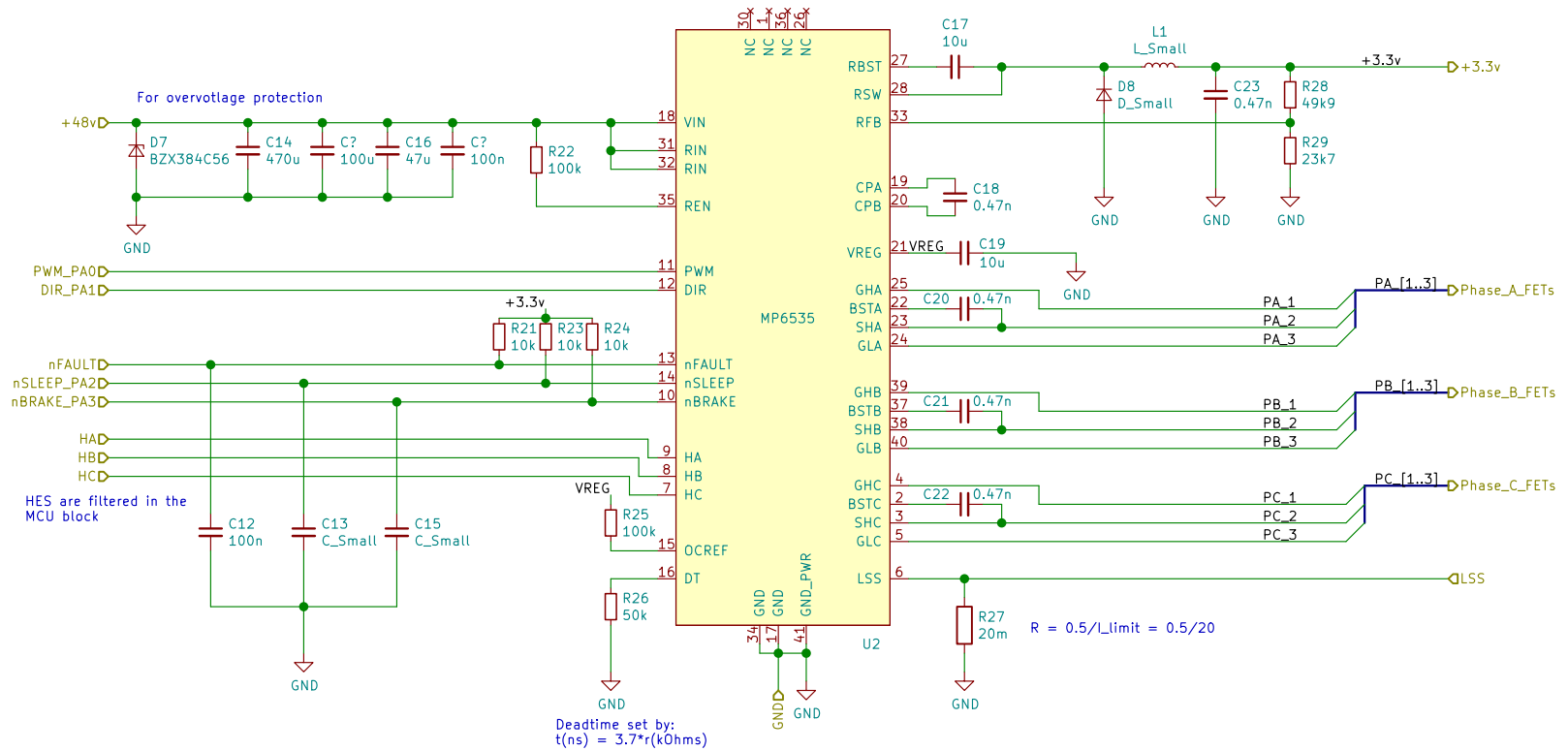
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Rev:
Id: 2/7





Sheet: /BLDC Driver/
 File: BLDC Driver.sch

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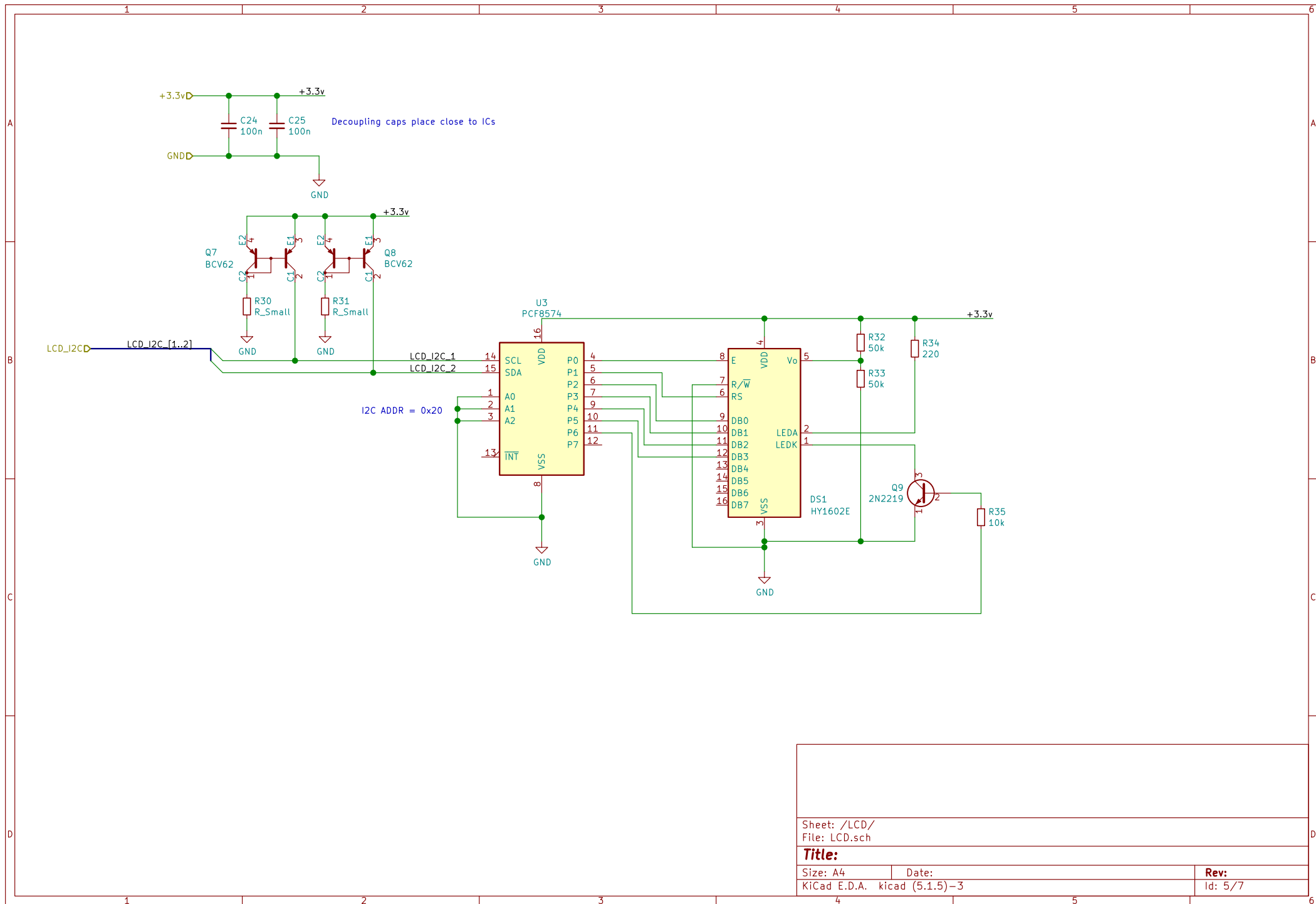
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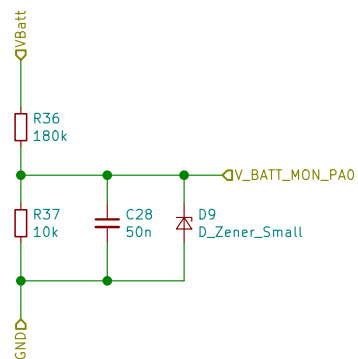
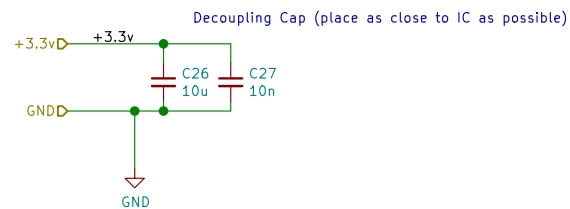
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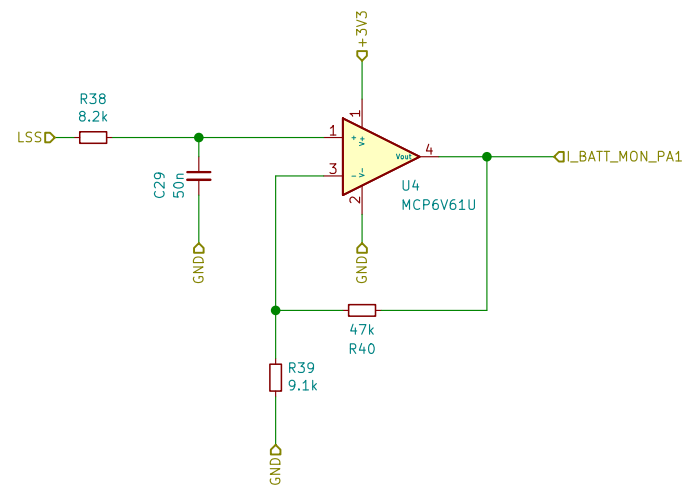
Rev:

Id: 4/7





$f_c = 330\text{Hz}$ (about 2 decades below the switching frequency (20KHz))
 $A_v = 18.3$, hence worst case $V_{in} = 55$, then $V_{out} = 3\text{V}$
 Equivalent resistance is 9.5K, so no need for buffer amp



$f_c = 330\text{Hz}$ (about 2 decades below the switching frequency (20KHz))
 $A_v = 6.1$, hence worst case $V_{in} = 0.5R \cdot 25A \cdot 6.1 \text{ V/V} = 3.1\text{V}$

Sheet: /BMS/
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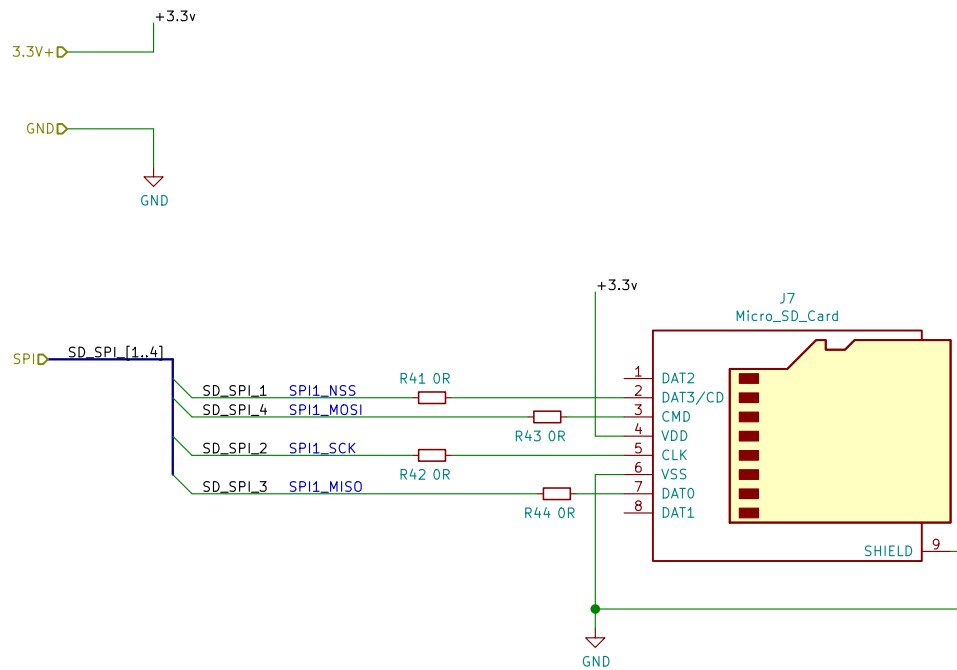
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Rev:

Id: 6/7



Sheet: /SD CARD/
File: SD CARD.sch

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Id: 7/7