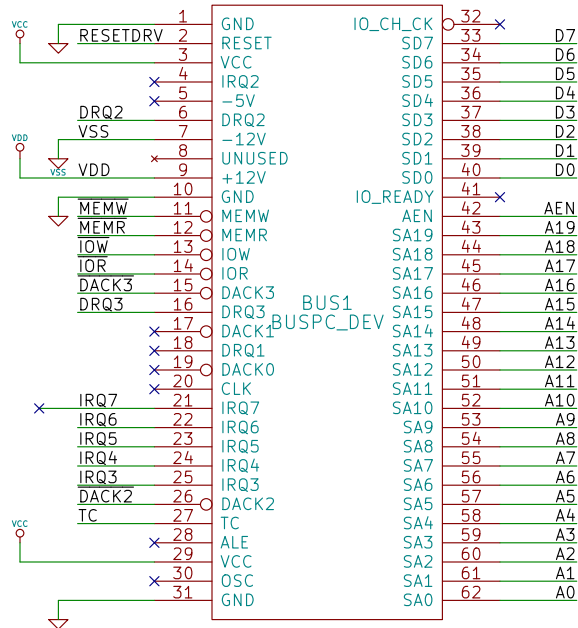
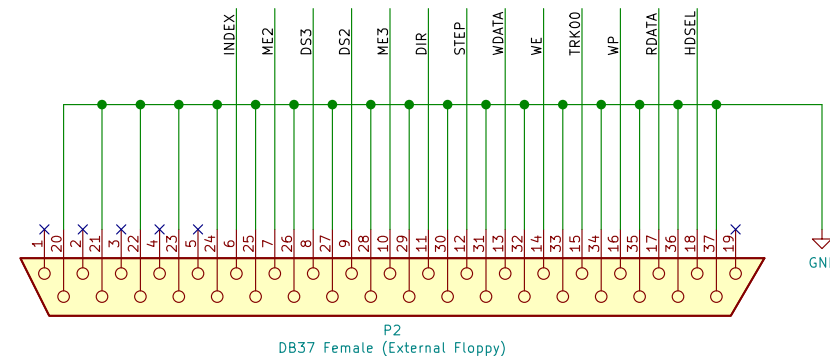
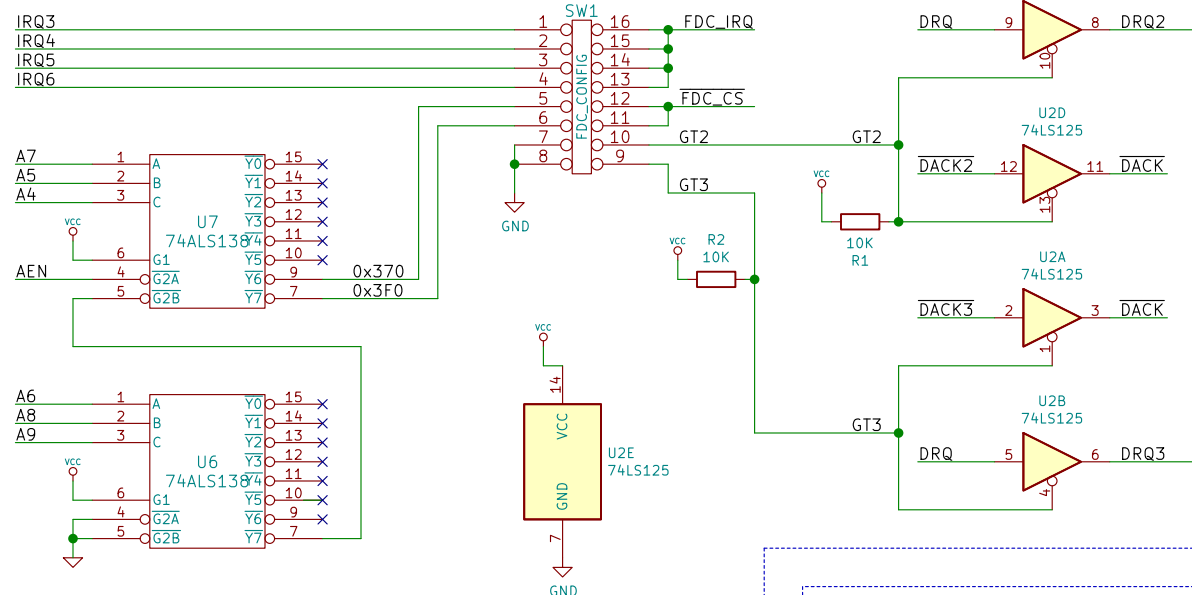


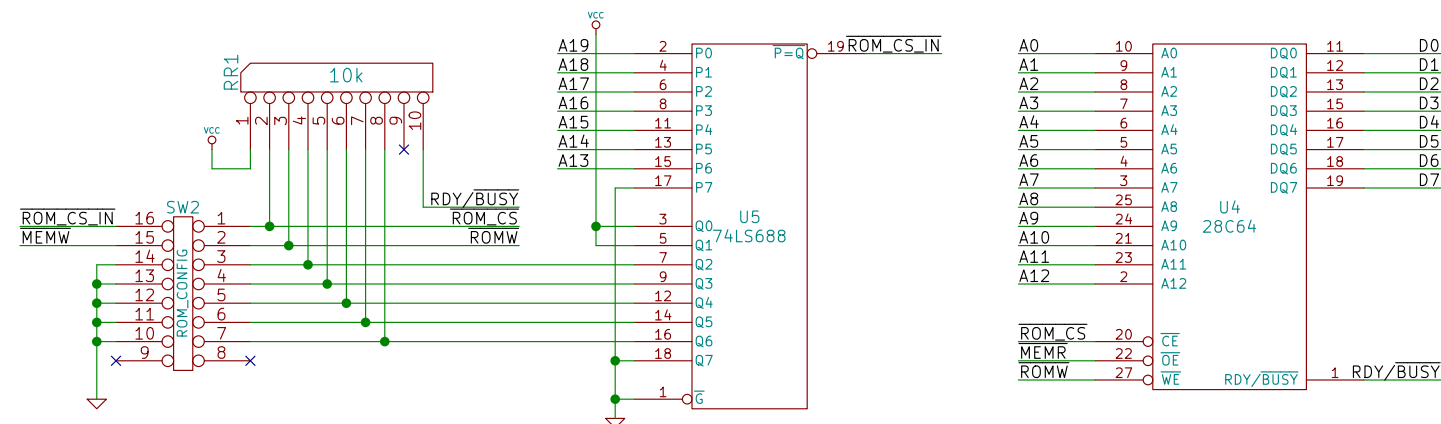
ISA Bus



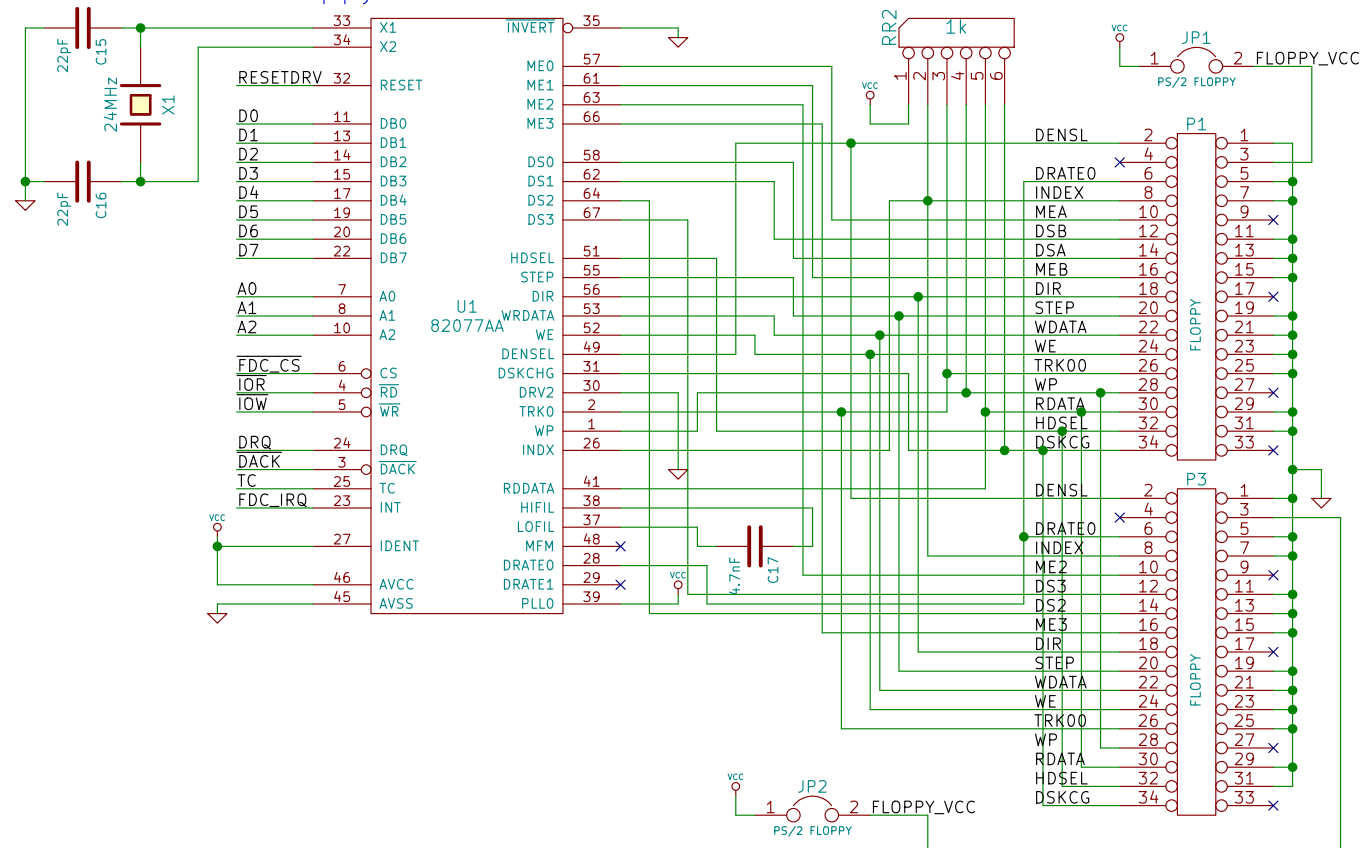
I/O Port/IRQ/DMA Chip Select Logic



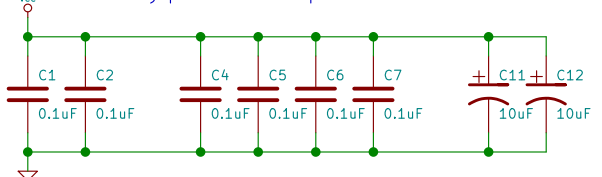
BIOS Extension ROM



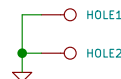
Floppy Disk Controller



Bypass Capacitors



Mount Holes



Settings

SW1.1 - ON: Use IRQ3 for UART (COM2, COM4)
 SW1.2 - ON: Use IRQ4 for UART (COM1, COM3)
 SW1.3 - ON: Use IRQ5 for UART
 SW1.4 - ON: Use IRQ7 for UART
 SW1.5 - ON: Set UART address to 0x2E8 (COM4)
 SW1.6 - ON: Set UART address to 0x3E8 (COM3)
 SW1.7 - ON: Set UART address to 0x2F8 (COM2)
 SW1.8 - ON: Set UART address to 0x3F8 (COM1)

SW2.1 - ON: Enable ROM
 SW2.2 - ON: Enable EEPROM write
 SW2.3 - ON: ROM A17 = 0; OFF: ROM A17 = 1
 SW2.4 - ON: ROM A16 = 0; OFF: ROM A16 = 1
 SW2.5 - ON: ROM A15 = 0; OFF: ROM A15 = 1
 SW2.6 - ON: ROM A14 = 0; OFF: ROM A14 = 1
 SW2.7 - ON: ROM A13 = 0; OFF: ROM A13 = 1
 SW2.8 - NOT USED