

BlueMod+S42 Pin Mapping

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APPLICABILITY TABLE

PRODUCTS

■ BLUEMOD+S42



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1. INTRODUCTION

1.1. Scope

This documentation describes the mapping of BlueMod+S42 pins to the nRF52832 pins as required by software developers.

1.2. Audience

This document is intended for Telit customers, especially system integrators, about to implement Bluetooth modules in their application.

1.3. Contact Information, Support

For general contact, technical support services, technical questions and report documentation errors contact Telit Technical Support at:

TS-SRD@telit.com

Alternatively, use:

http://www.telit.com/support

For detailed information about where you can buy the Telit modules or for recommendations on accessories and components visit:

http://www.telit.com

Our aim is to make this guide as helpful as possible. Keep us informed of your comments and suggestions for improvements.

Telit appreciates feedback from the users of our information.

1.4. Text Conventions



Danger – This information MUST be followed or catastrophic equipment failure or bodily injury may occur.



Caution or Warning – Alerts the user to important points about integrating the module, if these points are not followed, the module and end user equipment may fail or malfunction.



Tip or Information – Provides advice and suggestions that may be useful when integrating the module.

All dates are in ISO 8601 format, i.e. YYYY-MM-DD.



1.5. Related Documents

• BlueMod+S42 Hardware User Guide, 1VV0301303

2. PIN MAPPING

BlueMod+S42			nRF52832	
MODULE PIN	SIGNAL	WLCSP BALL	NAME	INTERNAL CONNECTED
A 1	GPIO[10]	F2	P0.22	flex
A2	GPIO[9]	H2	P0.18 TRCDATA[0]	flex
А3	NFCANT1	G6	P0.09 NFC	static
A4	NFCANT2	G5	P0.10 NFC	static
A5	XL-OUT Analog In	C7	P0.01 XL2	static
А6	XL-IN Analog In	D7	P0.00 XL1	static
B2	GPIO[1] I2C_SDA Analog In	B5	P0.31 AIN7	flex
В3	GPIO[11] Analog In	G2	P0.20	flex
В4	IUR-OUT	E6	P0.04 AIN2	flex
C2	GPIO[13]	H4	P0.15 TRCDATA[2]	flex
C3	GPIO[7] Analog In	А3	P0.28 AIN4	flex
C4	GPIO[6] Analog In	D6	P0.03 AIN1	flex



BlueMod+S42			nRF52832	
MODULE PIN	SIGNAL	WLCSP BALL	NAME	INTERNAL CONNECTED
D1	GPIO[2] SPI_MOSI	H5	P0.14 TRCDATA[3]	flex
D2	UART-RXD	G4 F4	P0.13 P0.11	flex
D3	GPIO[0] I2C_SCL Analog In/Ref	B4	P0.27	flex
D4	GPIO[4]	A5	P0.30 AIN6	flex
D5	IUR-IN#	A4	P0.29 AIN5	flex
D7	UART-RTS#	F6	P0.07	flex
E1	BOOT0 Analog In	В3	P0.25	flex
E2	GPIO[8] SPI_SCK Analog In	F7	P0.06	flex
E4	GPIO[3]	C6	P0.02 AIN0	flex
E5	GPIO[12]	НЗ	P0.16 TRCDATA[1]	flex
F1	TESTMODE# Analog In	G7	P0.08	flex
F2	GPIO[5] SPI_MISO Analog Ref	E7	P0.05 AIN3	flex
F3	UART-CTS#	H6	P0.12	flex
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BlueMod+S42			nRF52832	
MODULE PIN	SIGNAL	WLCSP BALL	NAME	INTERNAL CONNECTED
F4	UART-TXD	G3	P0.17	flex
F5	GPIO[14]	D3	P0.26	flex



3. GLOSSARY AND ACRONYMS

Description

TTSC	Telit Technical Support Centre
USB	Universal Serial Bus
NFC	Near Field Communication
DTE	Data Terminal Equipment
UART	Universal Asynchronous Receiver Transmitter
SPI	Serial Peripheral Interface
ADC	Analog – Digital Converter
DAC	Digital – Analog Converter
I/O	Input Output
GPIO	General Purpose Input Output
CLK	Clock
MOSI	Master Output – Slave Input
MISO	Master Input – Slave Output
CS	Chip Select
RTC	Real Time Clock



4. **DOCUMENT HISTORY**

Revision	Date	Changes
0	2016-08-16	First issue

SUPPORT INQUIRIES

Link to www.telit.com and contact our technical support team for any questions related to technical issues.

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