Mirarchial Carry - Look Ahead Addler

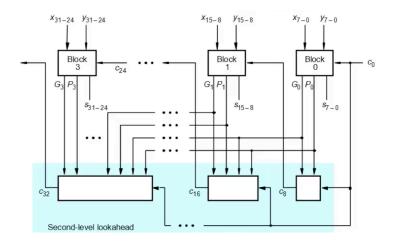
 $G_0 = g_7 + p_7 g_6 + p_7 p_6 g_5 + p_7 p_6 p_5 g_4 + p_7 p_6 p_5 p_4 g_3 + p_7 p_6 p_5 p_4 p_3 g_2 + p_7 p_6 p_5 p_4 p_3 p_2 g_1 + p_7 p_6 p_5 p_4 p_3 p_2 p_1 g_0$

 $P_0 = p_7 p_6 p_5 p_4 p_3 p_2 p_1 p_0$

$$c_{16} = G_1 + P_1c_8 = G_1 + P_1(G_0 + P_0c_0) = G_1 + P_1G_0 + P_1P_0c_0$$

$$c_{24} = G_2 + P_2G_1 + P_2P_1G_0 + P_2P_1P_0c_0$$

$$c_{32} = G_3 + P_3G_2 + P_3P_2G_1 + P_3P_2P_1G_0 + P_3P_2P_1P_0c_0$$



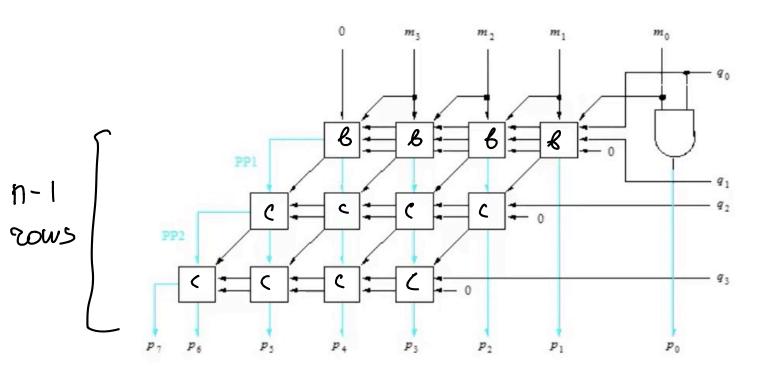
- $P_0 = p_7 \dots p_0$
- 2 for G_x
- $\bullet G_0 = g7 + ... + p_7 p_1 g_0$
- 2 to generate carry-outs
- $(c_8, c_{16}, c_{24}, and c_{32})$
- $\bullet G_0 + P_0 c_0$
- 2 for internal carries in
- Blocks 0-3
- Carry-lookahead delay
- 1 for sum in Blocks 0-3
- Critical-path delay = 8 gate delays

1101 > left shift > 11010 n left shifts multiply number by 2"

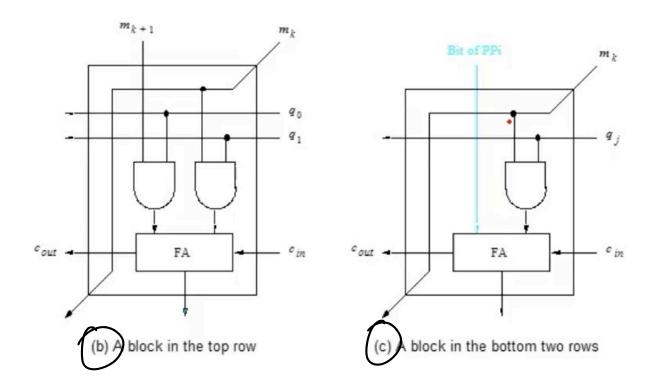
Multiplicand M Multiplier Q	(14) (11)	1110 × 1011
Partial product ()	1110
Partial product	1	+ 1110 10101 + 0000
Partial product2	2	01010
Product P	(154)	10011010

n-1 left shift sovel additions for 2 n-bit numbers

Delay = 4n+1



(a) Structure of the circuit



unigned multiplication Quells: $a \times b$ $a \times -b$ $a \times -b$ $a \times -b$ $a \times b$ $a \times b$

Man Hissa x Madix Exponent

Single - Precision Floating - Point Value = 1.14 x2 Precision: about 7 digits (a.y. 1,234567) Exponent runge: 10 = 38

Pouble . Procision:

52 - 6its me 4/1559 excess-1023 l-bit always l exponent E from 0-2047 0 mens escuct 0 2047 means infty Excess-1023: Expount = 2-1023

Normal range of escopenent: [-1022, 1023]

Value = 1.1Mx2 8-1623

Precision: 16 digits (1,234567890123456) Exponent range: about 10 = 308