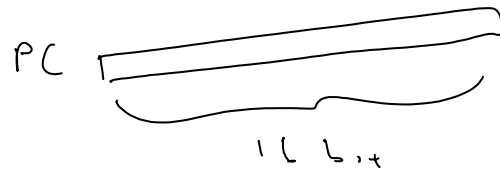
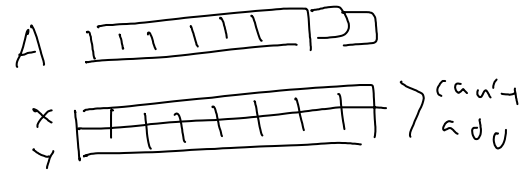


Lecture 11

Monday, September 28, 2015 9:59 AM

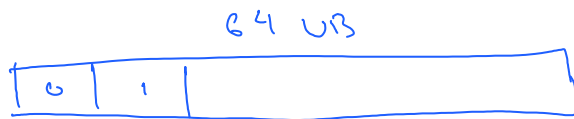
6502

- First real CPU, unlike SCRAM
- Approved for pacemakers
- 1975
- 8-bit like SCRAM
- Only add and subtract
- 16-bit address bus (65536 memory locations)

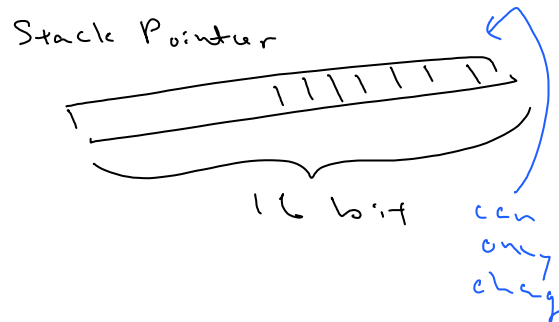


Website Resources

- Obelisk.me.uk/6502
- <http://skilldrick.github.io/easy6502/>



0 FF
100 1F#
0 page
variable length encoding!



1 byte instruction

2 byte instructions, opcode, operand

3 byte instructions, opcode, 2 operands.

LDA STA
LDX STX
LDY STY

transfer
shift instructions

0101 5 0101 5
← →
1010 10 0010 2

roughly multiply and divide by 2.

fills in with 0's

rotate (what ever is shifted out, comes back)

Branches / Flags

individual bits in a P register
processor status.



Zero bit / set and clear

Negative bit /