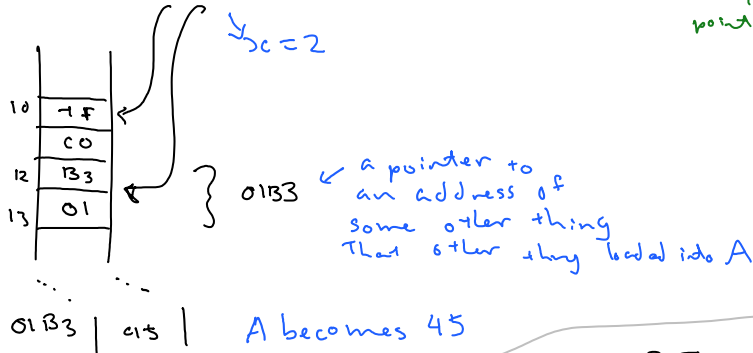


Lecture 13

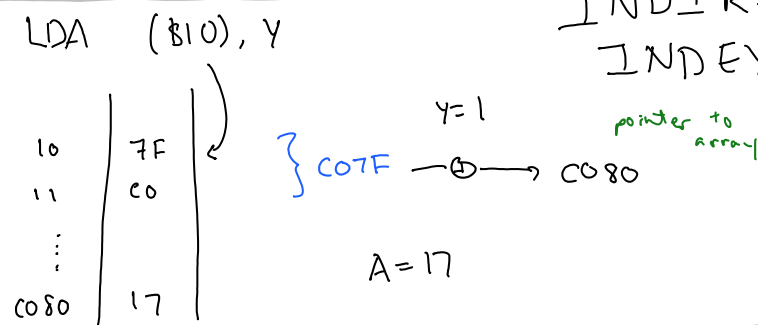
Friday, October 2, 2015 10:01 AM

6502 Addressing Modes

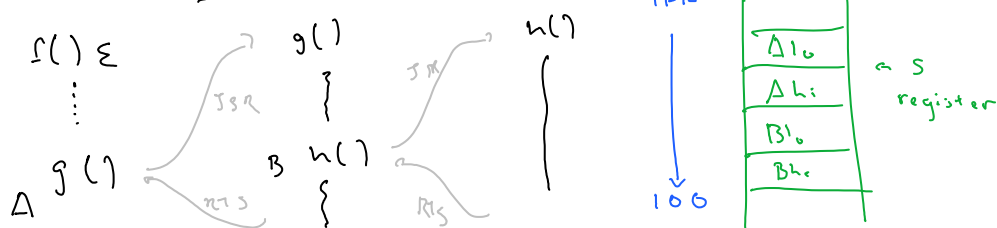
- Zero page only!
- LDA (\$10, X) indexed indirect



INDIRECT INDEXED



JSR / RTS



PHA mess w/ the stack

PLA unmess.

best way to mess w/ subroutines

PHA PLP for the status register
 TXA PLA not always
 PHA TAX
 TYA PLA
 PHA TAX
PLP PLA
 RTS

INTERRUPTS

- async events

FFFE / FFFF

