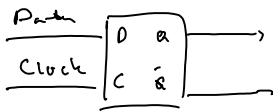


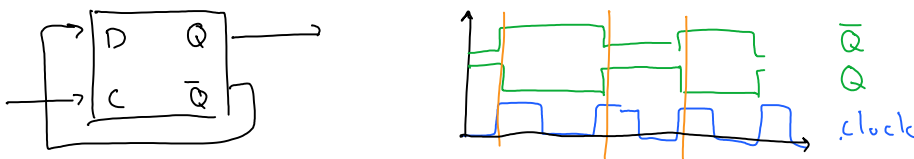
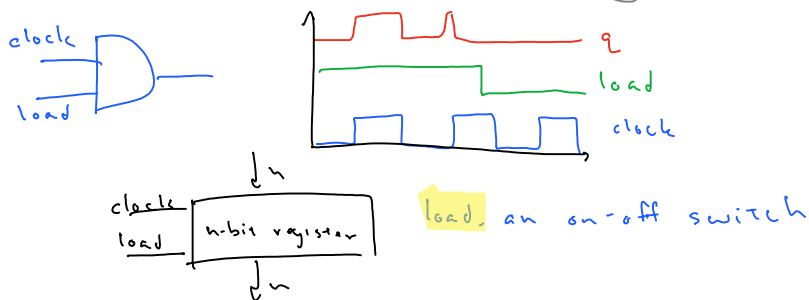
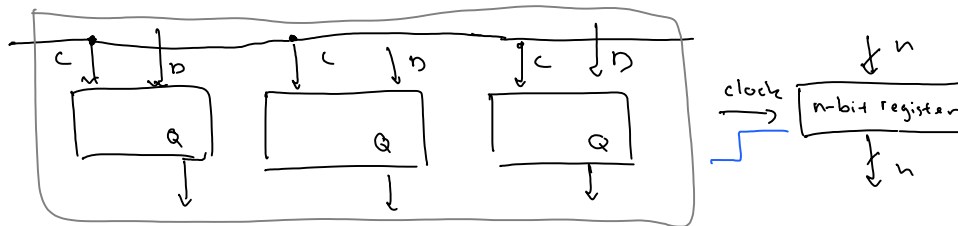
Lecture 7

Wednesday, September 16, 2015 9:58 AM

Edge Trigger D-Type Flip Flop *transparent for a SHORT time*

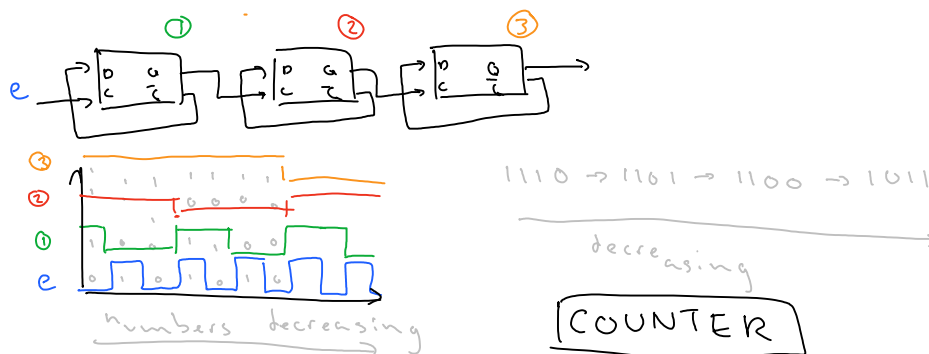


Register - n: Flip Flop to store n-bit quantity



This **HALVES** the frequency

Counter Circuit



From now on, clocks are assumed to be where they __ to

New version of 1-bit memory cell

