

Nada Badawi - 900201364

CSCE 3301-01

30th of April, 2023

RV32I PIPELINED IMPLEMENTATION JOURNAL

April 16, 10:30 AM: Added Pipelined Registers for the single-cycle architecture to support all instructions.

April 18, 3:00 PM: Added Forwarding and Hazard Unit to remove hazards except for the control hazards.

April 21, 5:15 PM: Fixed error in single memory architecture
Tested and validated with the Complete Test Suite with incorrect results yielded

April 24, 3:00 PM: Worked on forwarding and flushing to fix bugs
Removed Data Hazard Detection Unit