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CSCE 3302-01 Project Milestone III

RISCV32 IMPLEMENTATION JOURNAL

April 2023

1 Documentation

- April 16, 10:00 AM: Implemented Pipelined Architecture without Hazard Detection or Exceptions
- April 17, 11:20 AM:
 - Resolved Minor errors
 - Tested and validated the code with Complete Integrated Test Suite
- April 20, 2:15 PM:
 - Designed Memory Layout and Wiring
 - Edited Architecture to support single byte-addressable memory
- April 23, 10:30 AM: Resolved Error in Memory
- April 24, 3:30 PM: Added support code to accommodate Forwarding & Flushing.