Addressing Modes in 8085 up. 7
The way of specifying data to be operated by an instruction is
called addressing mode
Types of addressing mode:
In 8085 up there one fire types of addressing modes !
16) Immediate Addressing a first
To die addressing made the bource operation
data. If data is 8 bit, Inan instruction will be of 2 bytes. If data
is of 16 bit then instruction will be of 3 bytes
Example: MVI 8,45H
LXT H, 3050 n
JMP 2050+1
CALL 3600 H.
(b) Direct Add russing mode:
In 448s mode, the data to be operated is available inside
a memory location and that momory location is directly aprecipied
as an operand.
Funda : 100 2000 H
Example: LDA 2050 H.
IN 35H
ON MARKET STATE OF THE PROPERTY OF THE PROPERT
(c) Register Addressing mode. : m this addressing mode, the data to be repeal operated is
available inside the register(s) and register(c) is (and) operands Therefore
the operation is performed within various megisters of the microprocessive
Evanda ' Many O B
Example: MOV A. B
INR A
(d) Register Indirect Addressing mode:
In this addressing mode, the data to be operated is available and
a memory location and that memory location is indirectly expected by a
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Example	: MOVA,M.	- Herte -the	content of	the memory book	
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	LDAX B -	Here the con	itent of the	memory location	
	, charlings a	pointed by 1	ie B-C Pai	ir IS copied to	
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(e) Impl	ied/Implicit Addressim	g mode:			
	In this addressi	ng mode, the of	perand is hi	dden and the	
data to be	operated is available	in the instructi	on itself.		
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Fyan	ple: CMA		<u> </u>		
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