Hardware Simulator (2.5) - /Users/mayurgupta/Desktop/nand2tetris/projects/01/Mux.hdl

File View Run Help

Slow Fast Animate: Program flow Format: D... View: Scr...

Chip Nam... Mux Time : 0

Input pins

Name	Value
a	1
b	1
sel	1

Output pins

Name	Value
out	1

HDL

```
// This file is part of www.nanc
// and the book "The Elements of
// by Nisan and Schocken, MIT Pr
// File name: projects/01/Mux.hc
/**
 * Multiplexor:
 * if (sel == 0) out = a, else o
 */
CHIP Mux {
    IN a, b, sel;
    OUT out;

    PARTS:
    //// Replace this comment wi
```

Internal pins

Name	Value
notsel	0
andbnotsel	0
selandb	1

```
eval,
output;

set sel 1,
eval,
output;

set a 0,
set b 1,
set sel 0,
eval,
output;

set sel 1,
eval,
output;

set a 1,
set b 0,
set sel 0,
eval,
output;

set sel 1,
eval,
output;

set a 1,
set b 1,
set sel 0,
eval,
output;

set sel 1,
eval,
output;
```

End of script - Comparison ended successfully