

Input pins		Output pins	
Name	Value	Name	Value
a[16]	4660	out[16]	-21846
b[16]	-26506		

```
// File name: projects/02/Add16.tst

load Add16.hdl,
output-file Add16.out,
compare-to Add16.cmp,
output-list a%B1.16.1 b%B1.16.1 out%B1.16.1;

set a %B000000000000000000,
set b %B000000000000000000,
eval,
output;

set a %B000000000000000000,
set b %B111111111111111111,
eval,
output;

set a %B111111111111111111,
set b %B111111111111111111,
eval,
output;

set a %B101010101010101010,
set b %B0101010101010101,
eval,
output;

set a %B0011110011000011,
set b %B0000111111110000,
eval,
output;

set a %B0001001000110100,
set b %B1001100001110110,
eval,
output;
```