

Time : 107

HDL	Internal pins						
<pre>// by Nisan and Schocken, MIT Pr // File name: projects/03/a/Bit. /**  * 1-bit register:  * If load is asserted, the regi  * Otherwise, the register maint  * if (load(t)) out(t+1) = in(t)  */ CHIP Bit {     IN in, load;     OUT out;      PARTS:     //// Replace this comment wi</pre>	<table> <thead> <tr> <th>Name</th><th>Value</th></tr> </thead> <tbody> <tr> <td>dout</td><td>0</td></tr> <tr> <td>out1</td><td>0</td></tr> </tbody> </table>	Name	Value	dout	0	out1	0
Name	Value						
dout	0						
out1	0						

```
output;
```