Hardware Simulator (2.5) - /Users/mayurgupta/Desktop/nand2tetris/projects/03/a/RAM64.hdl

File   View   Run   Help

Animate: Format: View:
Program flow   D...   Scr...

Slow   Fast

Chip Nam... RAM64 (Clocked)   Time : 81

Input pins

Name	Value
in[16]	21845
load	0
address[6]	61

Output pins

Name	Value
out[16]	21845

HDL

```
    PARTS:
    //// Replace this comment wi
    DMux8Way(in=load,sel=address
    RAM8(in=in,load=load0,addres
    RAM8(in=in,load=load1,addres
    RAM8(in=in,load=load2,addres
    RAM8(in=in,load=load3,addres
    RAM8(in=in,load=load4,addres
    RAM8(in=in,load=load5,addres
    RAM8(in=in,load=load6,addres
    RAM8(in=in,load=load7,addres
    Mux8Way16(a=out0,b=out1,c=ou
}
```

Internal pins

Name	Value
load0	0
load1	0
load2	0
load3	0
load4	0
load5	0
load6	0
load7	0
out0[16]	21845
out1[16]	21845
out2[16]	21845
out3[16]	21845
out4[16]	21845
out5[16]	21845

```
set load 1,
set address %B111101,
set in %B0101010101010101,
tick,
output,
tock,
output;

set load 0,
set address %B000101,
tick,
output;
tock,
output;
set address %B001101,
eval,
output;
set address %B010101,
eval,
output;
set address %B011101,
eval,
output;
set address %B100101,
eval,
output;
set address %B101101,
eval,
output;
set address %B110101,
eval,
output;
set address %B111101,
eval,
output;
```

End of script – Comparison ended successfully