

Hardware Simulator (2.5) - /Users/mayurgupta/Desktop/nand2tetris/projects/03/b/RAM512.hdl

File View Run Help Animate: Format: View:

Slow Fast Program flow © D... © Scr... ©

	Slow Fas	st Program now Scr Scr
Chip Nam RAM512 (Clocked) Time: 81		set load 1, set address %B111101010,
Input pins	Output pins	set in %B01010101010101, tick,
Name Value in [16] 21845 load 0 address [9] 490		<pre>output, tock, output; set load 0, set address %B000101010, tick, output; tock, output; set address %B001101010, eval, output; set address %B010101010, eval,</pre>
HDL	Internal pins	output; set address %B011101010,
PARTS: //// Replace this comment wi DMux8Way(in=load, sel=address RAM64(in=in, load=load0, addre RAM64(in=in, load=load2, addre RAM64(in=in, load=load3, addre RAM64(in=in, load=load4, addre RAM64(in=in, load=load5, addre RAM64(in=in, load=load5, addre RAM64(in=in, load=load6, addre RAM64(in=in, load=load6, addre RAM64(in=in, load=load7, addre Mux8Way16(a=out0, b=out1, c=out)	Name Value load0 0 load1 0 load2 0 load3 0 load4 0 load5 0 load6 0 load7 0 out0[16] 21845 out1[16] 21845 out3[16] 21845 out4[16] 21845 out5[16] 21845	eval, output; set address %B100101010, eval, output; set address %B101101010, eval, output; set address %B110101010, eval, output; set address %B111101010, eval, output;

End of script - Comparison ended successfully























































