

Hardware Simulator (2.5) - /Users/mayurgupta/Desktop/nand2tetris/projects/03/b/RAM4K.hdl

File View Run Help



Chip Nam RAM4K (Clocked) Time: 81			set load 1,
Input pins	Output pins		set address %B111101010101, set in %B01010101010101,
Name Value in [16] 21845 load 0 address [12] 3925	out [16]	Value 21845	tick, output, tock, output; set load 0, set address %B000101010101, tick, output; tock, output; set address %B001101010101, eval, output; set address %B010101010101,
HDL	Internal pins		eval, output;
	Name	Value	set address %B011101010101, eval,
PARTS:	load0	0	output;
//// Replace this comment wi	load1	0	set_address %B100101010101,
DMux8Way(in=load, sel=address	load2	0	eval,
RAM512(in=in,load=load0,addr RAM512(in=in,load=load1,addr	load3 load4	0 0	output;
RAM512(in=in, load=load2, addr	load5	0	set address %B101101010101,
RAM512(in=in, load=load3, addr	load6	ő	eval, output;
RAM512(in=in, load=load4, addr	load7	0	set address %B110101010101,
RAM512(in=in, load=load5, addr	out0[16]	21845	eval,
RAM512(in=in,load=load6,addr	out1[16]	21845	output;
RAM512(in=in,load=load7,addr	out2[16]	21845	set address %B111101010101,
Mux8Way16(a=out0,b=out1,c=ou	out3[16]	21845	eval,
}	out4[16]	21845	output;
	out5[16]	21845	

End of script - Comparison ended successfully























































