

Time : 81

Input pins		Output pins	
Name	Value	Name	Value
in[16]	21845	out[16]	21845
load	0		
address[14]	15701		

HDL	Internal pins	
*/	Name	Value
CHIP RAM16K {	load0	0
IN in[16], load, address[14]	load1	0
OUT out[16];	load2	0
	load3	0
PARTS:	out0[16]	21845
//// Replace this comment w	out1[16]	21845
DMux4Way(in=load, sel=address	out2[16]	21845
RAM4K(in=in, load=load0, addre	out3[16]	21845

```
set load 0,  
set address %B00010101010101,  
tick,  
output;  
tock,  
output;  
set address %B00110101010101,  
eval,  
output;  
set address %B01010101010101,  
eval,  
output;  
set address %B01110101010101,  
eval,  
output;  
set address %B10010101010101,  
eval,  
output;  
set address %B10110101010101,  
eval,  
output;  
set address %B11010101010101,  
eval,  
output;  
set address %B11110101010101,  
eval,  
output;
```

End of script – Comparison ended successfully