Hardware Simulator (2.5) - /Users/mayurgupta/Desktop/nand2tetris/projects/01/Xor.hdl

File View Run Help

Slow Fast

Animate: Program flow Format: D... View: Scr...

Chip Nam... Xor Time : 0

Input pins

Name	Value
a	1
b	1

Output pins

Name	Value
out	0

HDL

```
// and the book "The Elements of
// by Nisan and Schocken, MIT Pr
// File name: projects/01/Xor.hd
/**
 * Exclusive-or gate:
 * out = (not(a) and b) or (a ar
 */
CHIP Xor {
    IN a, b;
    OUT out;

    PARTS:
    //// Replace this comment wi
    Nand(a=b , b=a , out=nandab)
```

Internal pins

Name	Value
nandab	0
nandabnanda	1
nandabnandb	1

```
// This file is part of www.nand2tetris.org
// and the book "The Elements of Computing Systems"
// by Nisan and Schocken, MIT Press.
// File name: projects/01/Xor.tst

load Xor.hdl,
output-file Xor.out,
compare-to Xor.cmp,
output-list a%B3.1.3 b%B3.1.3 out%B3.1.3;

set a 0,
set b 0,
eval,
output;

set a 0,
set b 1,
eval,
output;

set a 1,
set b 0,
eval,
output;

set a 1,
set b 1,
eval,
output;
```

End of script - Comparison ended successfully