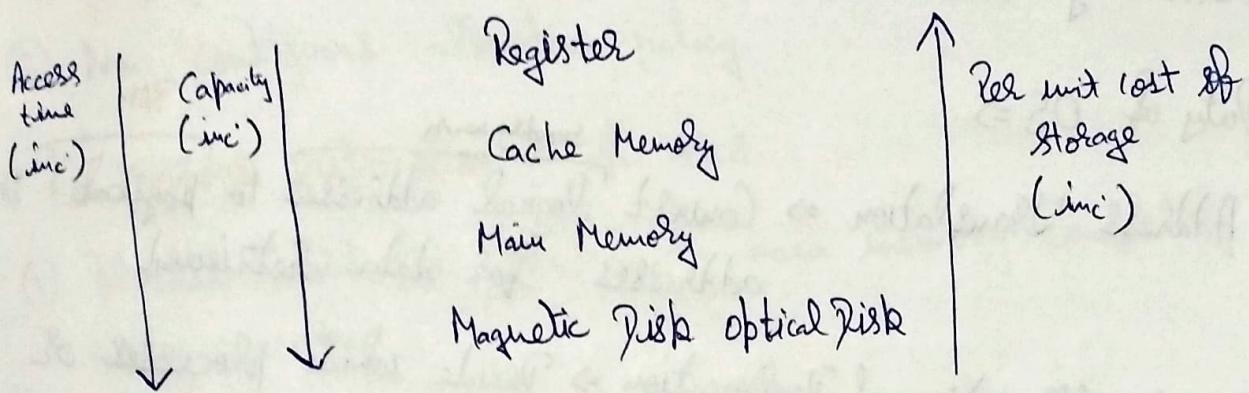
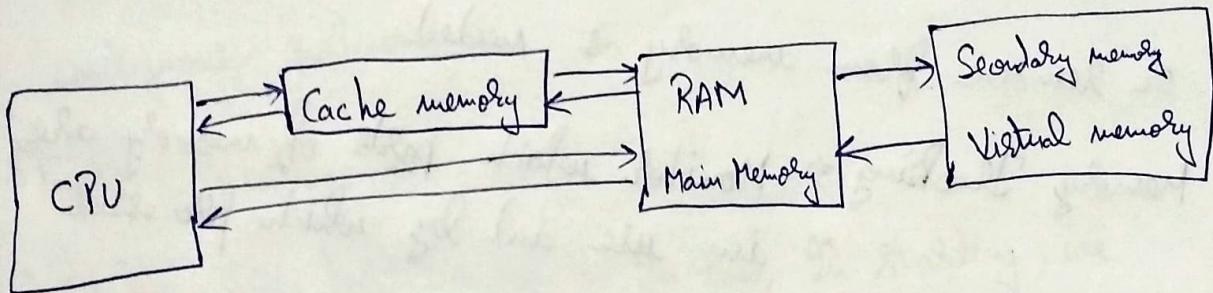


Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

* Memory Hierarchy \Rightarrow



*



* Locality of reference \Rightarrow The references to memory at any given interval of time tend to be confined within a few localized areas in memory. This phenomenon is known as property of locality of reference.

Main Ideas, Questions & Summary:

Library / Website Ref.:-

① Spatial Locality \Rightarrow Use of data elements in nearby locations.

② Temporal Locality \Rightarrow It refers to the reuse of specific data or resources within a relatively small time duration i.e. Most Recently used.

* Duty of OS \Rightarrow

-
- ① Address Translation \Rightarrow Convert logical addresses to physical addresses for data retrieved.
- ② Memory Allocation & Deallocation \Rightarrow Decide which process or data segment to load or remove from memory as needed.
- ③ Memory Tracking \Rightarrow Monitor which parts of memory are in use and by which processes.
- ④ Memory Protection \Rightarrow Implement safeguards to restrict unauthorized access to memory, ensuring both process isolation & data integrity.

Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

* 2 Approach to store process in main memory =>

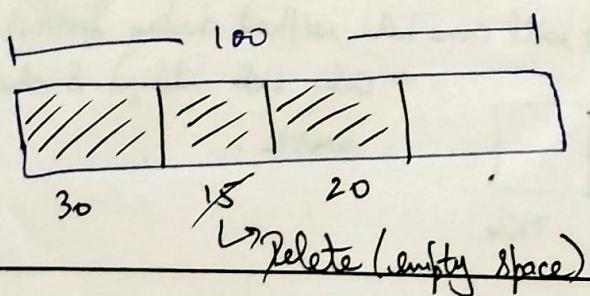
- ① Contiguous allocation policy
- ② Non-contiguous allocation policy

* Contiguous allocation => In this

- ① Process must be loaded to main memory completely for execution.
- ② Process must be stored in main memory in contiguous fashion.

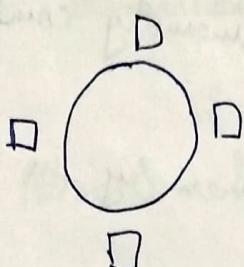
. Space Allocation =>

- ① Variable size partitioning => In this, In starting, we treat the memory as a whole or a single chunk & whenever a process request for some space, exactly same space is allocated if possible & the remaining space can be reused again.



Main Ideas, Questions & Summary:

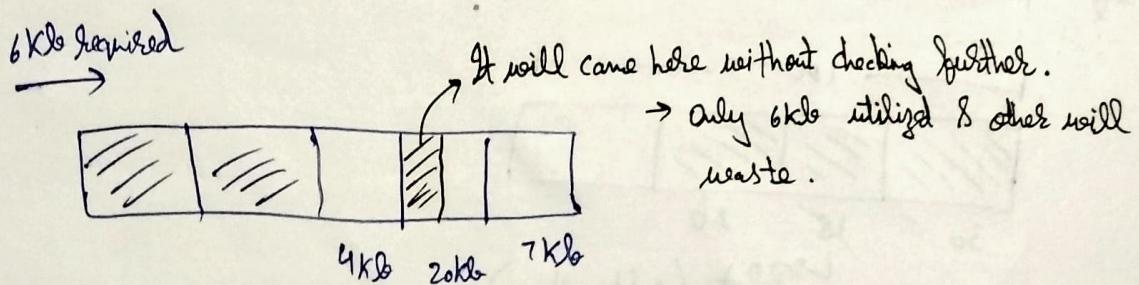
② Fixed size partitioning \Rightarrow Here, we divide memory into fixed size partitions, which may be of diff sizes, but here if a process request for some space, then a partition is allocated entirely if possible, & remaining space will be wasted internally.



\rightarrow Only 3 come to fit
then, slot is wasted

* First fit policy \Rightarrow It starts searching the memory from the least & will allocate first partition which is capable enough.

- Advantage \Rightarrow Simple, easy to use, easy to understand
- Disadvantage \Rightarrow Poor performance, both in terms of time & space.



Worked in both Variable & Fixed size partitioning.

Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

★ Best fit policy \Rightarrow We search the entire memory & will allocate the smallest partition which is capable enough.

- Advantage \Rightarrow Perform best in fixed size partitioning
- Disadvantage \Rightarrow Difficult to implement, perform worst in variable size partitioning as the remaining spaces which are of very small size.

★ Worst fit policy \Rightarrow It also searches the entire memory & allocate the largest partition possible.

- Advantage \Rightarrow Perform best in variable size partitioning
- Disadvantage \Rightarrow Worst in fix size partitioning, resulting into large internal fragmentation

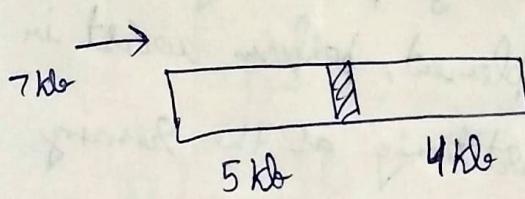
★ Next fit policy \Rightarrow

Main Ideas, Questions & Summary:

Library / Website Ref.: -

* External fragmentation \Rightarrow It is a function of contiguous allocation policy.

The space requested by the process is available in memory but, as it is not being contiguous, cannot be allocated this wastage is called external fragmentation.



Space is available but can't use because it is contiguous.

* Internal fragmentation \Rightarrow It is a function of fixed size partition which means, when a partition is allocated to a process. Which is either the same size or larger than request they, the unused space by the process in the partition is called internal fragmentation.

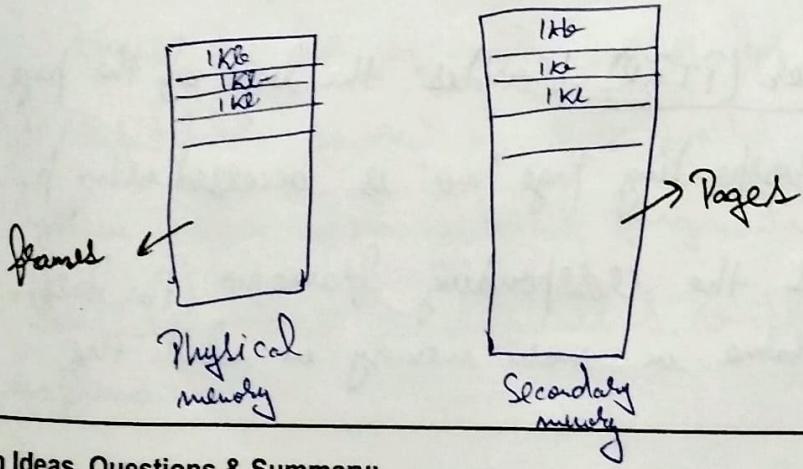
* How to solve internal fragmentation?

- Ans
- We should use non-contiguous allocation which means process can be divided into parts & diff parts can be allocated in different area.
 - We can also do defragmentation but this solution is very costly in respect to time. So we don't use this.

Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

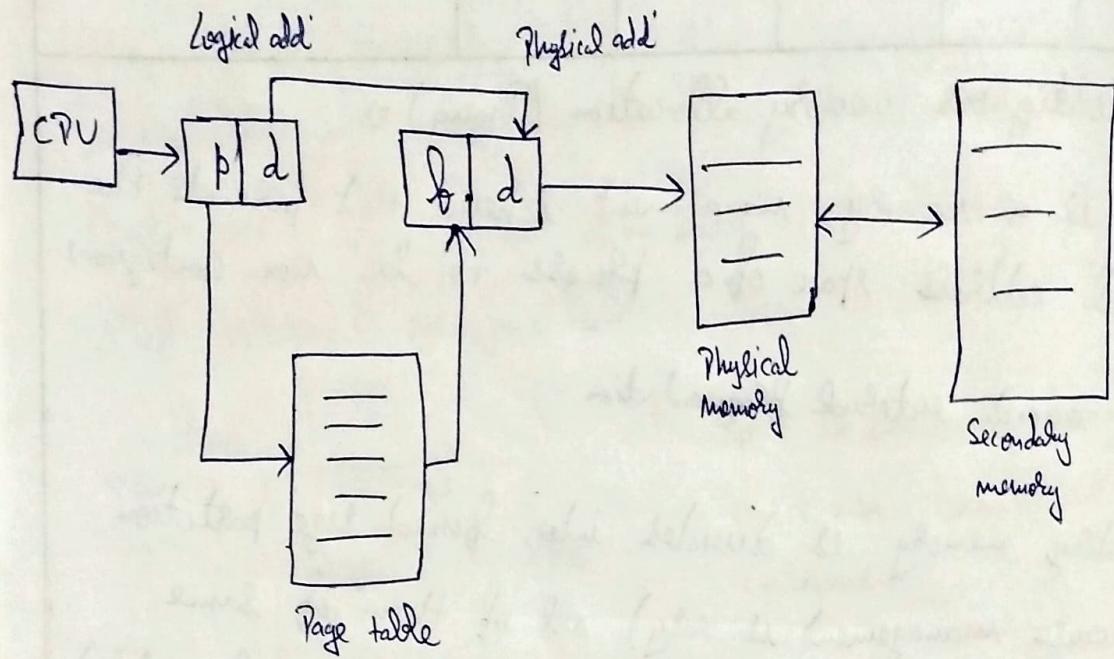
* Non Contiguous memory allocation (Paging) :-

- Paging is a memory-management scheme that permits the physical address space of a process to be non-contiguous.
- Paging avoid external fragmentation.
- Secondary memory is divided into fixed size partition (because management is easy) all of them of same size called pages (easy swapping & no external fragmentation).
- Main memory is divided into fix size partition , each of them having same size called frame.
- Size of frame = Size of pages (~~no of partition~~)
- In general no of pages are much more than no of frames.



Main Ideas, Questions & Summary:

★ Translation process \Rightarrow



- CPU generate a logical address which is divided into 2 parts $\Rightarrow p \& d$
 - $p \rightarrow$ page no.
 - $d \rightarrow$ instruction offset
- Page number (p) is used as an index into a Page table
- Page table base register (PTBR) provides the base of the page table & then the corresponding page no. is accessed using p .
- Here we will find the corresponding frame no (the base address of that frame in main memory in which the page is stored).
- Combine corresponding frame no ~~with~~ with the instruction offset & get physical address. Which is used to access main memory.

- Page-table size = No. of entries in Page-table * size of each entry (b)
- Block size = No. of pages * Size of each page

Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

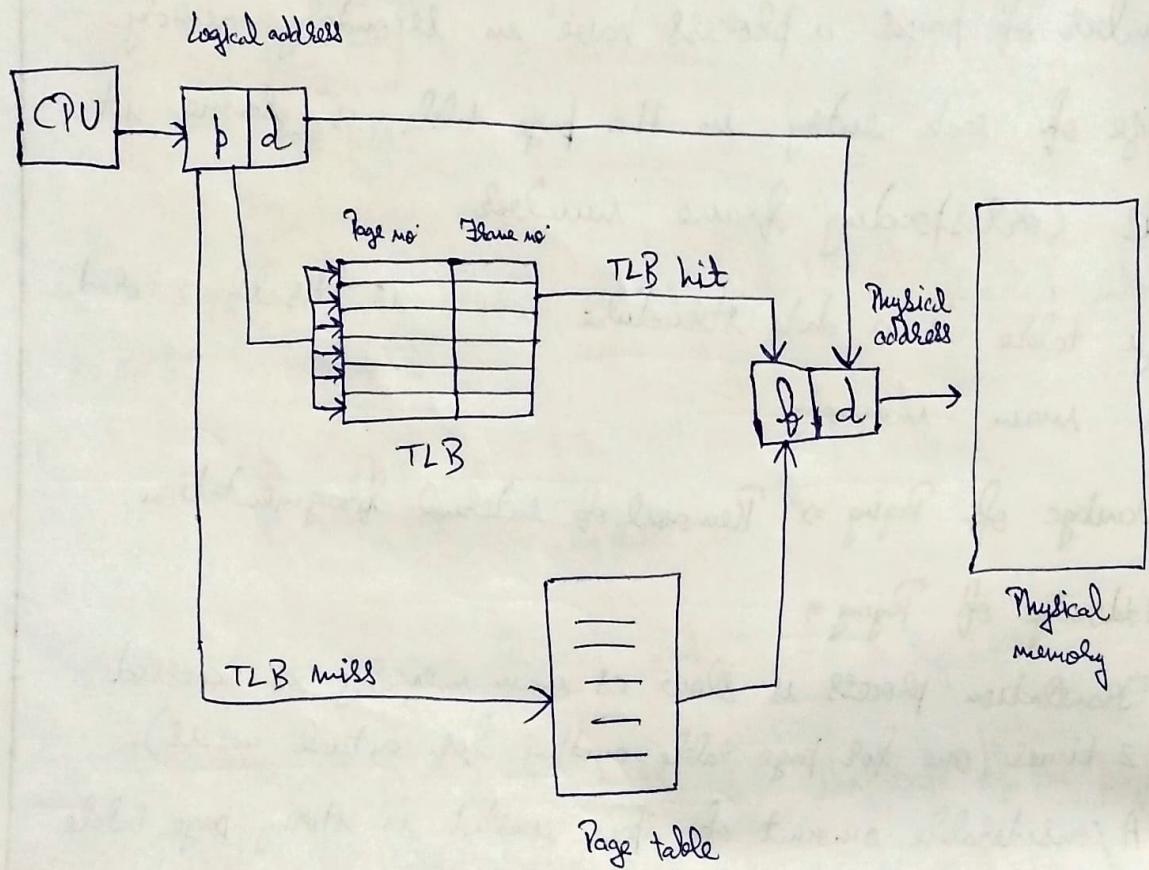
* Page Table \Rightarrow

- It is a data structure not hardware.
- Every process have a separate page table
- Number of entries a process have in page table is the number of pages a process have in secondary memory.
- Size of each entry in the page table is same it is corresponding frame number.
- Page table is a data structure which is itself stored in main memory.
- Advantage of Paging \Rightarrow Removal of internal fragmentation
- Disadvantage of Paging \Rightarrow
 - ① Translation process is slow as main memory is accessed 2 times (one for page table, another for actual access).
 - ② A considerable amount of space wasted in storing page table.
 - ③ System suffer from internal fragmentation (as it is fixed size/pat).
 - ④ Translation process is difficult & complex to understand & implement.

Main Ideas, Questions & Summary:

* Resolving disadvantage of paging =>

- Translation process is slow. To resolve this we take help of TLB. The TLB is associative, high speed memory.
- Translation Lookaside ~~buffer~~ buffer (TLB)
- It is a hardware, not software



Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

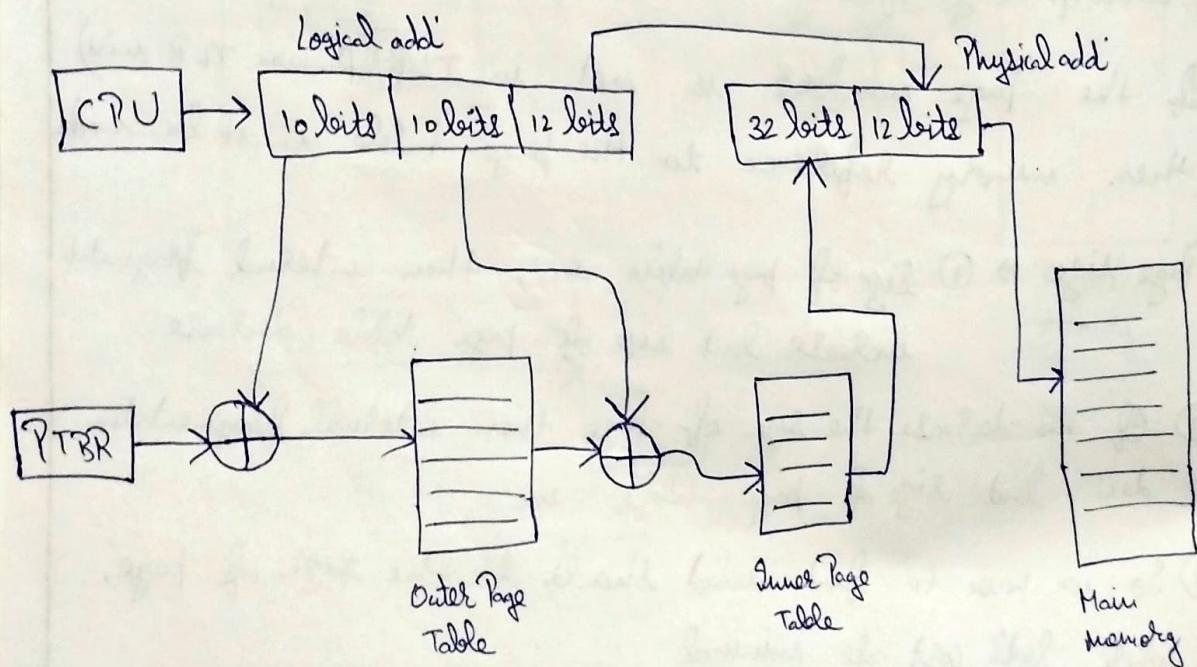
- Each entry in the TLB consists of 2 parts \Rightarrow a key (page no.) & a value (frame no.). When the associative memory is search for page no, the page no is compared with all page no simultaneously. If the item is found, the corresponding frame no field is returned.
- The search is fast; the hardware, however, is expensive, TLB contains the frequently referred page numbers & corresponding frame number.
- If the page number is not in TLB (known TLB miss) then memory reference to the page table must be made.
- Page size \Rightarrow
 - ① If we increase the size of page table inc, then internal fragmentation increase but size of page table decrease.
 - ② If we decrease the size of page then internal fragmentation dec but size of page table inc.
 - ③ So we have to find what should be the size of page, where both cost are minimal.

Main Ideas, Questions & Summary:

Library / Website Ref.:-

* Multilevel paging / Hierarchical paging \Rightarrow

- Modern systems supports a large logical address space.
In such case, the page table itself becomes excessively large and can contain millions of entries. It can take a lot of space in memory, so cannot be accommodated into a single frame.
- A simple solution to this is to divide page table into smaller pieces.
- One way is to use a two-level paging algorithm, in which the page table itself is also paged.



Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

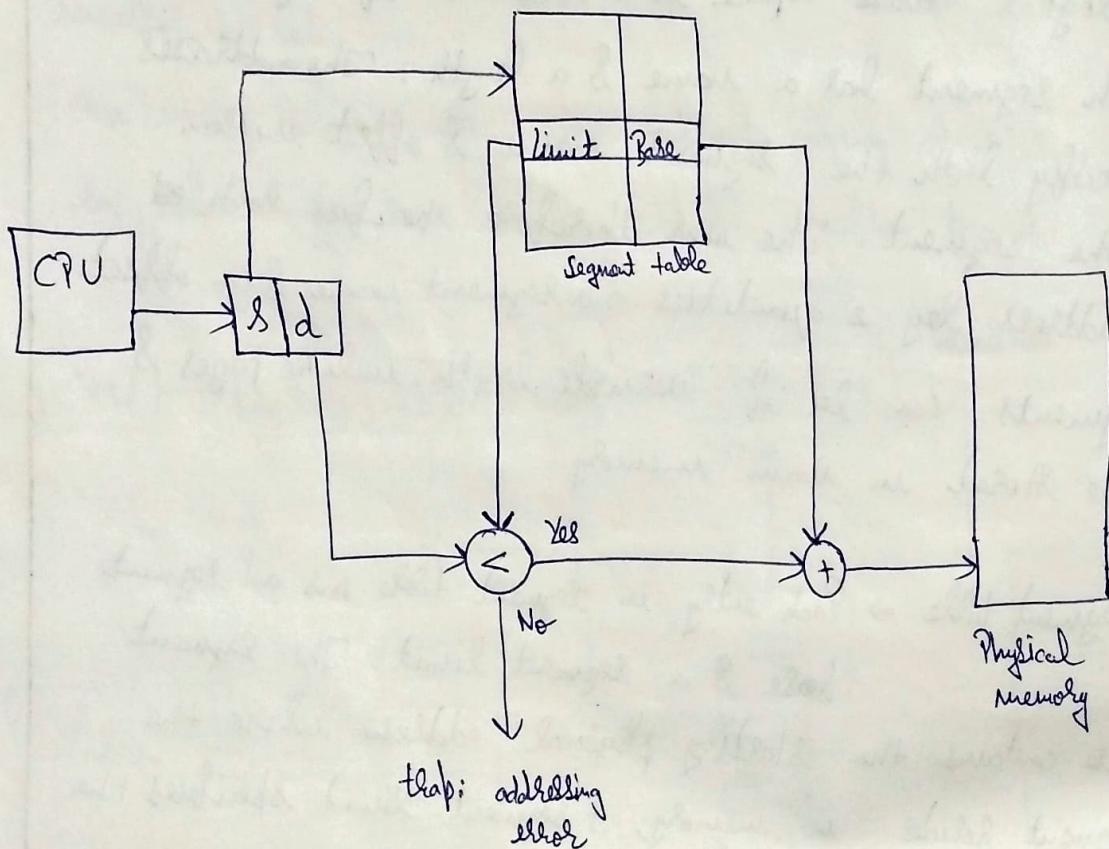
* Segmentation \Rightarrow Paging is unable to separate the user's view of memory from the actual physical memory. Segmentation is a memory management scheme that supports this user view of memory.

- A logical address space is a collection of segments. Each segment has a name & a length. The addresses specify both the segment name & offset within the segment. The user therefore specifies each address by 2 quantities \Rightarrow a segment name & an offset.
- Segments can be of variable length unlike pages & are stored in main memory.
- Segment table \Rightarrow Each entry in segment table has a segment base & a segment limit. The segment base contains the starting physical address where the segment resides in memory, & segment limit specifies the ~~limit~~ length of segment.

Main Ideas, Questions & Summary:

Library / Website Ref.: -

- The segment no. is used as an index to the segment table. The offset d of logical address must lie below 0 and segment limit. If it is not, we trap to the OS.
- When an offset is legal, it is added to segment base to produce the address in physical memory of the desired byte. Segmentation suffers from internal fragmentation.



It is not good, so use segmentation with paging.

Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

★ Segmentation with paging \Rightarrow

- Segmentation suffers from internal fragmentation. It is better to divide segments into pages.
- In segmentation with paging a process is divided into segments & further segments are divided into pages.
- It is quite better than multilevel paging.

Segment no.	Page no.	Offset
-------------	----------	--------

Frame no.	Offset
-----------	--------

Same as previous

★ Inserted page-table \Rightarrow

Main Ideas, Questions & Summary:

Library / Website Ref.: -

★ Virtual memory \Rightarrow It is a memory management technique used by OS to give the appearance of a large, continuous block of memory to application, even if the physical memory (RAM) is limited. Enabling large applications to run on system with less RAM.

★ Pure Demand paging \Rightarrow

- A memory management scheme where a process starts with no pages in memory. Pages are only loaded when explicitly required during execution.
- ① Process starts with zero pages in memory. Immediate page fault occurs.
 - ② Load the needed page into memory. Execution resumes after the required page is loaded into memory.
 - ③ Additional page faults occur as the process continues and requires new pages.
 - ④ Execution proceeds without further faults once all necessary pages are in memory. The key principle is to only load pages when absolutely necessary.

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Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

• Advantage =>

- A program would no longer be constrained by the amount of physical memory that is available, Allows the execution of processes that are not completely in main memory i.e. process can be larger than main memory.
- More programs could be run at the same time as use of main memory is less.

• Disadvantage =>

- Virtual memory is not easy to implement.
- It may substantially decrease performance if it is used ~~Carelessly~~ (Thrashing)

Main Ideas, Questions & Summary:

Library / Website Ref.:-

* Implementation of virtual memory =>

- We add a new column page table, which have binary value 0 or Invalid which means page is not currently in main memory, 1 or valid which means page is currently in main memory.

* Page Fault => When a process tries to access a page that is not in main memory then a page fault occurs.

* Steps to handle page fault =>

- ① If the reference was invalid, it means there is a page fault and page is not currently in main memory, now we have to load this required page in main memory.
- ② We find a free frame if available we can brought in desired page, but if not we have to select page as a victim & swap it out from main memory to secondary memory & then swap ~~in~~ in desired page.

Program

Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

We can reduce this overhead by using a Modify bit or Dirty bit as a new column in page table.

- 3.1 The modify bit for page is set whenever the page has been modified. In this case, we must write the page to disk.
 - 3.2 If the modify bit is not set \Rightarrow It means the page has not been modified since it was read into main memory. We need not write the memory page to disk.
- * Page Replacement algo \Rightarrow It will decide which page to replace in demand paging

Page

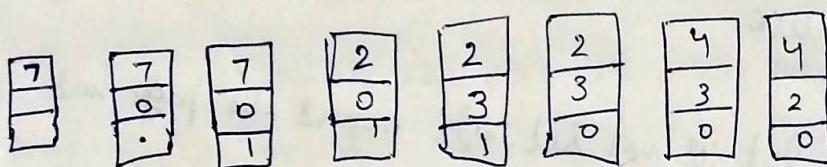
Main Ideas, Questions & Summary:

Library / Website Ref.:-

① First In First Out Algo \Rightarrow

It associates with each page, the time when that page was brought into memory. When a page must be replaced, the oldest page is chosen i.e. first page that came into memory will be replaced first.

$$P_n \Rightarrow 7 \ 0 \ 1 \ 2 \ 0 \ 3 \ 0 \ 4 \ 2 \ 3 \ 0$$

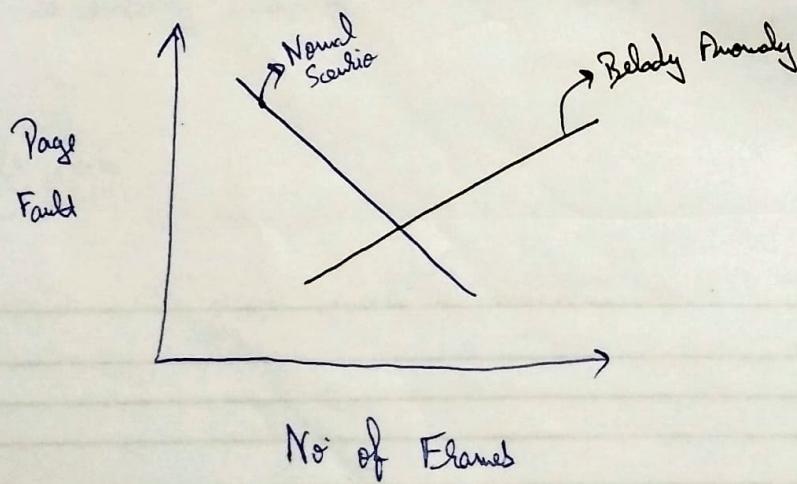


No. of page fault 8

hit / miss

- Easy to understand & program.
 - Performance not always good.

* Belady's Anomaly \Rightarrow For some page replacement algo;
the page fault rate may increase
as the no of allocated frames increase.

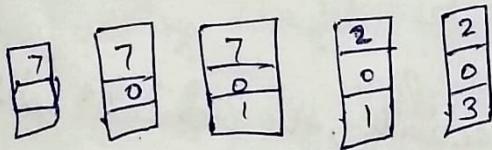


Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

(2) Optimal Page Replacement Algo \Rightarrow

- Replace the page that will not be used for longest period of time.
- It has lowest page fault rate of all algo.
- Never suffer from Belady's anomaly
- Difficult to implement because it requires future knowledge of reference string.

Ex: 7 0 1 2 0 3 0 4 2 3 0 3 2 1

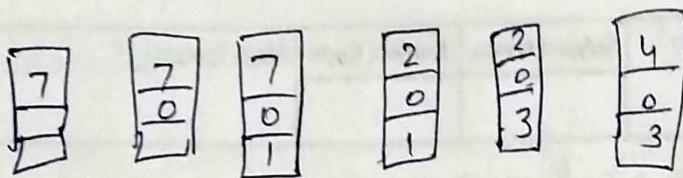
(3) Least Recently Used (LRU) algo \Rightarrow This algo looks backwards in time rather than forward. Replace the page that has not been used for longest period of time.

- Much better than FIFO in term of page fault.
- Never suffer from Belady anomaly.

Main Ideas, Questions & Summary:

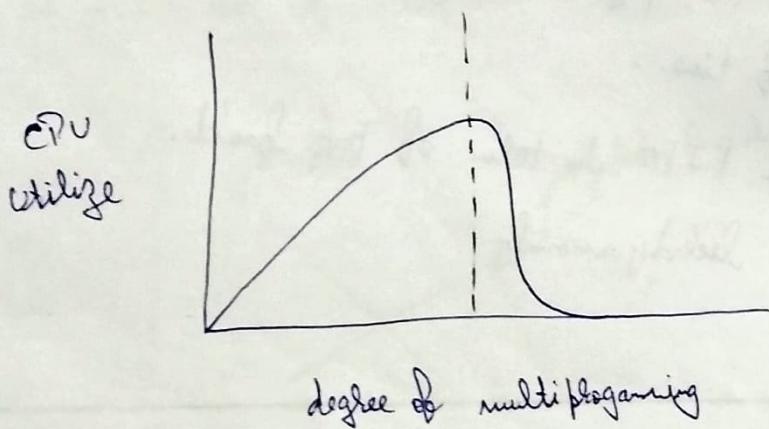
Library / Website Ref.: -

$P_k \Rightarrow 7 \ 0 \ 1 \ 2 \ 0 \ 3 \ 0 \ 4 \ 2 \ 3$



* Thrashing \Rightarrow

- ① When a process spends more time swapping pages than executing, it's called Thrashing. Low CPU utilization prompts adding more processes to increase multiprogramming.
- ② If a process needs more frames, it starts taking them from others, causing those processes to also fault & swap pages. This empties the ready queue & lower CPU utilization.
- ③ Responding to decreased CPU activity, the scheduler adds more processes, worsening the issue. This lead to thrashing.

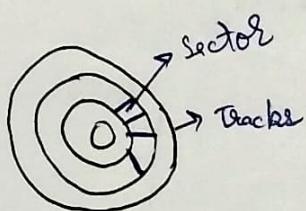


Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

★ Risk Management \Rightarrow

- Total Transfer time = Seek time + Rotational Latency + Transfer time
- Seek time \Rightarrow Time taken by Read/Write header to reach the correct track.
- Rotational Latency \Rightarrow It is the time taken by read/write header during the wait for the correct sector. In general, it's a random value, so for average analysis, we consider the time taken by disk to complete half rotation.
- Transfer time \Rightarrow Time taken by read/write header either to read or write on a disk.

$$\text{total time} = (\text{file size} / \text{track size}) * \text{time taken in one revolution}$$



Main Ideas, Questions & Summary:

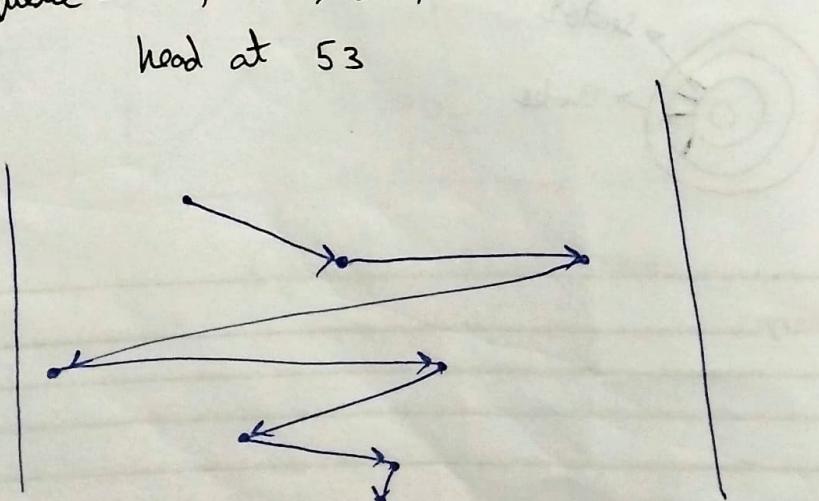
* Disk Scheduling \Rightarrow It is a technique, OS use to manage the order in which disk I/O requests are processed. Also known as I/O scheduling.

- Main goal is to optimize the performance of disk operation, reduces the time it takes to access data & improve overall system efficiency.

① FCFS (First come First serve) \Rightarrow In this, the requests are addressed in the order they arrive in disk queue.

- Advantage \Rightarrow Easy to use & understand
 \Rightarrow Every request gets a fair chance
 \Rightarrow No Starvation
- Disadvantage \Rightarrow Does not try to optimize seek time, or waiting time.

$R_u \Rightarrow$ queue = 48, 183, 37, 122, 14
head at 53

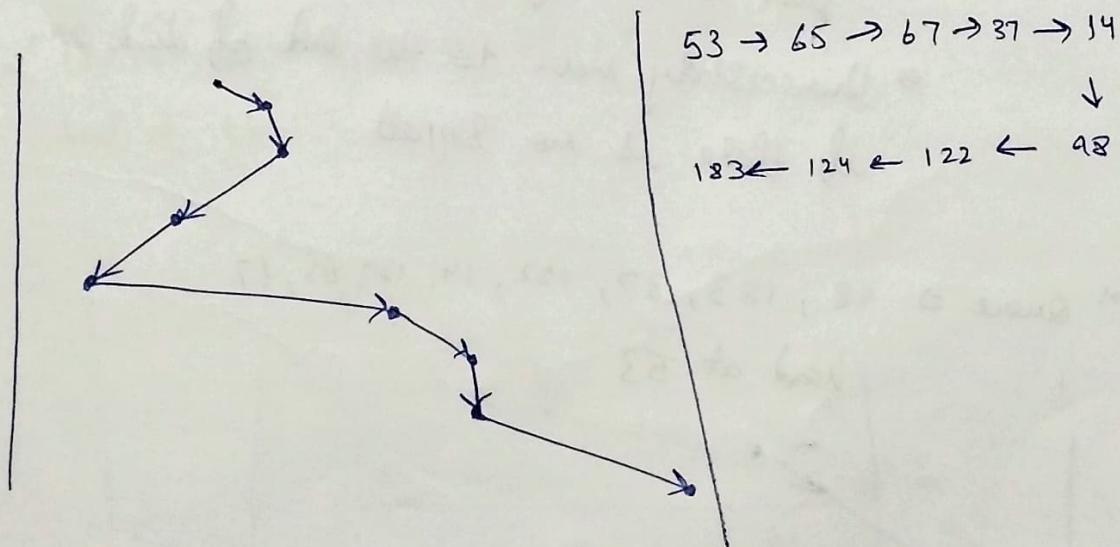


Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

② SSTF (Shortest Seek Time First) scheduling \Rightarrow

- In this, request nearest to the disk arm will get executed first i.e. requests having shortest seek time are executed first.
- Advantage \Rightarrow Seek movement decrease
 \Rightarrow Throughput increase
- Disadvantage \Rightarrow Overhead to calculate the closest request
 \Rightarrow Can cause starvation

Ex \Rightarrow Queue \Rightarrow 48, 183, 37, 122, 14, 124, 65, 67
 head at 53



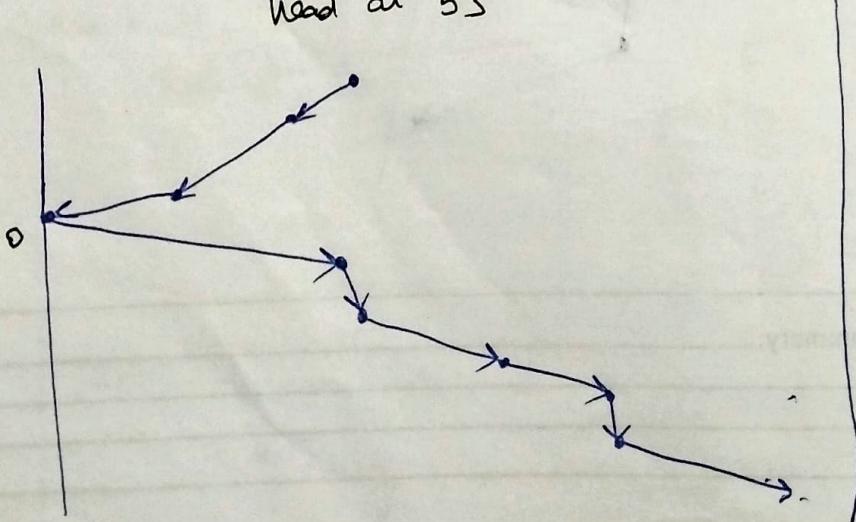
Main Ideas, Questions & Summary:

Library / Website Ref.:-

③ SCAN | Elevator Algo. \Rightarrow

- Disk arm starts at one end of disk & move towards the other end, servicing requests as it reaches each track, until it gets to other end of disk.
- At the other end, the direction of head movement is reversed, & servicing continues. The head continuously scans back & forth across the disk.
- Advantage \Rightarrow Easy to use & understand
 - \Rightarrow No starvation
 - \Rightarrow Average response time
- DisAdvantage \Rightarrow Long waiting time for request for location just visited by disk arm.
 - \Rightarrow Unnecessary move to the end of disk, even if there is no request

\hookrightarrow Queue \Rightarrow 48, 183, 37, 122, 14, 124, 65, 67
head at 53



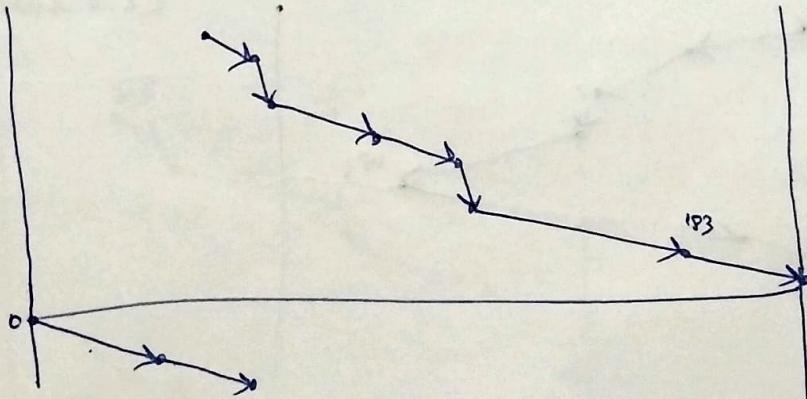
Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

(4) C-Scan Scheduling \Rightarrow In this, head moves from one end of disk to other, servicing requests along the way. When the head reaches the other end, however, it immediately returns to the beginning of the disk without servicing any request on return trip.

- Adv \Rightarrow Better response time compared to Scan
 \Rightarrow Provide more uniform wait time compared to SCAN.
- Pis Adv \Rightarrow More seek movement in order to reach starting position.

Ex \Rightarrow 48, 183, 37, 122, 14, 127, 65, 67

head at 53



Main Ideas, Questions & Summary:

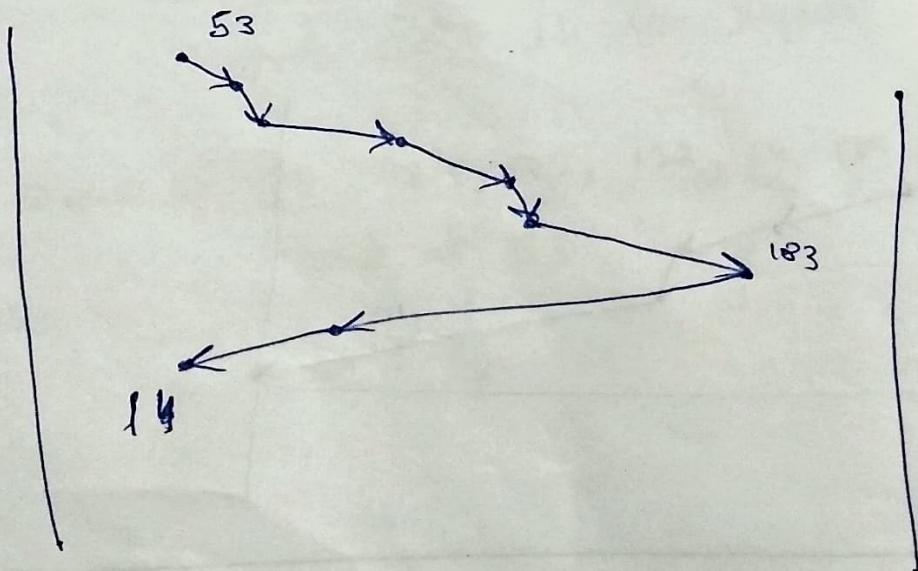
Library / Website Ref.: -

⑤ Look Scheduling \Rightarrow In this, disk arm inspite of going to end of disk goes only to the last request to be serviced in front of head and then reverses its direction from there only. Thus, it prevents the extra delay which occurs due to unnecessary traversal to end of disk.

- Adv \Rightarrow Better performance than SCAW
 \Rightarrow Should be used in case of less load
- DisAdv \Rightarrow Overhead to find last request
 \Rightarrow Should not be used in case of more load

Ex \Rightarrow 98, 183, 37, 122, 14, 124, 65, 67

head = 53



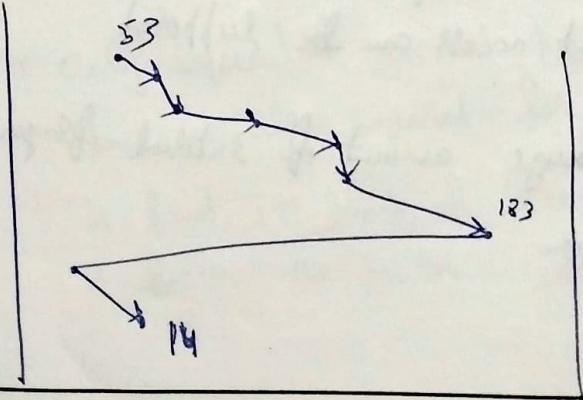
Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

⑧ Clook \Rightarrow In this, the disk arm in spite of going to end, goes only to the last request to serviced in front of head & then from there goes to other end's last request. Thus, it also prevents the extra delay which occurred due to unnecessary travelled to the end of disk.

- Adv \Rightarrow Better Response time than Look
 - \Rightarrow Provides more uniform wait time compared to Look.
- Pis Adv \Rightarrow Overhead to find the last request & go to initial position is more
 - \Rightarrow Should not used in case of more load

\hookrightarrow 98, 183, 37, 122, 14, 124, 65, 67

Head = 53



Main Ideas, Questions & Summary:

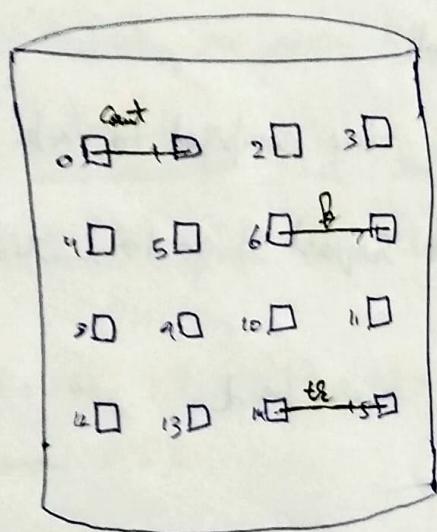
Library / Website Ref.:-

* File allocation methods =>

① Contiguous Allocation =>

- Each file occupies a set of contiguous blocks on disk.
- In directory usually we store 3 column file name, start addr, & length of file in number of blocks

disk block address



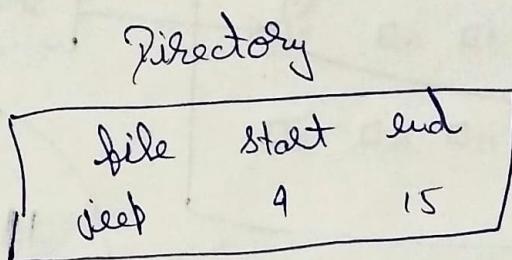
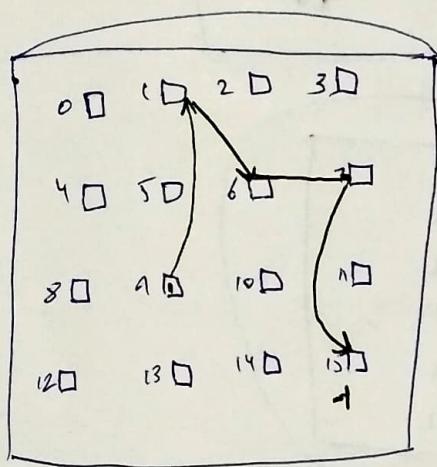
directory		
file	start	length
Count	0	2
the	14	3
of	6	2

- Adv => Accessing file is easy.
=> Sequential & direct access can be supported.
- Disadv => Suffer from huge amount of internal fragmentation
=> File modification

Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

② Linked allocation \Rightarrow

- Each file is a linked list of disk blocks; the disk blocks may be scattered anywhere on disk.
- The directory contains a pointer to the first and last block of file.

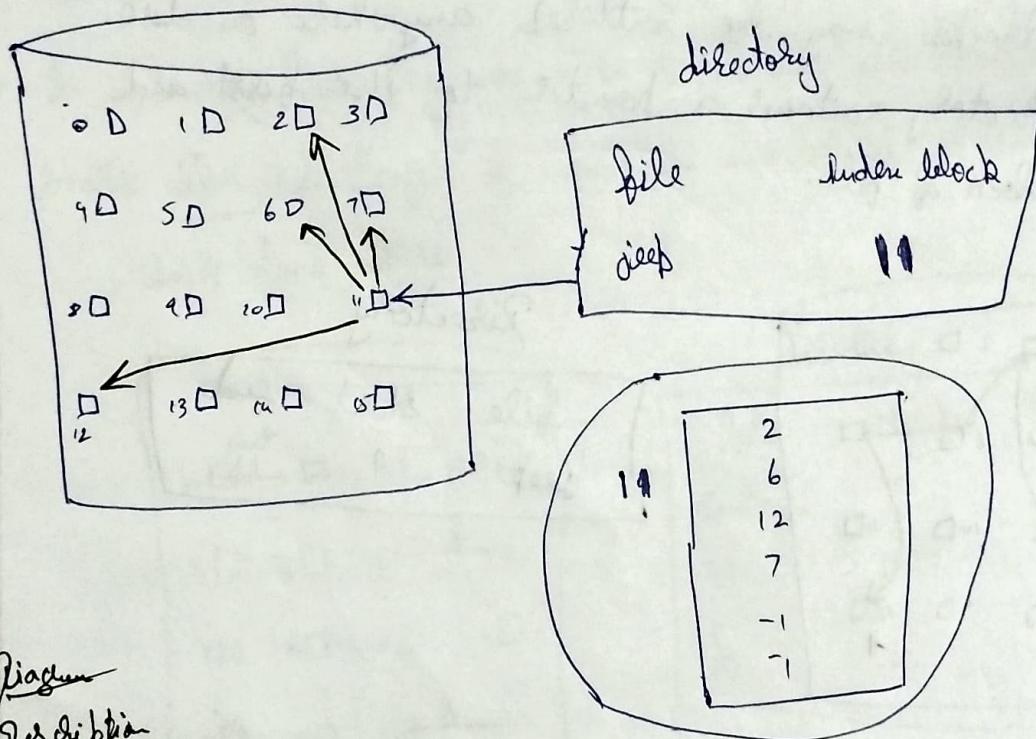


- Above \Rightarrow Create, read, write a new file easily.
 \Rightarrow Size of file need not to be declared when file created.
 \Rightarrow No internal fragmentation.
- Disadv \Rightarrow Only sequential access possible.
 \Rightarrow Space is also required for pointers
 \Rightarrow To find i^{th} block of file, we must start a beginning & follow the pointers until we get to i^{th} block.

Main Ideas, Questions & Summary:

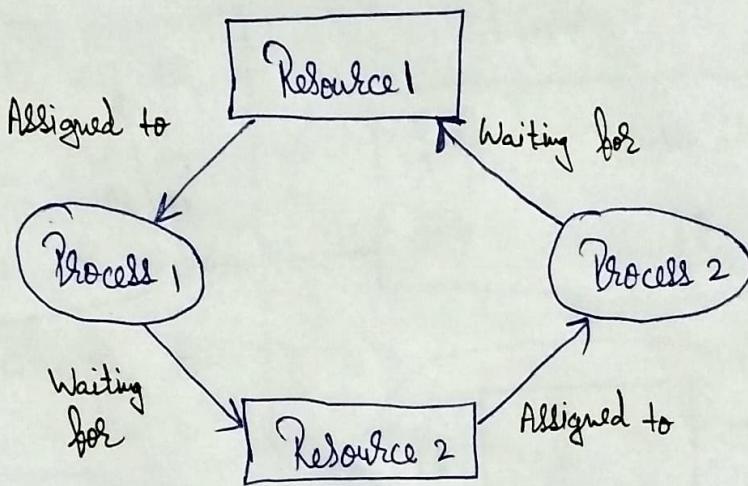
Library / Website Ref.:-

③ Indirect Allocation \Rightarrow This solves problems of contiguous and linked allocation, by bringing all the pointers together into one location: index block



- Adv \Rightarrow Indirect allocation supports direct access,
 \Rightarrow No internal fragmentation
- Pits Adv \Rightarrow Can suffer from wasted space in indirect allocation
 \Rightarrow pointer overhead of index block is generally greater than pointer overhead of linked allocation.

★ Deadlock \Rightarrow It is a situation where a set of processes are blocked because each process is holding a resource & waiting for another resource acquired by some other process.



★ Necessary conditions for deadlock \Rightarrow

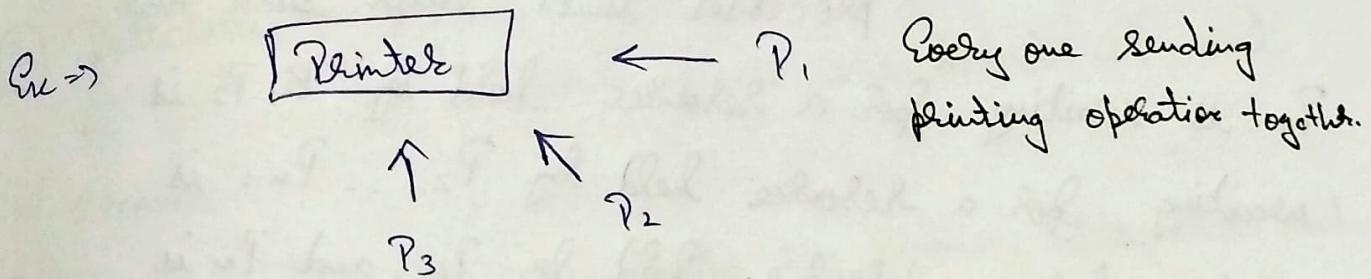
- Deadlock can occur if all these 4 condition occur in system simultaneously.

- ① Mutual exclusion
- ② Hold & wait
- ③ No pre-emption
- ④ Circular wait

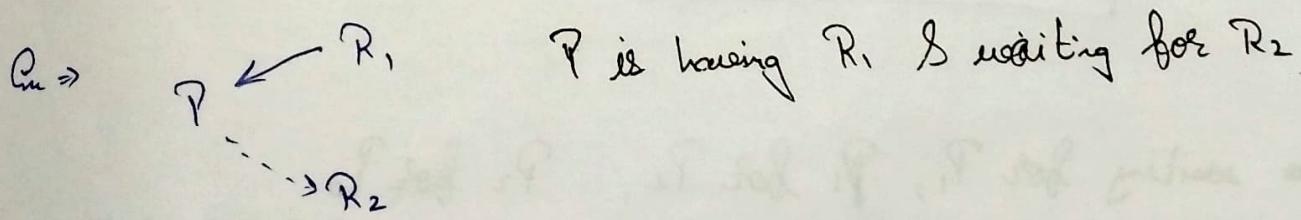
Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

① Mutual exclusion \Rightarrow In this, at least one resource must be held in a non-shareable mode, i.e. only one process at a time can use the resource.

- If another process request that resource, the requesting process must be delayed until the resource has been released.



② Hold & wait \Rightarrow A process must be holding at least one resource and waiting to acquire additional resources that are currently being held by other processes.

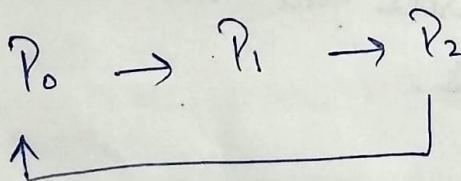


③ No pre-emption \Rightarrow

- Resources cannot be preempted.
- A resource can be released only voluntarily by the process holding it, after that process has completed its task.
- Process cannot take resource forcefully that is holding by another process.

④ Circular wait \Rightarrow A set P_0, P_1, \dots, P_m of waiting processes must exist such that

P_0 is waiting for a resource held by P_1 , P_1 is waiting for a resource held by P_2 , \dots P_{m-1} is waiting for a resource held by P_m , and P_m is waiting for a resource held by P_0 .



P_0 waiting for P_1 , P_1 for P_2 , P_2 for P_0 .

Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

★ Deadlock Handling methods =>

- ① Prevention => Design such protocol that there is no possibility of deadlock
- ② Avoidance => Try to avoid deadlock in run time by ensuring that the system will never enter a deadlock state.
- ③ Detection => We can allow the system to enter a deadlock state, then detect it, and recover.
- ④ Ignorance => We can ignore the problem altogether and pretended that deadlocks never occur in system.

Main Ideas, Questions & Summary:

Library / Website Ref.: -

① Prevention \Rightarrow It means designing such system where there is no possibility of existence of deadlock. For that we have to remove one of the 4 necessary condition of deadlock. \hookrightarrow Polio vaccine

② Mutual exclusion \Rightarrow In this approach, there is no solution for mutual exclusion as resource can't be made sharable.

③ Hold & wait \Rightarrow

(i) Conservative approach \Rightarrow Process is allowed to run if & only if it has acquired all the resources.

(ii) Alternative Protocol \Rightarrow A process may request some resources and use them.

Before it can request any additional resource, it must release all the resources that it is currently allocated.

(iii) Wait time out \Rightarrow We place a max time out up to which a process can wait. After which process must release all holding resources & exit.

Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

- (3) No pre-emption \Rightarrow PPF
- (4) Circular wait \Rightarrow PPF
- Problem of prevention \Rightarrow System becomes slow and resources utilization is reduced
System throughput.
- Costly
- (2) Avoidance \Rightarrow To avoiding deadlocks we require additional information about how resources are to be requested. Which resources a process will request during its lifetime.
 - With this additional knowledge, the OS can decide for each request whether process should be wait or not.

Main Ideas, Questions & Summary:

Library / Website Ref.: -

$P_m \Rightarrow$

	More Need		
	E	F	G
P_0	4	3	1
P_1	2	1	4
P_2	1	3	3
P_3	5	4	1

	Allocation		
	E	F	G
P_0	1	0	1
P_1	1	1	2
P_2	1	0	3
P_3	2	0	0

System Max			
	E	F	G
	8	4	6

Current Need			
	E	F	G
P_0	3	3	0
P_1	1	0	2
P_2	0	3	0
P_3	3	4	1

Available			
	E	F	G
	3	3	0

- First, P_0 complete,

$$\begin{array}{r}
 330 \\
 101 \\
 \hline
 431
 \end{array} \rightarrow \text{New Available}$$

- Second, P_2 complete,

$$\begin{array}{r}
 431 \\
 103 \\
 \hline
 534
 \end{array}$$

- Third, P_1 complete,

$$\begin{array}{r}
 534 \\
 112 \\
 \hline
 646
 \end{array}$$

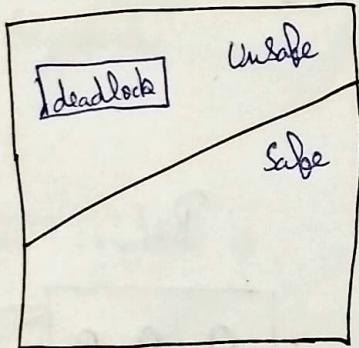
$P_0 \rightarrow P_2 \rightarrow P_1 \rightarrow P_3$
Safe Sequence

- Fourth, P_3 complete

$$\begin{array}{r}
 646 \\
 200 \\
 \hline
 846
 \end{array}$$

Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

- * Safe Sequence \Rightarrow Some sequence in which we can satisfies demand of every process without going into deadlock, if yes, this sequence is called safe sequence.
- * Safe state \Rightarrow If there exist at least one possible safe sequence
- * Unsafe state \Rightarrow If there exist no possible safe sequence.



- * Safety algo \Rightarrow

Main Ideas, Questions & Summary:

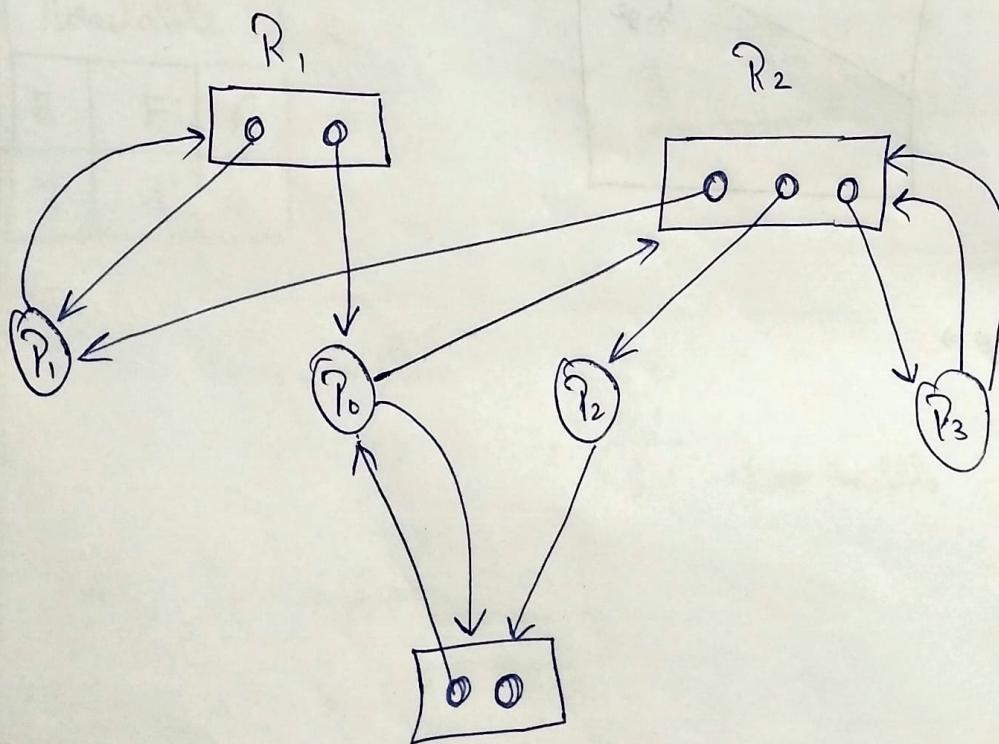
Library / Website Ref.:-

* Resource Allocation graph =

- Deadlock can also be described in terms of a directed graph called System resource-allocation graph.
- Set of vertices is partitioned into 2 different types of nodes

$$P = \{P_1, P_2, \dots, P_n\} \quad \text{processes}$$

$$R = \{R_1, R_2, \dots, R_m\} \quad \text{Resources}$$



R_3

$P_2 \rightarrow P_0 \rightarrow P_1 \rightarrow P_3$

No deadlock

Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

③ Deadlock detection & recovery \Rightarrow

- Once deadlock is detected there are 2 options for recovery from deadlock
- Process Termination \Rightarrow
 - Abort all deadlocked processes
 - Abort one process at a time until the deadlock is removed.
- ~~Resource~~
- Recourse pre-emption \Rightarrow
 - Selecting a victim
 - Partial or complete rollback

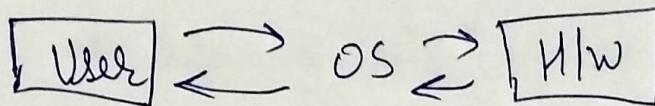
Main Ideas, Questions & Summary:

Library / Website Ref.:-

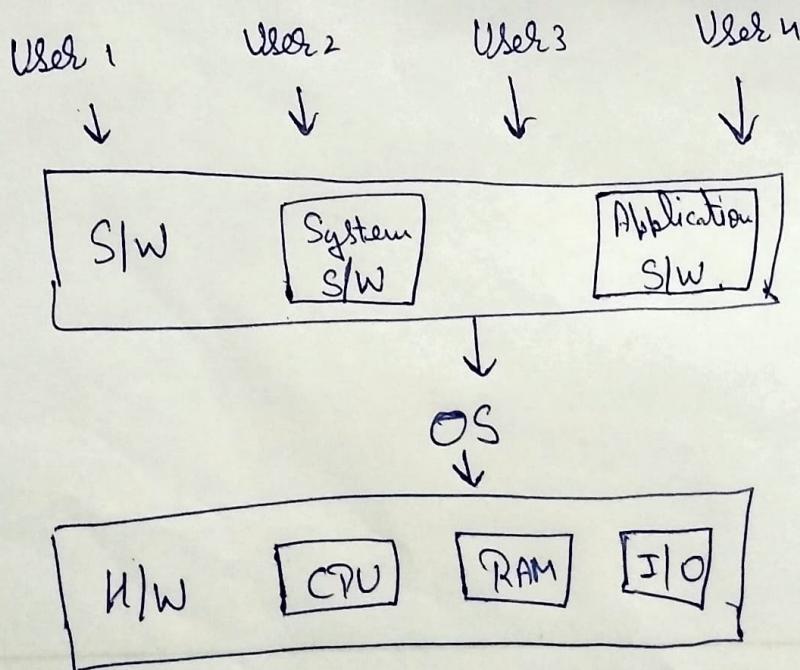
④ Ignorance (Ostrich algo) =>

- OS behaves like there is no concept of deadlock.
- Ignoring deadlock can lead to system performance issues as resources get locked by idle processes.
- Many OS opt for this approach to save on the cost of implementing deadlock detection.
- Deadlocks are often rare. Manual restart may be required when a deadlock occurs.

★ Operating System \Rightarrow It act as a intermediate layer between user and hardware by using application.



- Controls the system resources among various applications.
- It act as platform on which applications can be installed.
- Ex \Rightarrow Windows, IOS, Linux, Android
- Conceptual view of OS \Rightarrow



Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

★ Functions of OS =>

- ① Process Management
- ② Memory management
- ③ I/O device management
- ④ File management
- ⑤ Network management
- ⑥ Security & Protection

★ Batch OS =>

- ① Early computers were not interactive device
- ② User use to prepare a job which consist of
 - (i) Program
 - (ii) Control information
 - (iii) Input data
- ③ Only one job is given input at a time as there is no memory.
- ④ Common I/O device were punch card or tape driver.
- ⑤ Devices are very slow & processor remain idle most of time.

Main Ideas, Questions & Summary:

Library / Website Ref.: -

★ Spooling => It is a process in which data is temporarily held in memory or other volatile storage to be used by a device or a program.

★ Multiprogramming OS =>

★ Multitasking / time sharing OS =>

Difference

★ Multiprocessing OS =>

★ Symmetric & Asymmetric processing =>

★ Real time OS =>

★ Distributed OS =>

★ Structure of OS =>

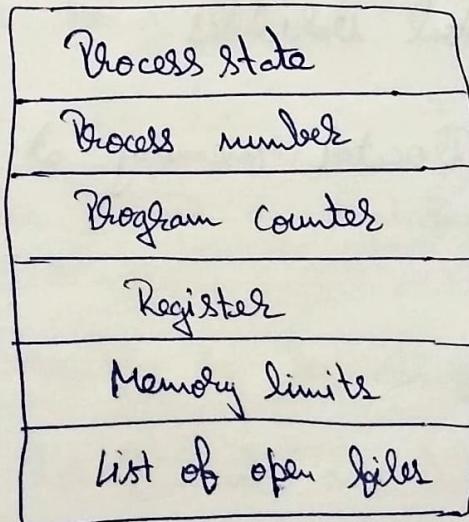
★ Micro Kernel =>

★ System call =>

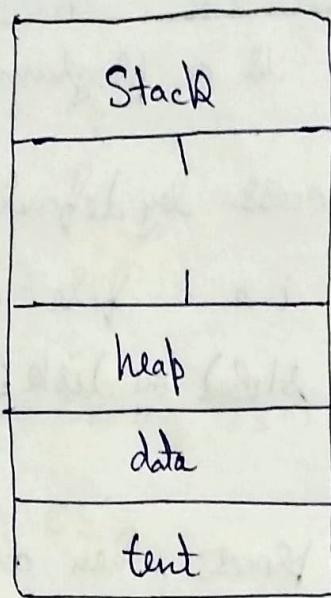
Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

★ Process \Rightarrow A process is a program in execution.

- A program is not a process by default. A program is a passive entity i.e a file containing a list of instructions stored on disk (often called executable file).
- A program becomes a process when an executable file is loaded into main memory & when its PCB (Process Control Block) created.
- A process is an Active Entity, which requires resources like main memory, CPU time, register, etc.



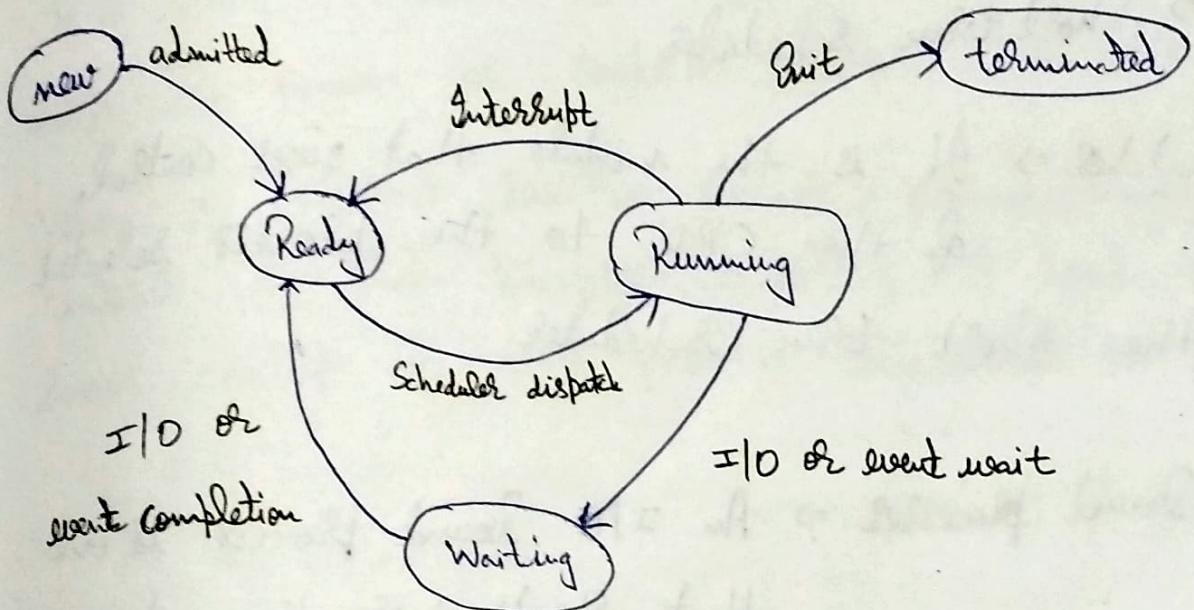
- Process consist of following sections \Rightarrow



- Text \Rightarrow Also known as program code
- Stack \Rightarrow Contains temporary data (function parameter, local variable)
- Data \Rightarrow Contain global variables
- heap \Rightarrow dynamically allocated memory at run time

Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

* Process State \Rightarrow A process changes states as it executes.



- New \Rightarrow Process are being created
- Running \Rightarrow Instruction are being executed
- Waiting (Blocked) \Rightarrow The process is waiting for some event to occur
- Ready \Rightarrow Process is waiting to be assigned to processor
- Terminated \Rightarrow Process has finished execution.

Main Ideas, Questions & Summary:

Library / Website Ref.: -

★ Scheduler \Rightarrow The main task is to select the jobs to be submitted into the system and to decide which process runs.

- Types \Rightarrow Long term scheduler | Spooler
 - \Rightarrow Medium term scheduler
 - \Rightarrow Short term scheduler

★ Dispatcher \Rightarrow It is the module that gives control of the CPU to the process selected by the short term scheduler.

★ I/O Bound Process \Rightarrow An I/O bound process is one that spends more time doing I/O than it spends ~~doing~~ doing computations.

★ CPU Bound Process \Rightarrow Generates I/O request infrequently, using more of its time doing computations

★ A system should have both I/O bound & CPU bound equally.

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Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

* Content Switch \Rightarrow Switching the CPU to another process required performing a state save of current process & a state restore of different process. This task is known as content switch.

When a content switch occurs, kernel saves the content of old process in its PCB & load the saved content of new process scheduled to run.

Main Ideas, Questions & Summary:

Library / Website Ref.:-

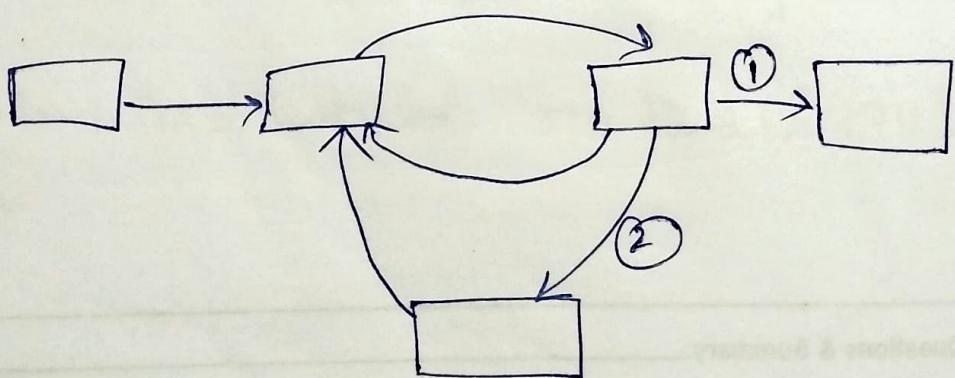
★ CPU Scheduling \Rightarrow Process that handles the removal of running processes from CPU and selection of another process on basis of particular strategy.

- Various algo are used for decision making.
- Make system fast
- Min waiting time
- Maximize throughput

★ Types of Scheduling \Rightarrow

① Non pre-emptive \Rightarrow Scheduling which takes place when a process terminates willingly.

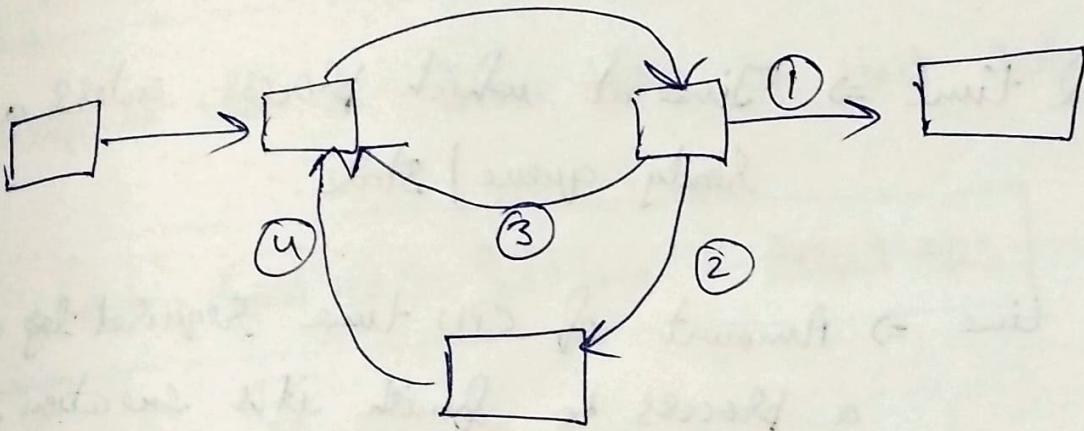
- Process will leave CPU only
 - (i) When a process completes its execution.
 - (ii) When a process wants to perform some I/O operation.



1 or 2

Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-
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② Pre-emptive \Rightarrow Scheduling which takes place when a process switches from Running state to Ready or from waiting state to Ready state.



* CPU utilization \Rightarrow Keeping the CPU as busy as possible

* Throughput \Rightarrow Number of processes that are completed per time unit, called throughput.

★ Waiting time \Rightarrow It is the sum of periods spent waiting in ready queue.

★ Response time \Rightarrow Time takes to start responding, not the time it takes to O/P the response.

★ Terminology of scheduling \Rightarrow

① Arrived time \Rightarrow Time at which process enters a ready queue / state.

② Burst time \Rightarrow Amount of CPU time required by a process to finish its execution.

③ Completion time \Rightarrow Time at which process finished its execution -

④ Turn Around time \Rightarrow Amount of time to execute a particular process.

$$\boxed{\text{Completion time} - \text{Arrived time}}$$

$$\text{Waiting time} + \text{Burst time}$$

Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

⑤ Waiting time \Rightarrow Amount of time a process has been waiting in ready queue.

$$\text{Turn around time} - \text{Burst time}$$

⑥ Response time \Rightarrow Time takes to start responding.

$$\text{Time at which a process got CPU first time} - \text{Arrived time}$$

* Scheduling Algorithms \Rightarrow

- ① First come First Serve \Rightarrow Simplest scheduling algo.
- . Process that requests the CPU first is allocated to CPU first
- . FIFO queue is used
- . Always non pre-emptive in nature

P.No	Arrival time (AT)	Burst time (BT)	Completion time (CT)	Turn Around time $TAT = CT - AT$	Waiting time $WT = TAT - BT$
P ₀	2	4	9	7	3
P ₁	1	2	5	4	2
P ₂	0	3	3	3	0
P ₃	4	2	12	8	6
P ₄	3	1	10	7	6
Allg				$\frac{7+4+3+7}{5}$ ⇒	$\frac{3+2+0+6}{5}$ ⇒

P₂ |
 P₁ |
 P₀ |
 P₄ |
 P₃ |

0 3 5 9 10 12

- Advantage ⇒ Easy to understand
 - ⇒ Easily implemented using queue
 - ⇒ Can be used for background processes where execution is not urgent.

Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

- Dis Advantage \Rightarrow Suffer from convoy effect
 - \Rightarrow Troublesome for time sharing system.
 - \Rightarrow Higher avg waiting time

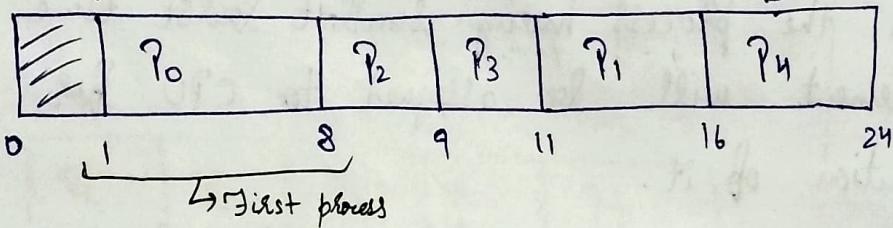
* Convoy Effect \Rightarrow If the small process have to wait more for CPU because of larger process then this effect called convoy.

② Shortest Job First (SJF) \Rightarrow

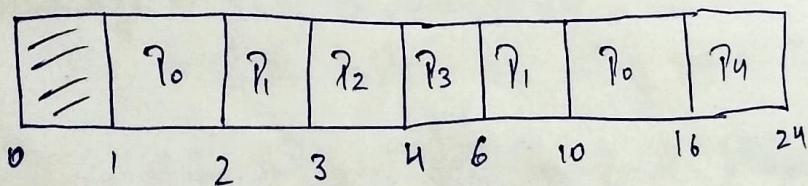
- . In this, the process having smallest burst time requirement will be assigned to CPU for execution of it.
- . If there is a ~~tie~~ tie, FCFS is used to break tie.

P.No	Arrival time (AT)	Burst time (BT)	Completion time (CT)
P ₀	1	7 6	
P ₁	2	5 4	
P ₂	3	1 ✓	
P ₃	4	2 ✓	
P ₄	5	8	

- Non pre-emptive



- Pre-emptive \Rightarrow



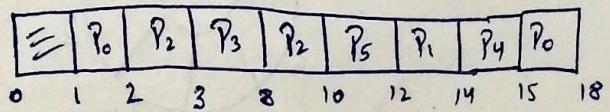
Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

- Advantage \Rightarrow Guarantees minimal average waiting time.
 - \Rightarrow Better average response time compare to FCFS.
- Disadvantage \Rightarrow Process with longest CPU burst time can goes to starvation.
 - \Rightarrow This algo cannot be implemented as there is no way to know the length of next CPU burst.

(3) Priority Scheduling \Rightarrow

P.No.	Arrival (AT)	Burst (BT)	Priority	CT
P ₀	1	4 3	4	
P ₁	2	2	5	
P ₂	2	3 2	7	
P ₃	3	5	8 (High)	
P ₄	3	1	5	
P ₅	4	2	6	

Arrival time



Main Ideas, Questions & Summary:

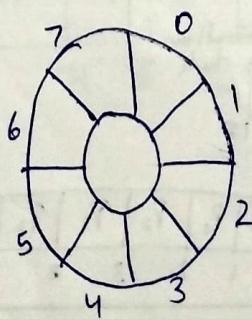
- Advantage \Rightarrow Allows us to run important process
 - \Rightarrow Support by system

- Disadvantage \Rightarrow Starvation with smaller priority
 - \Rightarrow No idea of response time or waiting time

* Ageing \Rightarrow A technique of gradually increasing the priority of process that wait in system for long time.

④ Round Robin =

- This algo is design for time sharing system.
- In this, we don't complete one process & then start another. Instead we divide time of CPU among the process in ready state (circular)
- Time Quantum which decides how much time should be given to processes.

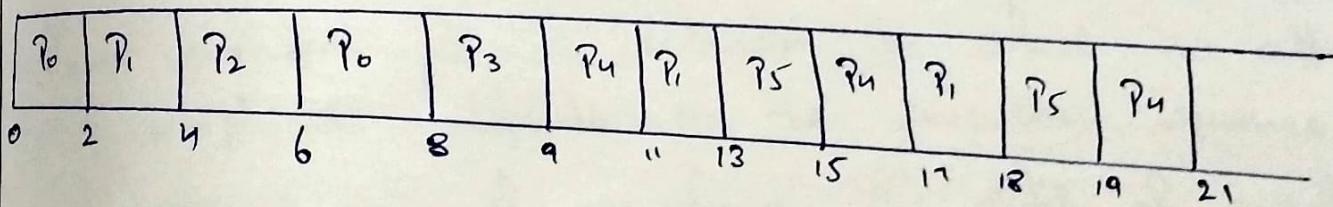


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Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-
P.No.	Arrival (AT)	Burst (BT)		Completion (CT)		
P ₀	0	4 2 ✓				
P ₁	1	5 3 1				
P ₂	2	2 ✓				
P ₃	3	1 ✓				
P ₄	4	6 4 2				
P ₅	6	3 1				

T.Q (Time quantum) = 2

P₀ P₁ P₂ P₀ P₃ P₄ P₁ P₅ P₄ P₁ P₅ P₄



Main Ideas, Questions & Summary:

Library / Website Ref.:-

- Advantage \Rightarrow Bestⁱⁿ terms of average response time
 - \Rightarrow Works well in time sharing system
 - \Rightarrow kind of SJF implementation
- DisAdvantage \Rightarrow Longer process may starve
 - \Rightarrow No idea of priority
 - \Rightarrow Heavily depends upon time quantum.
Performance

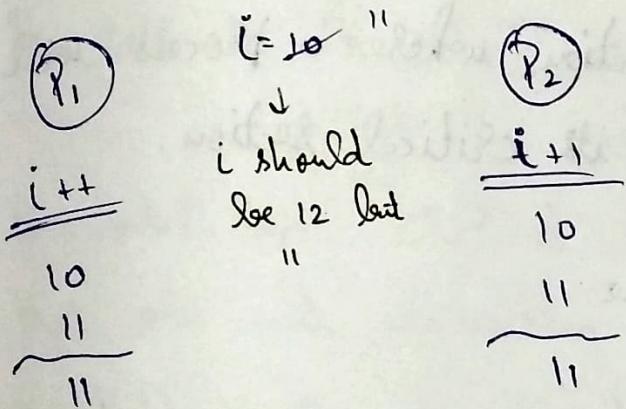
* Multi Level queue scheduling \Rightarrow

Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

* Process Synchronization & Race Condition =>

In a multiprogramming environment a good no. of processes compete for limited no. of resources.

Concurrent access to shared data at ~~some~~ some time may result in data inconsistency.



Race condition is a situation in which the output of a process depends on the execution sequence of process i.e. if we change the order of execution of diff' process with respect to other process the o/p may change.

- * General structure of a process \Rightarrow
- Initial section \Rightarrow Where process is accessing private resources.
 - Entry section \Rightarrow Entry section is that part of code where, each process request for permission to enter its critical section.
 - Critical section \Rightarrow Whole process is access shared resources.
 - Exit section \Rightarrow It is the section where a process will exit from its critical section.
 - Remainder section \Rightarrow Remaining code

$P() \{$

$\text{while}(\top) \{$

 Initial section

 Entry section

 Critical section

 Exit section

 Remainder section

} }

Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

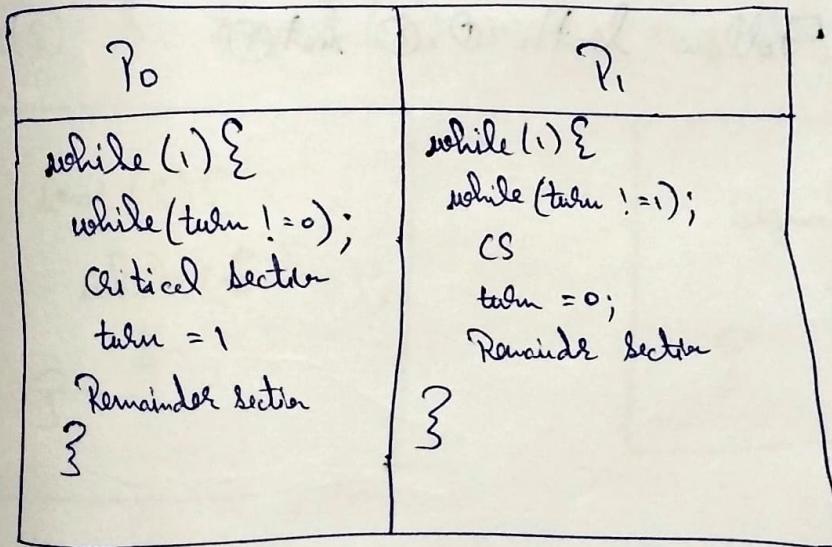
* Criterion to solve Critical section problem =>

- Mutual Exclusion => No 2 processes should be present inside the critical section at the same time, i.e. only one process is allowed in the critical section at an instant of time.
- Progress => If no process is executing in its critical section and some processes wish to enter their critical section, then only those processes that are not executing in their remainder sections can participate in deciding which will enter its critical section next. There should be no deadlock.
- Bounded Waiting => There exists a bound or a limit on the number of times a process is allowed to enter its critical section and no process should wait ~~for~~ indefinitely to enter the CS.

- ★ Some points to remember =>
 - Mutual exclusion & progress are mandatory requirements that needs to be followed in order to write a valid solution for CS problem.
 - Bounded waiting is optional criteria, if not satisfied then it may lead to starvation.
- ★ Solution to critical section problem =>
 - ① Two process solution
 - (i) Using Boolean variable turn
 - (ii) Using Boolean array flag
 - (iii) Peterson's Solution
 - ② Operating System solution
 - (i) Counting semaphore
 - (ii) Binary semaphore
 - ③ H/W solution
 - (i) Test & Set lock
 - (ii) Risible interrupt

Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

- * Two process Solution \Rightarrow
 - In general it will be difficult to write a valid solution in the first go to solve critical section problem among multiple processes, so it will be better to first attempt two process solution and then generalize it to N-process solution.
- Using boolean ~~is~~ variable turn \Rightarrow
- 0/1



turn = 0 (Initially)

Follow Mutual Exclusion

Not Follow Progress as we not ask Process to enter into CS or not

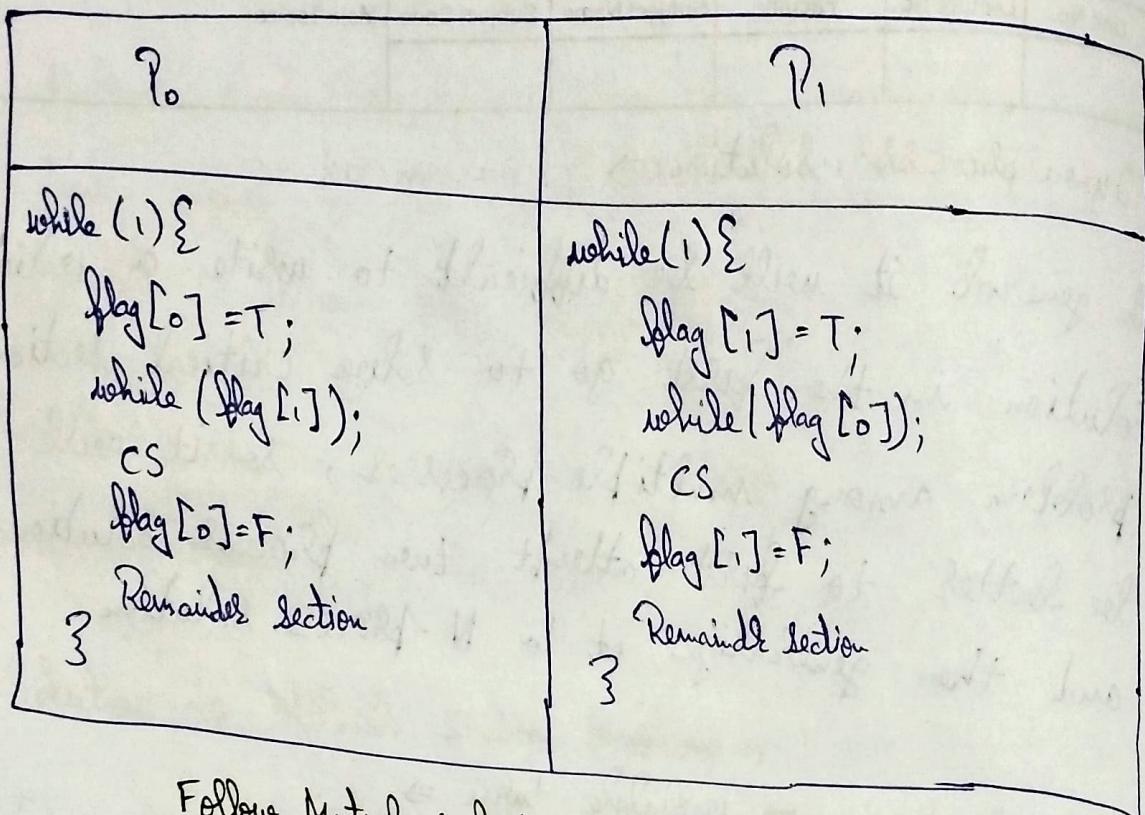
Main Ideas, Questions & Summary:

Library / Website Ref.:-

- Boolean array flag \Rightarrow flag

0	1
F	F

 initially



Follow Mutual exclusion

Not Follow progress

- Peterson Solution \Rightarrow Follow both ①, ② not ③

Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-
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* Operating System Solution (Semaphores) =>

- ① They are synchronization tools using which we will attempt M-process solution.
- ② A Semaphore (S) is a simple integer variable that, but apart from initialization it can accessed only through 2 standard atomic operations :
- Wait (S)
 - Signal (S)
- ③ The wait (S) operation was originally termed as $P(S)$ & signal (S) was originally called $V(S)$.

```
wait (S){  
    while ($ <= 0);  
    S--;  
}
```

```
Signal (S){  
    S++;  
}
```

④ While solving critical section problem only we
initialize semaphore $S = 1$

⑤ Semaphores are going to ensure Mutual Exclusion
and Progress but does not insure bounded waiting.

• $P_i() \{$

while (T) {

Initial Section

Wait (S)

CS

Signal (S)

Remainder Section

}

}

$P(2) \text{ lang2}$

$\rightarrow +2$

$P(1) \text{ lang}$

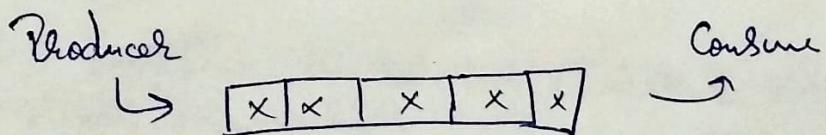
$(0 \rightarrow 2) \text{ lang}$

$\rightarrow -2$

Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

* Producer Consumer Problem | Boundary Buffer problem :-

- There are 2 process: producer & consumer
- Producer produces information & put it into buffer which have n cell, that is consumed by a consumer . Both producer & consumer can produce & consume only one article at a time .
- A producer needs to check whether the buffer is overflowed or not after producing an item, before accessing the buffer.
- Similarly, a consumer needs to check for an underflow before accessing the buffer & then consume an item.
- Producer & consumer cannot access buffer together.



Main Ideas, Questions & Summary:

Library / Website Ref.: -

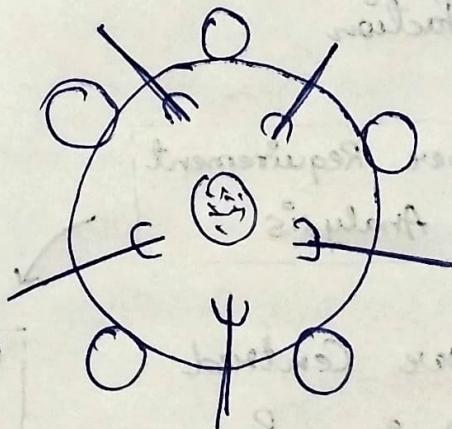
★ Reader-Writer Problem =>

- Suppose that a database is to be shared among several concurrent processes. Some of these processes may want only to read database (Readers), others may want to update (that is, read & write) database (Writers).
- If 2 readers access data simultaneously, no adverse effects will result.
- If a writer & some other processes (reader or writer) access the database simultaneously, chaos may ensue.
- Solution may allow more than one reader at a time but should not allow any writer.
- If writer is writing, no other ~~is~~ reader / writer should not allow

Date	Unit No.	Lecture No.	Faculty	Subject Name	Subject Code	Main Topics:-

* Dining Philosopher Problem =>

- Consider 5 philosopher who spend their lives thinking & eating.
- Philosopher share a circular table surrounded by 5 chairs, each belonging to one philosopher.
- In center of table is a bowl of rice; & the table is laid with 5 single chopsticks.
- When a philosopher thinks, she does not interact with her colleagues.



- Solution => Allow philosopher to pick her chopstick only if both chopsticks are available.