# Jayesh K. Gupta

Junior Undergraduate\_

CONTACT Information Dept. of Electrical Engineering

Indian Institute of Technology, Kanpur

Room B-105, Hall 5, IIT Kanpur

Kanpur - 208016, India

mobile: +91-9005434081 e-mail: jayeshkg@iitk.ac.in web: http://home.iitk.ac.in/~jayeshkg

RESEARCH INTERESTS

Machine learning and pattern recognition, bioinformatics, neural networks, signal and image processing, information theory.

EDUCATION

Indian Institute of Technology Kanpur, Kanpur, India

B.Tech in Electrical Engineering, 2011-present

- Cumulative Performance Index (CPI) 9.5 on a scale of 10 (after 4 semesters)

St. Paul's Sr. Sec. School, Jodhpur, Rajasthan, India

Senior School Certificate Examination (CBSE), March 2011

- Scored 93.8% marks in XII AISSCE, March 2011

Secondary School Certificate Examination (CBSE), March 2009

- Scored 94.6% marks in X AISSE, March 2009

ACADEMIC ACHIEVEMENTS

- All India Rank 477 in IIT Joint Entrance Examination 2011, out of around 500,000.
- Stood 10<sup>th</sup> in **Regional Mathematics Olympiad 2011**, Rajasthan Region.
- Received Academic Excellence Award for distinctive performance in the term 2011-12.
- Selected for KVPY (Kishore Vaigyanik Protsahan Yojana) Scholarship in 2011.
- Selected in the **Top 1%** in the **National Chemistry Olympiad** 2011.
- $\bullet$  Selected in the Top 1% in the Indian National Astronomy Olympiad 2011

Publication

Nishchal Verma, Sumanik Singh, **Jayesh K. Gupta**, Rahul K. Sevakula, Sonal Dixit and Al Salour, "Smart Phone Application for Fault Recognition", 2012 Sixth International Conference on Sensing Technology (ICST2012), 18-21 Dec. 2012

Nishchal Verma, Rahul K. Sevakula, **Jayesh K. Gupta**, Sumanik Singh, Sonal Dixit and Al Salour, "Smart Phone Application for Fault Recognition", *International Journal On Smart Sensing and Intelligent Systems*, Vol. 6, No.4, pp. 1763-1782, September 2013

Nishchal Verma, **Jayesh K. Gupta**, Sumanik Singh, Rahul Sevakula, Sonal Dixit, "**Feature Level Analysis**", *IEEE Workshop On Computational Intelligence: Theories, Applications and Future Directions*, (in review). (Poster presented can be found here)

Jayesh K. Gupta, Sumanik Singh, Nishchal K. Verma, "MTBA: Matlab Toolbox for Biclustering Analysis", IEEE Workshop On Computational Intelligence: Theories, Applications and Future Directions, (in review). (Poster presented can be found here)

RESEARCH EXPERIENCE Condition Based Monitoring of Air Compressors and Motors using Summer 2012
Acoustic Data

Mentor: Dr. Nishchal K. Verma

R&D Project, IIT Kanpur

A Smart Phone application, to **learn different fault states** of an industrial air compressor, was developed. The application was tested to recognize the fault state in real time as the air compressor was running. It has performed very well with classification accuracies above 93.73%. It is believed that similar application and model with some minor changes in specifications can be used for **acoustic pattern recognition** in wide range of fields; especially in industry.

## Feature Level Analysis

Winter 2012

Mentor: Dr. Nishchal K. Verma

R&D Project, IIT Kanpur

A case study of **acoustic and vibrational features** from different working states of an air compressor was undertaken and were **graphically analyzed** to derive the best feature set. We built an **SVM model** based upon these features and got comparable results to the standard PCA based SVM model.

## MTBA: Matlab Toolbox for Biclustering Analysis

Summer 2013

Mentor: Dr. Nishchal K. Verma

R&D Project, IIT Kanpur

A new Matlab toolbox, MTBA, designed to perform a variety of **biclustering algorithms** under a **common user interface** while providing additional facilities for data preprocessing, visualization, and validation, was developed. This allows the user to **compare biclustering results** from different algorithms and choose the approach that best fits their particular scenario.

MTBA is freely available at http://home.iitk.ac.in/~jayeshkg/mtba/ and bitbucket.

## Comparison of Biclustering Algorithms

Ongoing

Mentor: Dr. Nishchal K. Verma

R&D Project, IIT Kanpur

e-mail: nishchal@iitk.ac.in

Major biclustering algorithms are being surveyed, using the toolbox developed during summers to find their specific advantages and disadvantages for their usage in different datasets.

#### PROJECTS UNDERTAKEN

### **Autonomous Quadrotor**

**Summer 2012** 

Summer Project under Electronics and Aeromodelling Club, IIT Kanpur

Built a quadrotor using Arduino Mega 2560 for onboard processing and IMU Razor 9DOF for orientation determination. Xbee was used to communicate with the Arduino from an external computer.

TECHNICAL SKILLS

- Languages: C, Java, R, Python, Javascript, Haskell, SQL
- Software/Libraries: Matlab, GNU Octave, Android SDK, GNU Emacs, OpenCV
- Tools: UNIX shell scripting, LATEX, HTML5, CSS3

Relevant Courses

- Real and Complex Analysis, Linear Algebra, Differential equations, Probability and Statistics
- Microelectronics (Analog Circuits), Signal Systems and Networks, Control System Analysis
- Presently doing: Data Structures and Algorithm, Principles of Communication, Power Systems, Digital Electronics and Microprocessor Technology
- Next semester: Digital Signal Processing, Electromagnetic Theory, Microelectronics-II (Devices), Communication Systems

#### Positions of Responsibility

#### • Hobby Group Leader, Science CoffeeHouse

Currently managing the activities of the science discussion group at IIT Kanpur, creating a healthy scientific atmosphere to bring together students of all disciplines to share ideas/insights.

- Academic Mentor (2012-2013), Counselling Service, IIT Kanpur,
  - Provided academic assistance, along with taking extra-lectures for students struggling with academics.
- Institute Student Guide, Counselling Service, IIT Kanpur,

Mentoring 6 freshmen to guide them for a smooth transition into campus life in both academic and extracurricular spheres.

• Quiz Club Secretary

Worked for promotion of quizzing activities in the institute. Organized various quizzes, including *National General Quiz* at Antaragni 2012.

#### References

#### Dr. Nishchal K. Verma

Assistant Professor

Department of Electrical Engineering

IIT Kanpur