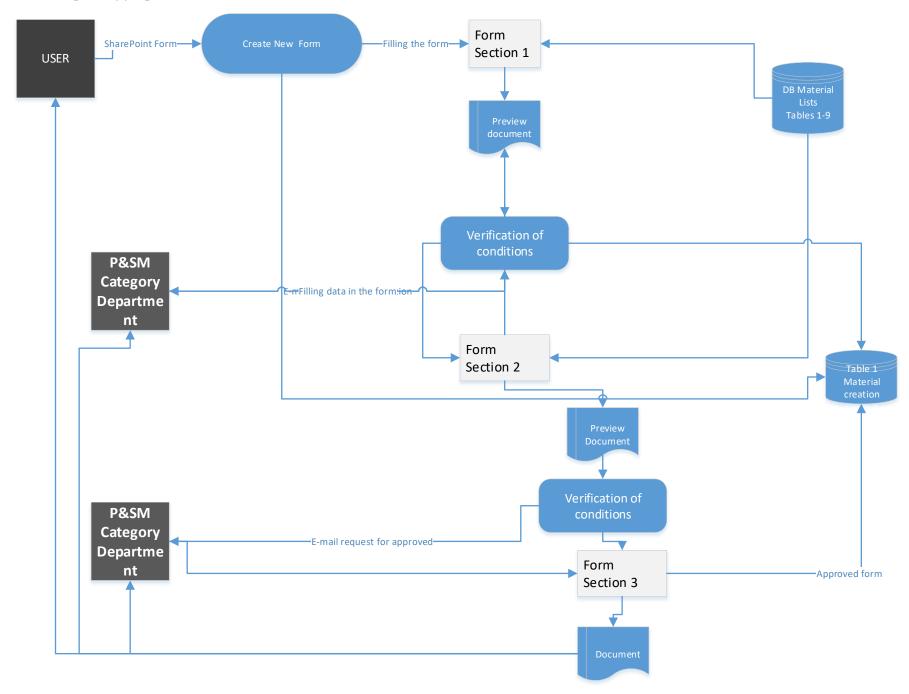
General Logic Mapping for Material Creation Process



Section 1 To be completed by Requestor

