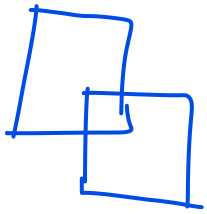
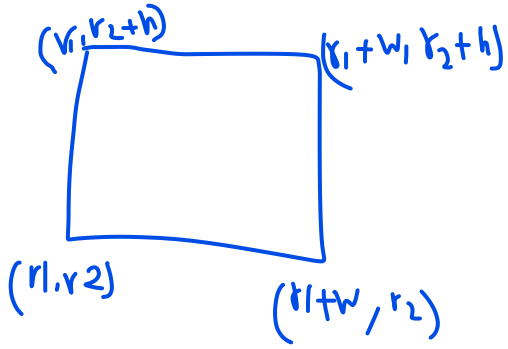
25X25 VLSI Chip

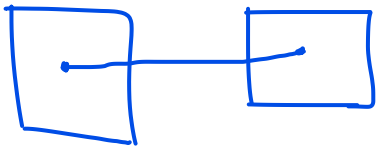
Constraints
1. Area Minimization
2. No Overlapping
3. Wire Length Minimization



overlap



$r_1 \times r_2$ = Area



wiring distance