Virtuozzo

VMBus (Hyper-V) devices in QEMU/KVM

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About me

- with Virtuozzo (formerly Parallels, formerly SWSoft) since 2005
- in different roles including
 - large-scale automated testing development for container and hypervisor
 - proprietary Parallels hypervisor development
 - now: opensource QEMU/KVM-based Virtuozzo hypervisor development



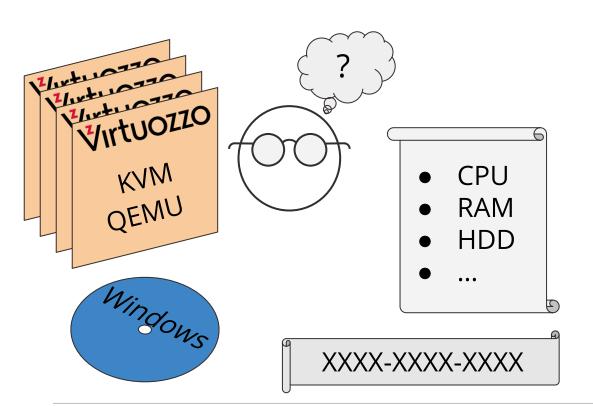
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- the only authoritative and up-to-date documentation is the code

Outline

- 1. Motivation
 - a. virtual h/w choice for Windows VM
- 2. Hyper-V / VMBus emulation
 - a. layers & components
 - b. implementation details
 - c. implementation status
- 3. Summary & outlook

Motivation

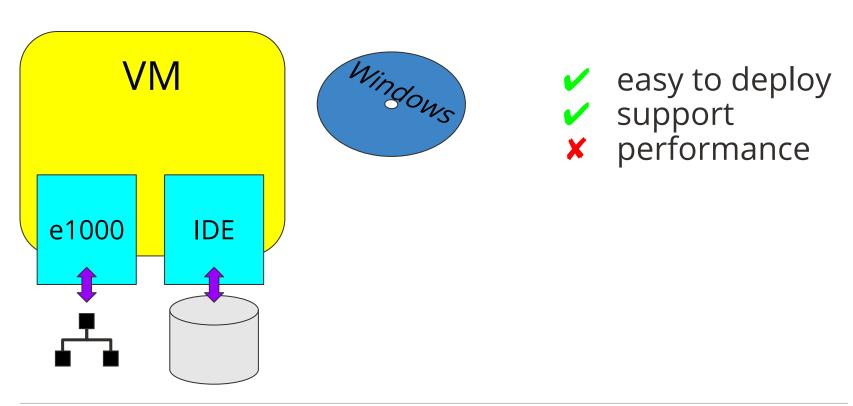


wanted:

- performanceeasy to deploy
- support

Virtuozzo

Choice #1: h/w emulation



Virtuozzo

Virtual machine ≠ physical machine

physical machine:

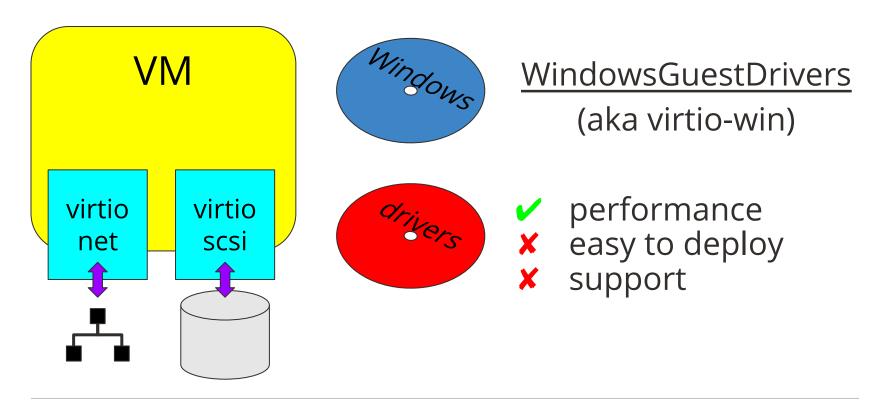
- all CPU and RAM is yours
- timing is (somewhat) predictable

virtual machine:

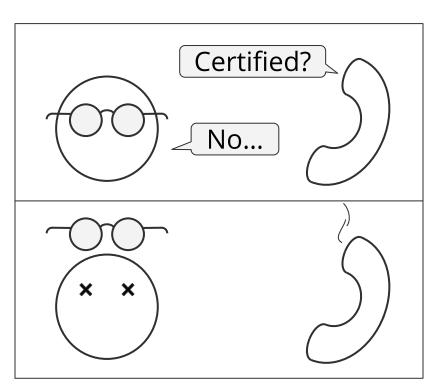
- can be preempted
- can be swapped out
- many things become expensive (APIC, I/O, MSRs, etc)

answer: paravirtualization

Choice #2: VirtIO



What's wrong with virtio-win?

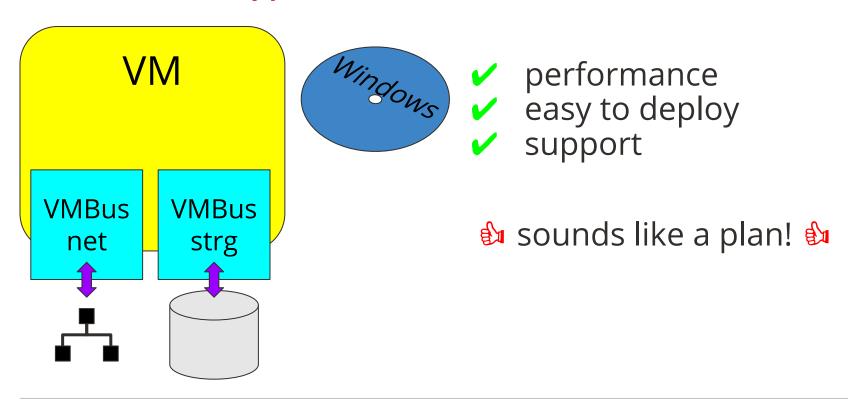


WHQL \Rightarrow SVVP \Rightarrow support

GPL & WHQL

in order to ship it, you need to own it

Choice #3: Hyper-V emulation



Hyper-V: how to?

- 1. Microsoft docs on GitHub
- 2. Linux guest code for Hyper-V (everything under CONFIG HYPERV)
- 3. trial & error
 - e.g. things work with Linux hyperv guest but break with Windows guest



Hyper-V paravirtualization

- previously implemented enlightenments
- management MSRs
- synthetic interrupt controller
- timers
- hypercalls
- VMBus
- devices

Hyper-V preexisting enlightenments

- management MSRs
 - GUEST OS ID
 - VP INDEX
- hypercall infrastructure
- scheduler
 - NOTIFY LONG SPIN WAIT hypercall
- LAPIC
 - MSR access to EOI / ICR / TPR
 - APIC assist page (aka pvEOI)



Hyper-V management MSRs

- reset
- panic
 - CRASH CTL, CRASH P0...P3 BSOD info
- VP RUNTIME



Hyper-V clocks

partition reference time: monotonic clock in 100ns ticks since boot

- time reference counter:
 - rdmsr HV_X64_MSR_TIME_REF_COUNT
 - 1 vmexit / clock read
 - no hardware requirements



Hyper-V clocks (cont'd)

- TSC reference page: similar to kvm_clock time = (scale * tsc) >> 64 + offset
 - no vmexits
 - invariant TSC req'd
 - one per VM
 - read consistency via seqcount
 - seqcount == 0 ⇒ fall-back to time ref count
 - no seqlock semantics ⇒ use fall-back on updates ⇒
 monotonicity with time ref count req'd

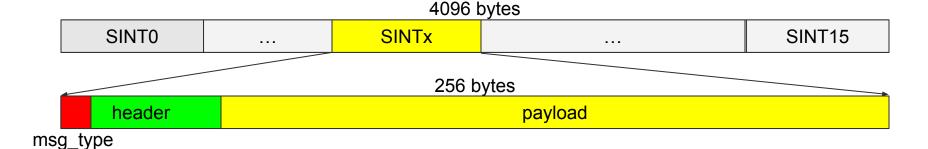


Hyper-V SynIC (synthetic interrupt controller)

- LAPIC extension managed via MSRs
- 16 SINT's per vCPU
- AutoEOI support
 - incompatible with APICv
- KVM_IRQ_ROUTING_HV_SINT
 - $\overline{GSI} \rightarrow \overline{VCPU}\#$, SINT#
- irqfd support
- KVM_EXIT_HYPERV(SYNIC) on MSR access



Hyper-V SynIC – message page



hypervisor post:

- msg_type: CAS

 TYPE NONE→TYPE NNN
- write payload
- deliver SINTx

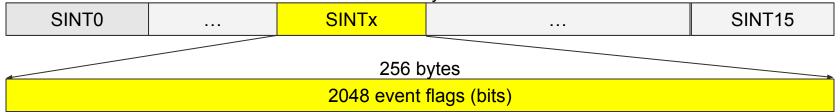
guest receive:

- read payload
- msg_type: atomic
 TYPE NNN→TYPE NONE
- EOI or EOM ⇒ eventfd



Hyper-V SynIC – event flags page

4096 bytes



hypervisor signal:

- event flag: *CAS* 0→1
- deliver SINTx

guest receive:

- event flag: *atomic* 1→0
- EOI or EŎM ⇒ eventfd



Hyper-V timers

- per vCPU: 4 timers × 2 MSRs (config, count)
- in partition reference time
- SynIC messages HVMSG TIMER EXPIRED
 - expiration time
 - delivery time
- in KVM ⇒ first to take message slot
- periodic / one-shot
- lazy (= discard) / period modulation (= slew)



Hyper-V hypercalls

extend existing implementation in KVM:

- new hypercalls
 - HVCALL POST MESSAGE
 - HVCALL SIGNAL EVENT
- pass-through to userspace
 - KVM EXIT HYPERV (HCALL)
- stub implementation in QEMU



Hyper-V VMBus

- announced via ACPI
- host-guest messaging connection
 - host → guest: SINT & message page
 - guest → host: POST MESSAGE hypercall
- used to
 - negotiate version and parameters
 - discover & setup devices
 - setup *channels*



Hyper-V VMBus channel

entity similar to VirtlO virtqueue

- descriptor rings akin to VirtIO vrings
- 1+ per device
- signaling:
 - host → guest: SINT & event flags page
 - guest → host: SIGNAL EVENT hypercall
- used for data transfer

Hyper-V VMBus devices

- util (shutdown, heartbeat, timesync, VSS, *etc*)
- storage
- net
- balloon

Firmware support

needed to boot off Hyper-V storage or network

- SeaBios
- OVMF

⇒ port over from kernel

Summary

- Hyper-V / VMBus emulation is a viable solution to make Windows guests' life on QEMU/KVM easier
- we have the groundwork in KVM and QEMU mostly complete
- the actual VMBus devices implementation is being worked on

Outlook

- performance measurement & tuning
- vhost integration
- AF VSOCK transport
- event logging
- debugging
- more devices
 - input
 - video

