

EECS 388
LABORATORY EXERCISE V

ANALOG-TO-DIGITAL CONVERTER

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LABORATORY OVERVIEW

This laboratory involves the writing, compiling, and demonstrating of a new task on the Tiva TM4C1294 evaluation board. This task reads the Analog-to-Digital Converter (ADC) on-board the evaluation board and display the read values using PuTTY.

BACKGROUND

UART and PuTTY will be used to print a ADC values once every second. PuTTY is prepared by setting the COM port to whichever COM port is being used on the workstation by the evaluation board, and setting the serial connection speed to 115200. This will open a terminal accepting output from programs being run on the evaluation board. UART works similarly to a standard `printf` statement in C syntax, and example use is as follows:

```
1 // Import APIs
2 #include "Drivers/UARTStdio_Initialization.h"
3 #include "Drivers/uartstdio.h"
4 // Initialize UART
5 UARTStdio_Initialization();
6 // Print to PuTTY
7 UARTprintf( "FreeRTOS Starting!\n" );
```

PROCEDURE & RESULTS

Design of the task `Task_ADC.c` is in three parts: (i) Initializing UART, (ii) Configuring the ADC, and (iii) Sampling, Reading, and Printing the ADC value. Overview of each part is below:

- (i) Initialization of UART is done via the following call:

```
1 UARTStdio_Initialization();
```

Now the program is prepared to print to PuTTY.

- (ii) Configuration of the ADC involves enabling the peripheral, and configuring and enabling the ADC sequence.

```
1 SysCtlPeripheralEnable( SYSCTL_PERIPH_ADC0 );
2
3 ADCSequenceConfigure( ADC0_BASE, 0, ADC_TRIGGER_PROCESSOR, 1 );
```

```

4     ADCSequenceStepConfigure( ADC0_BASE, 0, 0,
5                               ADC_CTL_IE | ADC_CTL_END | ADC_CTL_CH9 );
6
7     ADCSequenceEnable( ADC0_BASE, 0 );

```

Now the program is prepared to read potentiometer voltage on the board.

- (iii) Reading the ADC consists of triggering a sample sequence, waiting until said sequence is complete, reading the sequence values from the ADC, and printing to PuTTY. This is done via the following calls:

```

1     ADCProcessorTrigger(ADC0_BASE, 0);
2     while (!ADCIntStatus(ADC0_BASE, 0, false)) {
3     }
4     ADCSequenceDataGet(ADC0_BASE, 0, &ulValue);
5     UARTprintf( "ADC:%i\n", ulValue);

```

Note the sampled sequence is being set to a previously declared unsigned long variable by the name of `ulValue`.

Lastly, as all of part (iii) takes place within an infinite while loop, the task delays for one second before beginning at the top of the loop again using the following call:

```

1 vTaskDelay( pdMS_TO_TICKS(1000) );

```

ANALYSIS

The ADC on the Tiva TM4C1294 evaluation board generates 12-bits for a conversion, this information can be used to make additional sense of the this task's generated output in PuTTY. The range of possible values that the ADC can represent is computed as follows:

$$0 \leq x < 2^{12}$$

$$0 \leq x < 4096$$

It is clear that the lowest value the ADC can represent is 0, and the highest value is 4095, as only integer values are generated. With this in mind, one can expect PuTTY to read 0 when the potentiometer is turned all the way "down", and 4095 when the potentiometer is turned all the way "up", as well as every integer value between 0 and 4095 along the way.

CONCLUSIONS

This laboratory exercises observes the reading of the ADC on the Tiva TM4C1294 evaluation board once per second, without any interaction from a user. The implemented logic is broken into easily understandable steps and is consequently both appropriately modular and effective. Thus, this logic could be extended to include interaction from the user, or perhaps reading of an additional peripheral as a means of introducing new complexities to the reading and processing of the ADC.

CODE

```
1 #include <stddef.h>
2 #include <stdbool.h>
3 #include <stdint.h>
4 #include <stdarg.h>
5
6 #include "FreeRTOS.h"
7 #include "task.h"
8
9 #include "Drivers/UARTStdio_Initialization.h"
10 #include "Drivers/uartstdio.h"
11
12 #include "Driverlib/adc.h"
13 #include "Driverlib/sysctl.h"
14 #include "inc/hw_memmap.h"
15
16 extern void Task_ADC( void *pvParameters ) {
17
18     unsigned long ulValue;
19
20     //
21     // Initialize UART
22     //
23     UARTStdio_Initialization();
24
25     //
26     // Configure ADC<9> to read potentiometer voltage.
27     //
28     SysCtlPeripheralEnable( SYSCTL_PERIPH_ADC0 );
29
```

```
30  ADCSequenceConfigure( ADC0_BASE, 0, ADC_TRIGGER_PROCESSOR, 1 );
31  ADCSequenceStepConfigure( ADC0_BASE, 0, 0,
32                          ADC_CTL_IE | ADC_CTL_END | ADC_CTL_CH9 );
33
34  ADCSequenceEnable( ADC0_BASE, 0 );
35
36  while ( 1 ) {
37      //
38      // Trigger the sample sequence.
39      //
40      ADCProcessorTrigger(ADC0_BASE, 0);
41
42      //
43      // Wait until the sample sequence has completed.
44      //
45      while (!ADCIntStatus(ADC0_BASE, 0, false)) {
46
47      }
48
49      //
50      // Read the value from the ADC.
51      //
52      ADCSequenceDataGet(ADC0_BASE, 0, &ulValue);
53
54      //
55      // Print
56      //
57      UARTprintf( "ADC:%i\n", ulValue);
58
59      //
60      // Delay
61      //
62      vTaskDelay( pdMS_TO_TICKS(1000) );
63  }
64 }
```