

$$\overline{R} = a_1 \cdot \overline{R_1} + a_2 \cdot \overline{R_2} + a_3 \cdot \overline{R_3}$$

$$R^* = \overline{a_1} \cdot R_1 + \overline{a_2} \cdot R_2 + \overline{a_3} \cdot R_3$$

R * reads as R dual

$$R = 0.48107 \cdot (r_7^1 \cdot 2^7 + ... + r_0^1 \cdot 2^0)$$

$$+ 0.35857 \cdot (r_7^3 \cdot 2^7 + ... + r_0^3 \cdot 2^0)$$

$$+ 0.16035 \cdot (r_7^2 \cdot 2^7 + ... + r_0^2 \cdot 2^0)$$

$$\overline{R} = 0.48107 \cdot \begin{vmatrix} r_7^1 \\ r_6^1 \\ r_5^1 \\ r_5^1 \\ r_5^1 \\ r_1^2 \\ r_1^3 \\ r_1^2 \\ r_0^1 \end{vmatrix} + 0.35857 \cdot \begin{vmatrix} r_7^3 \\ r_6^3 \\ r_5^3 \\ r_5^3 \\ r_5^3 \\ r_5^3 \\ r_5^3 \\ r_6^3 \\ r_1^3 \\ r_1^3 \\ r_1^3 \\ r_1^2 \\ r_1^3 \\ r_1^2 \\ r_1^3 \\ r_1^2 \\ r_1^3 \\ r_1^3 \\ r_1^3 \\ r_2^2 \\ r_1^3 \\ r_1^3 \\ r_1^3 \\ r_1^2 \\ r_1^2 \\ r_1^2 \\ r_1^3 \\ r_$$

$$R^* = r_7^1 r_6^1 r_5^1 r_4^1 r_3^1 r_7^1 r_1^1 r_0^1 r_7^3 r_6^3 r_5^3 r_4^3 r_3^3 r_7^3 r_1^3 r_0^2 r_7^2 r_6^2 r_5^2 r_4^2 r_3^2 r_7^2 r_1^2 r_0^2 \qquad \text{call it} \quad R_{BIG}$$

$$R_1 = 128_{10} = 100000000_2$$

$$R_2 = 53_{10} = 00110101_2$$

$$R_3 = 127_{10} = 011111111_2$$

$$R_{IDEAL} = 0.48 \cdot 128 + 0.36 \cdot 127 + 0.16 \cdot 53$$

= 115.64
= 01110011₂

$$R_{BIG} = R_1 R_3 R_2 = 10000000011111111100110101, R = 10000100_2 = 132$$

24bit

$$R = 10000100_2 = 132$$

not good approx.

Ex2

Px0, three surrounding red pixels

$$R_1 = 128_{10} = 10000000_2$$

 $R_2 = 53_{10} = 00110101_2$
 $R_3 = 127_{10} = 011111111_2$

$$R_{IDEAL} = 0.48 \cdot 128 + 0.36 \cdot 127 + 0.16 \cdot 53$$

= 115.64
= 01110011₂

Let
$$R_{12} = \overline{(R_1 >> 2) \oplus (R_2 << 1)} = 01110100_2$$
,

where $\stackrel{>>}{_1}$ and $\stackrel{<<}{_1}$ - logical shift with '1' fill

⊕ - XNOR boolean operator

- better approx.

Let
$$R = r_7 r_6 r_5 r_4 r_3 r_2 r_1 r_0$$

Define Я as:
$$\mathcal{H} = r_4 r_5 r_6 r_7 r_0 r_1 r_2 r_3$$
 And define reversal operator Я as: $\mathcal{H}(R) = \mathcal{H}(R)$

Ex3 From Ex2
$$R_{12} = 01110100_2$$

Let
$$H_{12} = 11100010_2$$

$$\overline{\mathcal{H}_{12} \oplus (R_3 << 1)} = 11100010_2$$

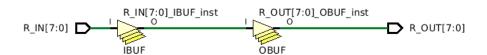
$$\mathcal{A}\left(\overline{\mathcal{A}_{12}\oplus(R_3 \stackrel{<<}{1})}\right) = 01110100_2 = R_{12}$$
 - identity operator

reverser 8b.vhd

```
entity reverser_8b is
  Port ( R_IN : in std_logic_vector(7 downto 0);
    R_OUT : out std_logic_vector(7 downto 0));
end reverser_8b;

architecture Dataflow of reverser_8b is

begin
    R_OUT(7) <= R_IN(4);
    R_OUT(6) <= R_IN(5);
    R_OUT(5) <= R_IN(6);
    R_OUT(4) <= R_IN(7);
    R_OUT(3) <= R_IN(0);
    R_OUT(2) <= R_IN(1);
    R_OUT(1) <= R_IN(2);
    R_OUT(0) <= R_IN(3);
end Dataflow;</pre>
```



Recall
$$R^* = r_7^1 r_6^1 r_7^1 r_4^1 r_1^3 r_7^1 r_1^1 r_0^1 r_7^3 r_6^3 r_5^3 r_4^3 r_3^3 r_1^3 r_0^3 r_7^2 r_6^2 r_5^2 r_4^2 r_3^2 r_2^2 r_1^2 r_0^2$$
 call it R_{BIG}

Therefore R_{BIG} is a finite space and dual space R^* can be viewed as a rectangle or a plane consisting of 2^{24} elements in it arranged in a lattice pattern.

Thus we can define our binary operations such as AND,OR, etc as two different things:

- 1) Means of traversing the grid by flipping some of the bits
- 2) vectors

We choose one boolean operator to measure similarity and go from there. Let it be XNOR. Other operations will have to do with adjusting weights as we arrived here from the weighted sum and the end goal is weighted average.

Ex4

$$B=011_2$$
 $A=000_2$
 $A=000_2$

Can try

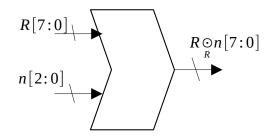
Actual space such be from on the space of the space

Can try different geometries/origin also. Actual notion of distance in this space may depend on the amount of such binary operations required to arrive from origin to any other point. Remember that we are looking for a point in this grid, that surely does exist, that is the closest approximation to the weighted average. Also remember once we contrived this space to be linear and dual by assuming 0.48 0.36 and 0.16 real numbers are independent (orthogonal) thus their products are equal to 0, all normal algebra went out the window.

Therefore \vec{v}, \vec{w} are orthogonal (hint from linear algebra) and these two operations (right shift with '1' fill and XNOR) span the entire lattice.

Let
$$R = r_7 r_6 r_5 r_4 r_3 r_2 r_1 r_0$$

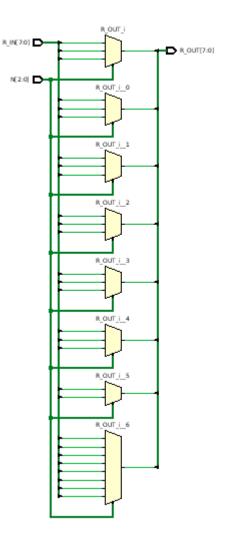
Define RPUSH operator as:



where n – control signal choosing which bit to push to the top position

rpush 8b.vhd

```
entity rpushn_8b is
  Port ( R_IN : in std_logic_vector(7 downto 0);
    N : in std_logic_vector(2 downto 0);
    R_OUT : out std_logic_vector(7 downto 0));
end rpushn_8b;
architecture Dataflow of rpushn_8b is
begin
    with N select
        R_OUT(7) <= R_IN(0) when "000",
          R_IN(1) when "001",
          R_IN(2) when "010",
          R_IN(3) when "011",
          R_IN(4) when "100",
          R_IN(5) when "101",
          R_IN(6) when "110",
          R_IN(7) when others;
      with N select
        R_OUT(6) <= R_IN(6) when "111",
          R_IN(7) when others;
      with N select
        R_OUT(5) <= R_IN(5) when "111",
          R_IN(5) when "110",
          R_IN(6) when others;
      with N select
        R_OUT(4) <= R_IN(4) when "111",
          R_IN(4) when "101",
          R_IN(5) when others;
      with N select
        R_{OUT}(3) \le R_{IN}(3) when "111",
          R_IN(3) when "100",
          R_IN(4) when others;
      with N select
        R_OUT(2) <= R_IN(2) when "111",
          R_IN(2) when "011",
          R_IN(3) when others;
      with N select
        R_OUT(1) <= R_IN(1) when "111",
          R_IN(1) when "010",
          R_IN(2) when others;
      with N select
        R_OUT(0) <= R_IN(0) when "111",
          R_IN(0) when "001",
          R_IN(1) when others;
end Dataflow:
```



Ex6

Let
$$R = r_7 r_6 r_5 r_4 r_3 r_2 r_1 r_0$$

 $R \underset{R}{\odot} 3 = r_3 r_7 r_6 r_5 r_4 r_2 r_1 r_0$

Ex7

$$\vec{u} = A \odot 0 = \vec{0}$$

$$A = 000_{2}$$

RPUSH operator clearly doesn't do anything for the origin $\begin{tabular}{l}$