

TDM3452

DESCRIPTION

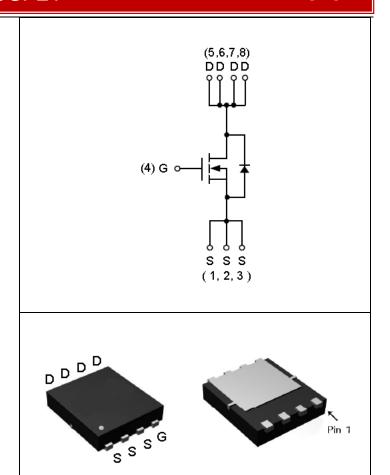
The TDM3452 uses advanced trench technology to provide excellent RDS(ON) and low gate charge. This device is suitable for use as a load switch or in PWM applications.

GENERAL FEATURES

- RDS(ON) < 5.1mΩ @ VGS=4.5V
 RDS(ON) < 3.1mΩ @ VGS=10V
- High Power and current handling capability
- Lead free product is available
- Surface Mount Package

Application

- PWM applications
- Load switch
- Power management



DFN5x6-8

ABSOLUTE MAXIMUM RATINGS(T_A=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	30	V
Gate-Source Voltage	VGS	<u>+</u> 20	V
Drain Current @ Continuous(Note 1)	ID (Tc=25℃)	80	А
	ID (Tc=100°C)	60	А
Drain Current @ Current-Pulsed (Note 2)	IDM (Tc=25°С)	160	А
Drain Current @ Continuous(Note 1)	ID (TA=25°C)	23.5	А
	ID (TA=100°C)	18.8	А
Maximum Power Dissipation (T _A =25 °C)	PD	2.5	W
Maximum Operating Junction Temperature	Тл	150	$^{\circ}$
Storage Temperature Range	Тѕтб	-55 To 150	$^{\circ}$
THERMAL CHARACTERISTICS	•		•
Thermal Resistance, Junction-to-Ambient (Note 2)	Rөла	50	°C/W



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ELECTRICAL CHARACTERISTICS (TA=25 °C unless otherwise noted)

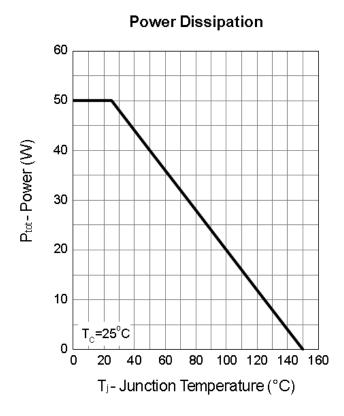
Parameter	Symbol	Condition	Min	Тур	Max	Unit
OFF CHARACTERISTICS						-
Drain-Source Breakdown Voltage	BVDSS	Vgs=0V ID=250μA	30	-	-	V
Zero Gate Voltage Drain Current	IDSS	V _{DS} =24V,V _{GS} =0V	-	-	1	μА
Gate-Body Leakage Current	Igss	Vgs=±20V,Vps=0V	-	-	±100	nA
ON CHARACTERISTICS (Note 3)						
Gate Threshold Voltage	VGS(th)	V _{DS} =VGS,I _D =250μA	1.4	1.7	2.4	V
Drain-Source On-State Resistance	Rds(on)	Vgs=4.5V, ID=15A	-	4.2	5.1	mΩ
		Vgs=10V, ID=20A	-	2.7	3.1	mΩ
DYNAMIC CHARACTERISTICS (Note4)	- 1		•	•	•	
Input Capacitance	Ciss	V _{DS} =15V,V _{GS} =0V, F=1.0MHz	-	1345	-	PF
Output Capacitance	Coss		-	908	-	PF
Reverse Transfer Capacitance	Crss		-	60	-	PF
SWITCHING CHARACTERISTICS (Note 4)						-
Turn-on Delay Time	td(on)	V _{DS} =12V, R _L =2.1Ω, V _{GEN} =10V,	-	14.3	-	nS
Turn-on Rise Time	tr	Rg=2.9 Ω ID=5.7A	-	26	-	nS
Turn-Off Delay Time	td(off)		-	24	-	nS
Turn-Off Fall Time	t f		-	4.4	-	nS
Total Gate Charge	Qg	V _{DS} =15V,I _D =20A,V _{GS} =4.5V	-	9.2	-	nC
Gate-Source Charge	Qgs		-	6	-	nC
Gate-Drain Charge	Qgd		-	2	-	nC
Body Diode Reverse Recovery Time	Trr	I _F =20A, dI/dt=100A/μs	-	21	-	nS
Body Diode Reverse Recovery Charge	Qrr		-	6.3	-	nC
DRAIN-SOURCE DIODE CHARACTERISTIC	:S		•	•		•
Diode Forward Voltage (Note 3)	VsD	VGS=0V,IS=20A	-	0.81	1.1	V

NOTES:

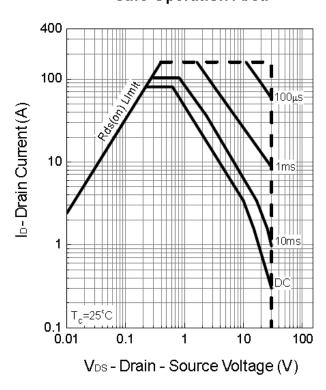
- 1. continue current is limited by bonding wire.
- 2. Pulse width limited by max. junction temperature.
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production testing



Typical Operating Characteristics

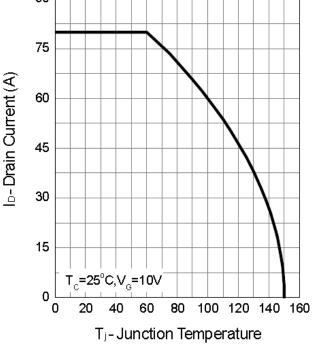


Safe Operation Area

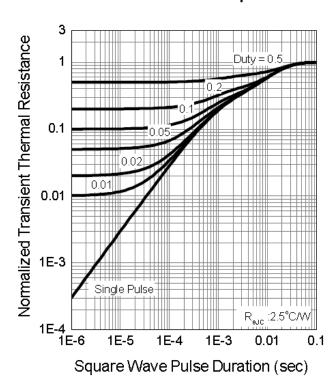




Drain Current



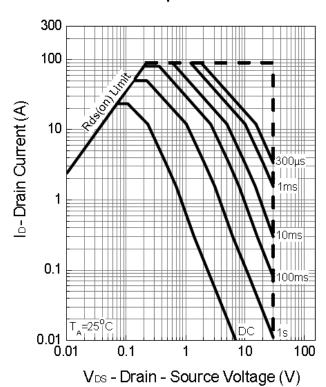
Thermal Transient Impedance



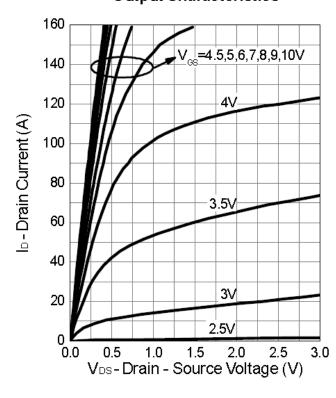


Typical Operating Characteristics(Cont.)

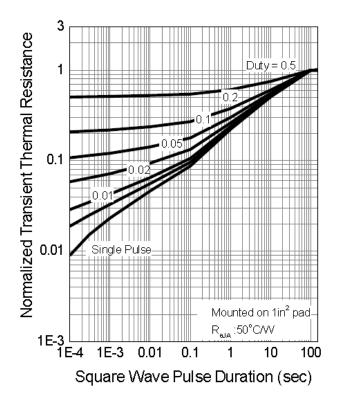
Safe Operation Area



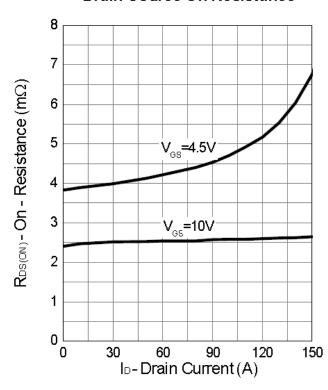
Output Characteristics



Thermal Transient Impedance



Drain-Source On Resistance



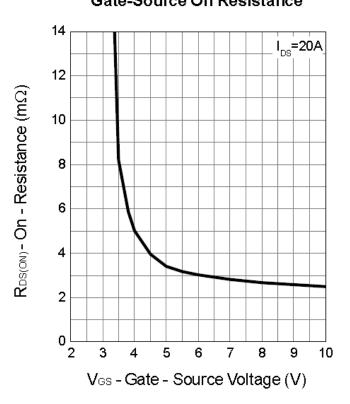
TDM3452



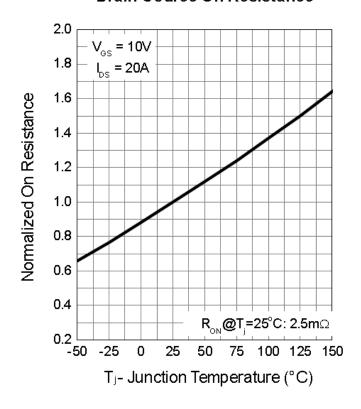
N-Channel Enhancement Mode MOSFET

Typical Operating Characteristics (Cont.)

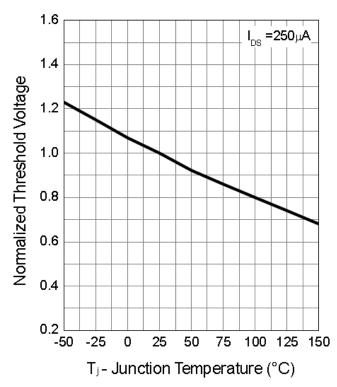
Gate-Source On Resistance



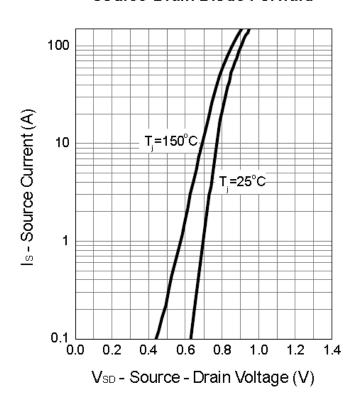
Drain-Source On Resistance



Gate Threshold Voltage



Source-Drain Diode Forward



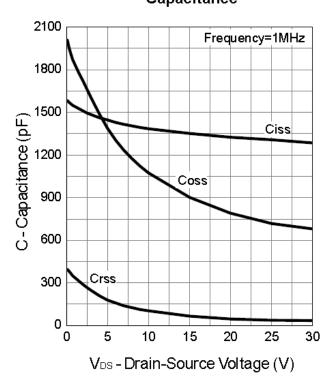
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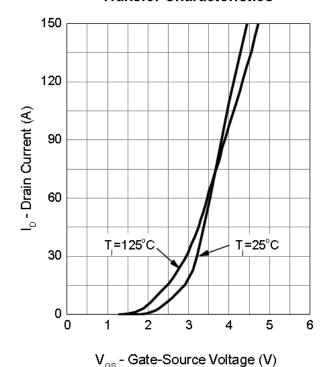
N-Channel Enhancement Mode MOSFET

Typical Operating Characteristics (Cont.)

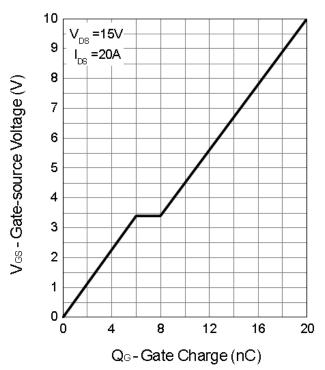
Capacitance



Transfer Characteristics



Gate Charge



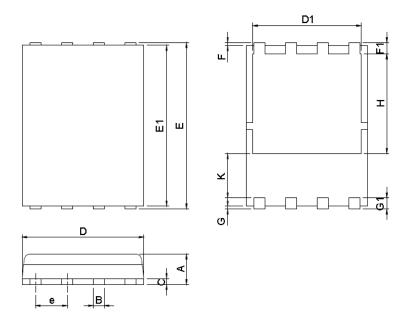
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N-Channel Enhancement Mode MOSFET

Package Information

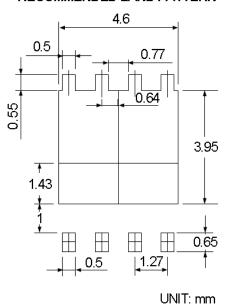
DFN5*6-8 Package



Ş	DFN5x6-8						
SYMBOL	MILLIM	ETERS	INCHES				
6	MIN.	MAX.	MIN.	MAX.			
Α	0.90	1.20	0.035	0.047			
В	0.3	0.51	0.012	0.020			
С	0.19	0.25	0.007	0.010			
D	4.80	5.30	0.189	0.209			
D1	4.00	4.40	0.157	0.173			
Е	5.90	6.20	0.232	0.244			
E1	5.50	5.80	0.217	0.228			
е	1.27 BSC		0.050 BSC				
F	0.05	0.30	0.002	0.012			
F1	0.35	0.75	0.014	0.030			
G	0.05	0.30	0.002	0.012			
G1	0.35	0.75	0.014	0.030			
Н	3.34	3.9	0.131	0.154			
K	0.762	•	0.03	-			

Note : 1.Dimension D, D1,D2 and E1 do not include mold flash or protrusions. Mold flash or protrusions shall not exceed 10 mil.

RECOMMENDED LAND PATTERN





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Design Notes