Pins BGA100	Pins LQFP64	Pins MCB32 LQFP100	Pin name	Typ Pin (1). Type (1)	I / O Level (2)	Hauptfunktion nach Reset. (3). Main function(3) (after reset)	Alternative Funktion (4). Alternate functions(4) Default	Remap	Function	Devices	Port Name Nummer
			PA								***Port A***
G2	14	23	PA0-WKUP	I/O	-	PA0	WKUP/USART2_CTS(7) ADC12_IN0/TIM2_CH1_ETR TIM5_CH1/ ETH_MII_CRS_WKUP	-	Wakeup	Switch Wakeup	Port A 0
H2	15	24	PA1	I/O	-	PA1	USART2_RTS(7) / ADC12_IN1 / (7) ETH_MII_RX_CLK/ ETH_RMII_REF_CLK	-	RMII_REF_CLK	Ethernet LAN	Port A 1
J2	16	25	PA2	I/O	-	PA2	USART2_TX(7) / TIM5_CH3/ ADC12_IN2 / TIM2_CH3 (7) / ETH_MII_MDIO / ETH_RMII_MDIO	-	RMII_MDIO	Ethernet LAN	Port A 2
K2	17	26	PA3	I/O	-	PA3	USART2_RX(7) / TIM5_CH4 / ADC12_IN3 / TIM2_CH4(7) / ETH_MII_COL	-	Free_1.	-	Port A 3
G3	20	29	PA4	I/O	-	PA4	SPI1_NSS(7) / DAC_OUT1 / USART2_CK(7) / ADC12_IN4	SPI3_NSS/I2S3_WS	Free_2.	-	Port A 4
H3	21	30	PA5	I/O	-	PA5	(7) DAC_OUT2 / ADC12_IN5	-	SPI1_SCK	SD Card CLK	Port A 5
J3	22	31	PA6	I/O	-	PA6	SPI1_MISO(7) / ADC12_IN6 / TIM3_CH1(7)	TIM1_BKIN	SPI1_MISO	SD Card DAT0	Port A 6
K3	23	32	PA7	I/O	-	PA7	SPI1_MOSI(7) / ADC12_IN7 / (7) ETH_MII_RX_DV(8) / ETH_RMII_CRS_DV	TIM1_CH1N	SPI1_MOSI	SD Card CMD	Port A 7
D9	41	67	PA8	I/O	FT	PA8	USART1_CK/OTG_FS_SOF / TIM1_CH1 (8) / MCO	-	MCO	Ethernet LAN	Port A 8
C9	42	68	PA9	I/O	FT	PA9	USART1_TX(7)/ TIM1_CH2(7) / OTG_FS_VBUS	-	FS_VBUS	USB OTG/Device	Port A 9
D10	43	69	PA10	I/O	FT	PA10	(7) TIM1_CH3(7)/OTG_FS_ID	-	FS_ID	USB OTG	Port A 10
C10	44	70	PA11	I/O	FT	PA11	USART1_CTS / CAN1_RX / TIM1_CH4(7)/OTG_FS_DM	-	FS_DM	USB Data HOST/OTG/Device	Port A 11
B10	45	71	PA12	I/O	FT	PA12	USART1_RTS / OTG_FS_DP / CAN1_TX(7) / TIM1_ETR(7)	-	FS_DP		Port A 12
A10	46	72	PA13	I/O	FT	JTMS-SWDIO	-	PA13	JTAG_TMS	JTAG	Port A 13
A9	49	76	PA14	I/O	FT	JTCK-SWCLK	-	PA14	JTAG_TCLK	JTAG	Port A 14
A8	50	77	PA15	I/O	FT	JTDI	SPI3_NSS / I2S3_WS	TIM2_CH1_ETR / PA15 SPI1_NSS	JTAG_TDI	JTAG	Port A 15
			PB								***Port B***
J4	26	35	PB0	I/O	-	PB0	ADC12_IN8/TIM3_CH3/ ETH_MII_RXD2(8)	TIM1_CH2N	Free_3.	-	Port B 0
K4	27	36	PB1	I/O	-	PB1	ADC12_IN9/TIM3_CH4(7)/ ETH_MII_RXD3(8)	TIM1_CH3N	Free_4.	-	Port B 1
G5	28	37	PB2	I/O	FT	PB2/BOOT1	-	-	BOOT1	Jumper BOOT1	Port B 2
A7	55	89	PB3	I/O	FT	JTDO	SPI3_SCK / I2S3_CK	PB3 / TRACESWO/ TIM2_CH2 / SPI1_SCK	JTAG_TDO	JTAG	Port B 3
A6	56	90	PB4	I/O	FT	NJTRST	SPI3_MISO	PB4 / TIM3_CH1/ SPI1_MISO	JTAG_TRST	JTAG	Port B 4
C5	57	91	PB5	I/O	-	PB5	I2C1_SMBA / SPI3_MOSI / ETH_MII_PPS_OUT / I2S3_SD ETH_RMII_PPS_OUT	TIM3_CH2/SPI1_MOSI/ CAN2_RX	Free_5.	-	Port B 5
B5	58	92	PB6	I/O	FT	PB6	I2C1_SCL(7) /TIM4_CH1(7)	USART1_TX/CAN2_TX	USART1_TX	UART1	Port B 6
A5	59	93	PB7	I/O	FT	PB7	I2C1_SDA(7) /TIM4_CH2(7)	USART1_RX	USART1_RX	UART1	Port B 7
B4	61	95	PB8	I/O	FT	PB8	TIM4_CH3(7) / ETH_MII_TXD3	I2C1_SCL/CAN1_RX	I2C1_SCL	24C01,STMPE811	Port B 8
A4	62	96	PB9	I/O	FT	PB9	TIM4_CH4(7)	I2C1_SDA / CAN1_TX	I2C1_SDA	24C01,STMPE811	Port B 9
J7	29	47	PB10	I/O	FT	PB10	(8) ETH_MII_RX_ER (7)	TIM2_CH3	Free_6.	-	Port B 10
K7	30	48	PB11	I/O	FT	PB11	I2C2_SDA(8)/USART3_RX(7)/ ETH_MII_TX_EN/ ETH_RMII_TX_EN	TIM2_CH4	RMII_TXEN	Ethernet LAN	Port B 11
K8	33	51	PB12	I/O	FT	PB12	SPI2_NSS(8) /I2S2_WS(8) / I2C2_SMBA(8) / USART3_CK(7) / TIM1_BKIN(7) / CAN2_RX/ ETH_MII_TXD0/ ETH_RMII_TXD0	-	RMII_TXD0	Ethernet LAN	Port B 12
J8	34	52	PB13	I/O	FT	PB13	SPI2_SCK(8) / I2S2_CK(8) / USART3_CTS(7) / TIM1_CH1N/CAN2_TX/ ETH_MII_TXD1/ ETH_RMII_TXD1	-	RMII_TXD1	Ethernet LAN	Port B 13
H8	35	53	PB14	I/O	FT	PB14	SPI2_MISO(8) / TIM1_CH2N / USART3_RTS (7)	-	Free_7.	-	Port B 14
G8	36	54	PB15	I/O	FT	PB15	(8) TIM1_CH3N(7)	-	Free_8.	-	Port B 15
			PC								***Port C***
F1	8	15	PC0	I/O	-	PC0	ADC12_IN10	-	Free_9.	-	Port C 0
F2	9	16	PC1	I/O	-	PC1	ADC12_IN11 / ETH_MII_MDC / ETH_RMII_MDC	-	RMII_MDC	Ethernet LAN	Port C 1
E2	10	17	PC2	I/O	-	PC2	ADC12_IN12 / ETH_MII_TXD2	-	Free_10.	-	Port C 2
F3	11	18	PC3	I/O	-	PC3	ADC12_IN13 / ETH_MII_TX_CLK	-	Free_11.	-	Port C 3
G4	24	33	PC4	I/O	-	PC4	ADC12_IN14 / ETH_MII_RXD0(8) / ETH_RMII_RXD0	-	ADC14	Volume VR1	Port C 4
H4	25	34	PC5	I/O	-	PC5	ADC12_IN15 / ETH_MII_RXD1(8) / ETH_RMII_RXD1	-	GPIO Out	SD Card / CD(CS#)	Port C 5

Pins MCB32			Pin name	Typ Pin (1). Type (1)	I / O Level (2)	Hauptfunktion nach Reset. (3). Main function(3) (after reset)	Alternative Funktion (4). Alternate functions(4)				
BGA100	LQFP64	LQFP100					Default	Remap	Function	Devices	Port Name Nummer
F10	37	63	PC6	I/O	FT	PC6	I2S2_MCK/	TIM3_CH1	Free_12.	-	Port C 6
E10	38	64	PC7	I/O	FT	PC7	I2S3_MCK	TIM3_CH2	Free_13.	-	Port C 7
F9	39	65	PC8	I/O	FT	PC8	-	TIM3_CH3	GPIO Out	GLCD CS#	Port C 8
E9	40	66	PC9	I/O	FT	PC9	-	TIM3_CH4	HOST_EN	USB HOST/OTG	Port C 9
B9	51	78	PC10	I/O	FT	PC10	UART4_TX	USART3_TX/ SPI3_SCK/I2S3_CK	SPI3_SCK	GLCD WR#/SCL	Port C 10
B8	52	79	PC11	I/O	FT	PC11	UART4_RX	USART3_RX/ SPI3_MISO	SPI3_MISO	GLCD SDO	Port C 11
C8	53	80	PC12	I/O	FT	PC12	UART5_TX	USART3_CK/ SPI3_MOSI/I2S3_SD	SPI3_MOSI	GLCD SDI	Port C 12
A2	2	7	PC13	I/O	-	(6)	TAMPER-RTC (PC13-TAMPER- RTC(5))	-	Tamper	Switch Tamper	Port C 13
A1	3	8	PC14	I/O	-	(6)	OSC32_IN (PC14- OSC32_IN(5))	-	OSC32_IN	RTC X-TAL	Port C 14
B1	4	9	PC15	I/O	-	(6)	OSC32_OUT (PC15- OSC32_OUT(5))	-	OSC32_OUT	RTC X-TAL	Port C 15
			PD								***Port D***
-	-	81	PD0	I/O	FT	PD0	-	OSC_IN(9)/CAN1_RX	Free_14.	-	Port D 0
-	-	82	PD1	I/O	FT	PD1	-	OSC_OUT(9)/CAN1_TX	Free_15.	-	Port D 1
B7	54	83	PD2	I/O	FT	PD2	TIM3_ETR / UART5_RX	0	Free_16.	-	Port D 2
C7	-	84	PD3	I/O	FT	PD3	-	USART2_CTS	Free_17.	-	Port D 3
D7	-	85	PD4	I/O	FT	PD4	-	USART2_RTS	Free_18.	-	Port D 4
B6	-	86	PD5	I/O	FT	PD5	-	USART2_TX	USART2_TX	UART2(ISP)	Port D 5
C6	-	87	PD6	I/O	FT	PD6	-	USART2_RX	USART2_RX	UART2(ISP)	Port D 6
D6	-	88	PD7	I/O	FT	PD7	-	USART2_CK	GPIO Out	GLCD BL LED	Port D 7
K9	-	55	PD8	I/O	FT	PD8	-	USART3_TX/ ETH_MII_RX_DV/ ETH_RMII_CRS_DV	RMII_CRS_DV	Ethernet LAN	Port D 8
J9	-	56	PD9	I/O	FT	PD9	-	USART3_RX/ ETH_MII_RXD0/ ETH_RMII_RXD0	RMII_RXD0	Ethernet LAN	Port D 9
H9	-	57	PD10	I/O	FT	PD10	-	USART3_CK/ ETH_MII_RXD1/ ETH_RMII_RXD1	RMII_RXD1	Ethernet LAN	Port D 10
G9	-	58	PD11	I/O	FT	PD11	-	USART3_CTS/ ETH_MII_RXD2	GPIO Input	Joy Switch Up	Port D 11
K10	-	59	PD12	I/O	FT	PD12	-	TIM4_CH1 / USART3_RTS/ ETH_MII_RXD3	GPIO Input	Joy Switch Left	Port D 12
J10	-	60	PD13	I/O	FT	PD13	-	TIM4_CH2	GPIO Input	Joy Switch Down	Port D 13
H10	-	61	PD14	I/O	FT	PD14	-	TIM4_CH3	GPIO Input	Joy Switch Right	Port D 14
G10	-	62	PD15	I/O	FT	PD15	-	TIM4_CH4	GPIO Input	Joy Switch Select	Port D 15
			PE								***Port E***
D4	-	97	PE0	I/O	FT	PE0	TIM4_ETR		Free_19.	-	Port E 0
C4	-	98	PE1	I/O	FT	PE1	-		USB_OVRCR	USB HOST/OTG	Port E 1
A3	-	1	PE2	I/O	FT	PE2	TRACECK	-	Free_20.	-	Port E 2
B3	-	2	PE3	I/O	FT	PE3	TRACED0	-	GPIO Input	ADS7846 PEN#	Port E 3
C3	-	3	PE4	I/O	FT	PE4	TRACED1	-	GPIO Input	ADS7846 DOUT	Port E 4
D3	-	4	PE5	I/O	FT	PE5	TRACED2	-	GPIO Out	ADS7846 DIN	Port E 5
E3	-	5	PE6	I/O	FT	PE6	TRACED3	-	GPIO Out	ADS7846 CS#	Port E 6
H5	-	38	PE7	I/O	FT	PE7	-	TIM1_ETR	GPIO Out	ADS7846 DCLK	Port E 7
J5	-	39	PE8	I/O	FT	PE8	-	TIM1_CH1N	GPIO Out/Free_21	LED0	Port E 8
K5	-	40	PE9	I/O	FT	PE9	-	TIM1_CH1	GPIO Out/Free_22	LED1	Port E 9
G6	-	41	PE10	I/O	FT	PE10	-	TIM1_CH2N	GPIO Out/Free_23	LED2	Port E 10
H6	-	42	PE11	I/O	FT	PE11	-	TIM1_CH2	GPIO Out/Free_24	LED3	Port E 11
J6	-	43	PE12	I/O	FT	PE12	-	TIM1_CH3N	GPIO Out/Free_25	LED4	Port E 12
K6	-	44	PE13	I/O	FT	PE13	-	TIM1_CH3	GPIO Out/Free_26	LED5	Port E 13
G7	-	45	PE14	I/O	FT	PE14	-	TIM1_CH4	GPIO Out/Free_27	LED6	Port E 14
H7	-	46	PE15	I/O	FT	PE15	-	TIM1_BKIN	GPIO Out/Free_28	LED7	Port E 15
D5	60	94	BOOT0	I	-	BOOT0	-	-			- B 0