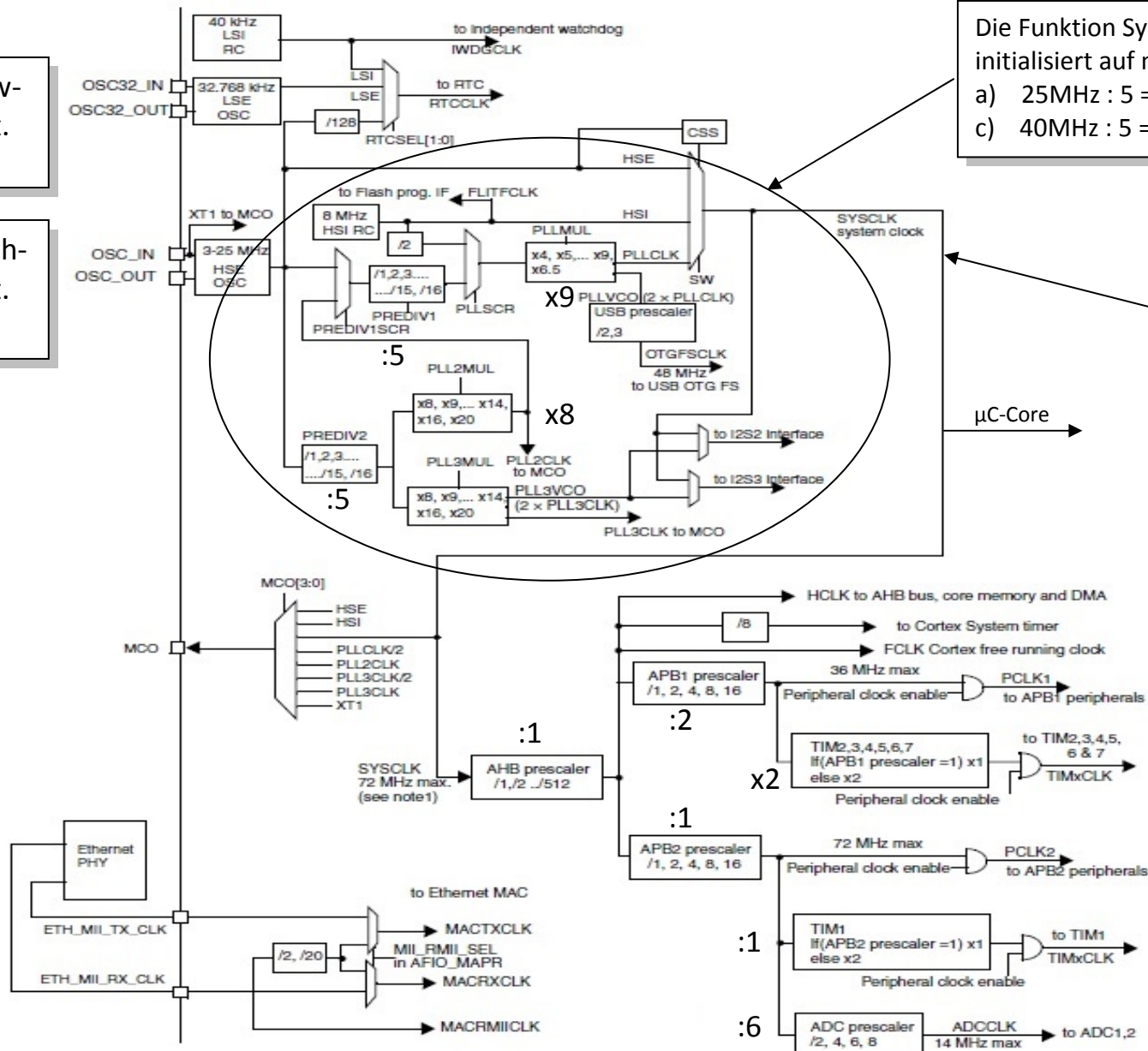




## Takteinstellung im STM32F107

Quarz Low-Speed ext.  
32'768Hz

Quarz High-Speed ext.  
25MHz



Die Funktion SystemInit()

initialisiert auf nachstehende Taktfrequenzen

- |                     |                     |
|---------------------|---------------------|
| a) 25MHz : 5 = 5MHz | b) 5MHz x 8 = 40MHz |
| c) 40MHz : 5 = 8MHz | d) 8MHz x 9 = 72MHz |

**Systemclock**      SYSCLK  
 HW-Reset = HSI = 8MHz  
 nach SystemInit() = 72MHz

**Advanced High Performance Bus AHB**  
 HCLK = 72MHz  
 SystemTimer = 72MHz (9MHz)  
 FCLK = 72MHz

**Advanced Peripheral Bus APB1**  
 PCLK1 = 36MHz  
 TIMxCLK = 72MHz

**Advanced Peripheral Bus APB2**  
 PCLK2 = 72MHz  
 TIMxCLK = 72MHz  
 ADCCLK = 12MHz