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Education

Franklin W. Olin College of Engineering

May 2026

Computer Science

Needham, MA

Redmond, WA

- 3.9/4.0 GPA
- Relevant Coursework: Advance Computer Architecture, Advance Algorithms, Advanced Systems Programming (Fall), Software Systems, Topology (Fall), Real Analysis, Abstract Algebra, Foundations of Computing, Linear Algebra, Multi-variable Calculus

Experience

Microsoft, SWE Intern

May 2024 - August 2024

Microsoft Edge

- Received "greatly exceeds all expectations" designation on final performance review (MS connect).
- Drove collaboration across Edge CI and Edge BDW to bring remote build acceleration to Official Edge Mac Builds.
- Learned about high-scale technologies including Kubernetes, Envoy, GRPC, and Azure ARM/Bicep as part of Official Edge Mac Work.
- Regularly deployed changes to production remote-execution endpoint for internal Edge/Chromium builds used by hundreds.
- Created a new framework for real-time performance telemetry for internal Edge and Chromium builds.
- Collaborated with PMs to create dashboards for developers to uncover insights into their personal build performance.

Olin College, SWE Research Intern

May 2023 - August 2023

Assistive Software Technology

Needham, MA

• Awarded summer stipend to improve spatial-localization software for vision-impaired individuals navigating indoor spaces.

Foursquare, SWE Intern

September 2021 - May 2022

Re:build & Data Platform

Seattle, WA

- · Accepted internship extension during gap-year to join new experimental Re:build engineering team.
- Shadowed in technical interview loops of two junior/senior SWE candidates.
- Led multi-month migration off of legacy python2 EMR steps, reducing usage by 95%+.
- Safely removed 183k LOC of dead or duplicated code as part of python2 migration.
- Executed technically complicated upgrade to AWS EMR@6.4.0 spanning six java repos, orchestration repo, and ops repo.

Foursquare, SWE Intern

June 2021 - September 2021

Attribution Platform

Seattle, WA

- Laid experimental groundwork for migration to new ARM64 Graviton EC2 instance types promising 40% reduction in compute costs.
- Automated production of six distribution flavors of AWS AMI images with Packer (AWS AL1 X86, AWS AL2 X86, and AWS AL2 ARM64).
- Executed migration to AWS EMR@5.33 (AL2) from blocked EMR@5.24 (AL1) release label.
- Reduced technical debt by refactoring deprecated Elasticity dependency in favor of Ruby AWS EMR SDK.

Floop, SWE Intern Floop Educational Technologies

February 2021 - April 2021

Seattle, WA

• Conducted case study for performance impact of code-splitting and lazy-loading with Webpack.

Events and Awards

Jane Street ETC (Electronic Trading Challenge)

July 2022 Seattle, WA

Live simulated market making competition

• 1st place in "Final Hour" scoring: \$1.5k total cash prize awarded to our team.

UWB Hackathon, Team Lead

April 2020

University of Washington-Bothell Hacks the Cloud Hackathon

Seattle, WA

Received "Most Innovative" award for proof-of-concept solution to quickly labeling images and training data-sets with Google Auto-ML.

Projects

RISC-V CPU Linux Microprocessor on FPGA

2024

• Implemented OS-capable RISC-V microcontroller specification using System Verilog and Xilinx development environment.

FPGA Digital Audio Processor

2024

- Implemented FIR (Finite Impulse Response) filter using System Verilog interfacing with audio over I2S2 serial protocol.
- Optimized area and space in multiplier accelerator using Radix-4 Booth Recoding and Adder Tree Reduction.

Twitter Scalding Open Source Contributions

2022

• Implemented spark backend pure counters API (#1992), automated publishing of release artifacts (#1985, #1976, #1975), enabled automated code coverage upload (#1981), removed deprecated scalding-jbdc (#1969), and migrated CI build (#1963, #1982).

Technologies

- Big Data: Apache Spark, Hadoop MR, HDFS.
- Cloud Compute: AWS EMR, Amazon Linux, AWS S3, AWS Lambda, Azure Bicep.
- Languages: Java, Scala, Typescript, Ruby, Python, C++.
- Infra: Packer, Docker, Terraform.
- CI/CD: Github Actions, Jenkins, Travis CI.
- Hardware: System Verilog, Verilog, Xilinx, RISC-V, Embedded Linux.