

# KEVIN MCFARLAND

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## Experience

### **VALIDATION ENGINEER & AUTOMATION EXECUTION ENGINEER | INTEL CORPORATION | JUNE 2015 - PRESENT**

- Performing customer product sample validation
- Finding and debugging of hardware and software issues.
- Developing and maintaining automation flows for volume test execution in post-silicon environments
- Script development for emulation (pre-silicon) automation testing environments
- Development of image creation and emulation continuous integration framework
- OS based Validation execution leader
- Development of Python REST API interaction mechanisms for test management, tracking, and modification
- Execute BIOS branch validation prior to release mainline integration

#### *Prior Focus Areas*

- Post-Silicon validation of virtualization functionality
- Post-Silicon validation of Release Acceptance Operating System installation and basic functionality

### **STUDENT CONSULTANT | CLEMSON COMPUTING & INFORMATION TECHNOLOGY | AUGUST 2012 - MAY 2015**

- Installing, troubleshooting, and repairing software
- Diagnosed hardware issues.
- Consulted with users about repair options.
- Received best customer reviews among co-workers.
- Commended for punctuality and dependability

### **INTERN | ITRON | SUMMER (MAY - AUGUST) 2014**

- Performed automated tests
- Managed, recorded, and organized automated test results
- Automated graph visualizations of test results for presentations
- Set up and maintained testing lab

## Education

### **BACHELOR OF SCIENCE | SPRING 2015 | CLEMSON UNIVERSITY**

- Major: Computer Engineering
- Curriculum Emphasis: Programming in C; Networking, Network Security, Machine Learning and Artificial Intelligence in various languages including OCaml, Flex & Bison, Prolog, MATLAB; Basic electrical circuits and electronics; Hardware logic design in VHDL

## Honors & Awards

**INTEL QUARTERLY ACHIEVEMENT AWARD, MULTIPLE RECOGNITIONS ON DEAN'S LIST & PRESIDENT'S LIST, BSA EAGLE SCOUT AWARD**