

Figura 13.13 Registos de memória distribuídos no Atmel AVR Mega 128

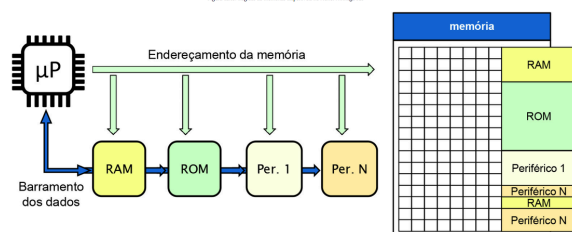


Figura 13.12 Mapa de memória e circuitos correspondentes

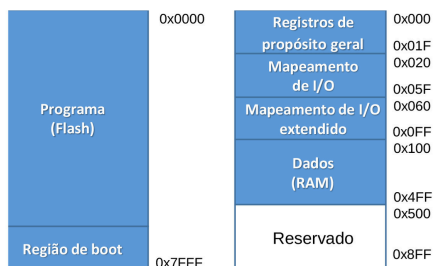
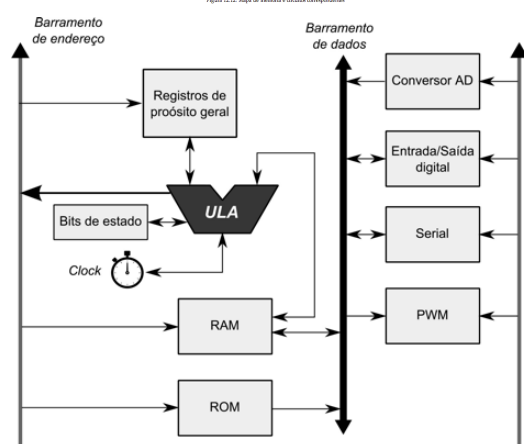
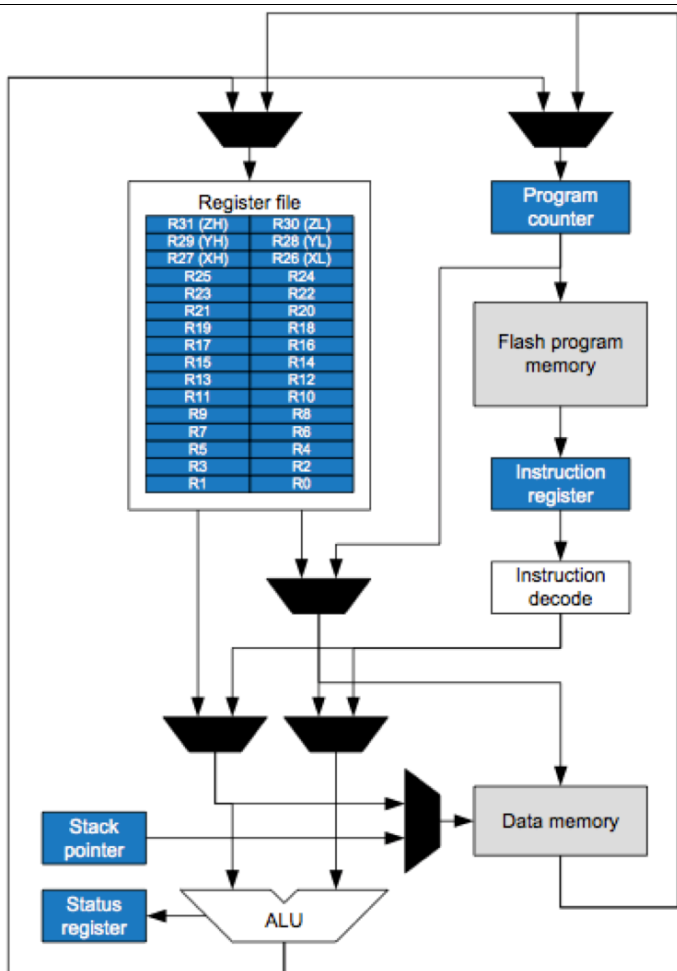


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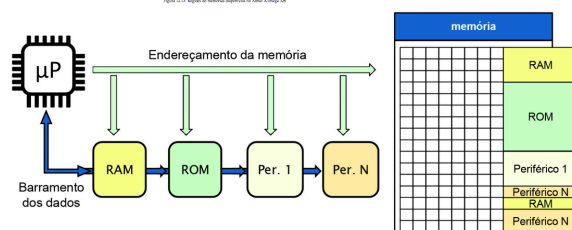


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