## CENG 412 Embedded Systems Lab 4 A to D Converter

Na	ame Signed
In	troduction this lab you will use the A/D converter of the HCS12 to measure a DC voltage. You will write Assembly language file and call this program from a C code file.
Eq	uipment 1. HCS12 workstation board with Demo board attached.
	ocedure - Part A  A variable DC voltage will be derived from the potentiometer circuit on the <b>Demo</b> board. The potentiometer is a round thumbwheel next to the LED display. Consult the Schematic diagram for the <b>Demo Board</b> on the course web page and locate R12, the 10 $\Omega$ potentiometer. Trace the wiper connection of the pot back to the P1 Primary I/O connector.
	What is the pin number of the potentiometer wiper on the P1 header?
2.	Examine the HCS12 Workstation Pinouts link on the course web page and verify the pin number for the pot and verify that the pin number found in step 1 is connected to channel 2 of the A/D converter.
3.	Connect a DC voltmeter to this pin and verify that the wiper voltage varies from 0 to 5 volts DC. Set the potentiometer voltage to 2.50 volts.
4.	Create a new project in CodeWarrior with a C program and an Assembler program. The C program will be the same as in the previous lab. Refer to the sample A/D program file shown at the end of this lab. Modify the code as follows:
	Find and enter the values for all the missing addresses for the A/D enable register, the A/D control registers, the A/D Status register and the A/D result registers.
	Determine the values needed for the admask2 (ATD0CTL2), admask3 (ATD0CTL3), admask4 (ATD0CTL4) and admask5 (ATD0CTL5) parameters to configure the A/D converter as follows
	<ul> <li>Power-Up the A/D converter</li> <li>4 conversions</li> <li>8 bit converter SMP0 &amp; SMP1=0, PRS4 - PRS0 - set the pre scale value to 00101</li> <li>unsigned, right justified results, single scan on channel 2 only</li> </ul>
	Enter the values for admask2, admask3, admask4, and admask5 in the program code.
5.	<b>Make</b> the project in CodeWarrior. Open the <b>Project.map</b> file and locate the <b>Entry point</b> address of your code in the STARTUP section. This will be close to the beginning of the file. Scroll down to almost the end of the Project.map file (just above the UNUSED-OBJECTS SECTION) and locate the address of the label <b>results</b> .
	Entry point address results label address

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- 6. Open AsmIDE and download the s19 file. Run the program g <entry point address> and locate for the results at the address you found above md <results label address>. Record all 4 values. Calculate the expected value for the conversions 2.5 volts is the point value between 0 and 5 volts. What is the midpoint value between 0 and the full scale value FF in hex? Mid-point value = \_\_\_\_\_\_\_\_
- 7. Run the program again with the voltage set to 1.0 volt and then 4.0 V. **Record all these values in a table**. Verify they are correct. **Each value is 4 hex digits 2 bytes long.**

Analog voltage	Measured o	digital value	Calc digital value	
1.0 V				
2.5 V				
4.0 V				

Modify the program to do the following

- 10 bit converter with 4 conversions
- signed, left justified results, single scan on channel 2 only

## Record the values in the table.

Analog voltage	Measured dig	Calc digital value		
1.0 V				
2.5 V				
4.0 V				

## **Procedure - Part B Continuous Conversions**

- 1. Create a new project in CodeWarrior. Open the main.asm file from Part A and copy/paste the assembler code from this project to your new main.asm file. Edit this code to make the red LED display on the Demo board show the value of the A/D conversion continuously. You will need to change the following:
  - modify the admask3 parameter for 1 conversion
  - modify admask4 for an 8 bit conversion
  - modify admask5 for right justified, unsigned results, continuous conversion sequences and sample only channel 2
- 2. You will need to define the address for PORTH and DDRH. configure Port H as an Output port and transfer the contents of the Result Register to register D and then send this value to Port H. Modify the last part of code as shown below.

Again: brclr ATD0STAT0,SCFflag,\* ; wait for conversion to complete | Idd ATD0DR0 ; get result | stab PORTH ; send to port H | bra Again | rts

3. Vary the R 12 potentiometer and check to see if the display varies as well. Measure and record  $V_{in}$  for 1 bit only high (0000 0001) and the lower 7 of 8 bits high (0111 1111).

Demonstrate the final working version of all your programs.

```
A/D Converter
       Name:
       Date:
       Course: Embedded Systems
     uses ATD0 converter, 8 bit conversion, 4 conversions, unsigned
     right justified results, Single scan, channel 2
; export symbols
       XDEF asm_main
; register addresses
ATDODIEN:
                           ;A/D enable register address
              equ
              equ
                           ; A/D Control Register 2 address
ATD0CTL2:
ATD0CTL3:
              equ
                            ; A/D Control Register 3 address
                            ; A/D Control Register 4 address
ATD0CTL4:
              equ
ATD0CTL5:
                            ; A/D Control Register 5 address
              equ
ATDOSTATO:
              equ
                            ; A/D status register address
ATDODRO:
              equ
                            ; Result register 1 address
ATD0DR1:
              equ
                            ; Result register 2 address
ATD0DR2:
              equ
                            ; Result register 3 address
                            ; Result register 4 address
ATD0DR3:
              equ
ATD0DR4:
              equ
                            ; Result register 5 address
                            ; Result register 6 address
ATD0DR5:
              equ
ATD0DR6:
              equ
                            ; Result register 7 address
                            ; Result register 8 address
ATD0DR7:
              equ
; values of parameters
                    $600
                            ; Delay value
Delay:
             equ
SCFflag:
             equ
                     $80
                            ; detect conversion done byte
admask2:
             equ
                            ; ADPU = 1 Enable A/D
admask3:
                            : do 4 conversions
             equ
                            ;8 bit conver,2 A/D conver cycles, prescale 00101
admask4:
             equ
                            ; Right justified, unsigned, single scan, channel 2
admask5:
             equ
; memory storage
results:
             ds.w
                     04
; Code section - this assembly routine is called by the C application
asm_main:
               #$00.ATDODIEN
    movb
                                          ; disable digital inputs
               #admask2,ATD0CTL2
                                          ; configure ATD0CT12
    movb
                                          ; Delay 100 usec - A/D powers up
    ldx
              #Delay
loop: dex
    bne
               #admask3,ATD0CTL3
                                          ; configure ATD0CT13
    movb
```

movb movb brclr	#admask4,AT #admask5,AT ATD0STAT0,S	DOCTL5	configure ATD0CT14; start conversion; wait for conversion to complete;
ldd std ldd std ldd std ldd std rts	ATDODRO results ATDODR1 results+2 ATDODR2 results+4 ATDODR3 results+6	; get 1st re ; store in r ; get 2nd r ; store in r ; get 3rd re ; store in r ; get 4th re ; store in r ; return to	nemory result nemory result nemory result nemory result nemory