

The Modified Nodal Analysis (MNA) Method

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Nodal Formulation

- Matrix T in the Tableau formulation is very sparse
- The Nodal formulation emerges as a block elimination process on the Tableau formulation
- It yields a very compact formulation, but it is not general

Nodal Formulation (cont.)

From the Tableau equations,

$$\mathbf{A}^T \mathbf{V}_n - \mathbf{V}_b = \mathbf{0} \quad \mathbf{Y} \mathbf{V}_b + \mathbf{Z} \mathbf{I} = \mathbf{W} \quad \mathbf{A} \mathbf{I} = \mathbf{0}$$

Substituting first equation into the second equation,

$$\mathbf{Y} \mathbf{A}^T \mathbf{V}_n + \mathbf{Z} \mathbf{I} = \mathbf{W}$$

Combining last two equations,

$$\begin{bmatrix} \mathbf{Y} \mathbf{A}^T & \mathbf{Z} \\ \mathbf{0} & \mathbf{A} \end{bmatrix} \begin{bmatrix} \mathbf{V}_n \\ \mathbf{I} \end{bmatrix} = \begin{bmatrix} \mathbf{W} \\ \mathbf{0} \end{bmatrix} \text{ an } (b+n) \times (b+n) \text{ matrix is achieved}$$

Nodal Formulation (cont.)

If every element in the network can be represented by

$$-\mathbf{Y} \mathbf{V}_b + \mathbf{I} = \mathbf{J}$$

Using $\mathbf{A}^T \mathbf{V}_n - \mathbf{V}_b = \mathbf{0}$

$$-\mathbf{Y} \mathbf{A}^T \mathbf{V}_n + \mathbf{I} = \mathbf{J}$$

Since $\mathbf{A} \mathbf{I} = \mathbf{0}$

$$\mathbf{A} \mathbf{I} = \mathbf{A}(\mathbf{J} + \mathbf{Y} \mathbf{A}^T \mathbf{V}_n) = \mathbf{0}$$

$$\mathbf{A} \mathbf{Y} \mathbf{A}^T \mathbf{V}_n = -\mathbf{A} \mathbf{J} \text{ an } n \times n \text{ matrix is achieved}$$

$\mathbf{Y}_n = \mathbf{A} \mathbf{Y} \mathbf{A}^T$ is the Nodal Admittance Matrix, and $\mathbf{J}_n = -\mathbf{A} \mathbf{J}$

$$\mathbf{Y}_n \mathbf{V}_n = \mathbf{J}_n$$

Nodal Formulation – Final Remarks

- It requires solving an n by n system of equations
- It is restricted to elements that have an admittance representation:

$$-YV_b + I = J$$

- Independent voltage sources, ideal CCVS, ideal CCCS, transformers, etc, can not be represented in this manner

Modified Nodal Analysis (MNA)

It splits the circuit elements into groups: one group for elements that accept an admittance description, and one group for those which do not

$$-Y_1V_{1b} + I_1 = J_1 \quad Y_2V_{2b} + Z_2I_2 = W_2$$

If A_1 and A_2 are the corresponding incidence matrices for each group of elements, since $AI = 0$ then

$$A_1I_1 + A_2I_2 = 0$$

Since $A^T V_n = V_b$, then

$$A_1^T V_n = V_{1b} \quad A_2^T V_n = V_{2b}$$

Modified Nodal Analysis, MNA (cont.)

Substituting last two equations into first two,

$$-Y_1 A_1^T V_n + I_1 = J_1 \quad Y_2 A_2^T V_n + Z_2 I_2 = W_2$$

Pre-multiplying by A_1 first previous equation

$$-A_1 Y_1 A_1^T V_n + A_1 I_1 = A_1 J_1$$

Using $A_1 I_1 + A_2 I_2 = 0$

$$-A_1 Y_1 A_1^T V_n - A_2 I_2 = A_1 J_1$$

Then

$$\begin{bmatrix} A_1 Y_1 A_1^T & A_2 \\ Y_2 A_2^T & Z_2 \end{bmatrix} \begin{bmatrix} V_n \\ I_2 \end{bmatrix} = \begin{bmatrix} -A_1 J_1 \\ W_2 \end{bmatrix} \quad HX = W$$

MNA – Final Remarks

- Once the MNA equation is solved,

$$\begin{bmatrix} A_1 Y_1 A_1^T & A_2 \\ Y_2 A_2^T & Z_2 \end{bmatrix} \begin{bmatrix} V_n \\ I_2 \end{bmatrix} = \begin{bmatrix} -A_1 J_1 \\ W_2 \end{bmatrix}$$

- The rest of the unknowns are calculated using

$$I_1 = J_1 + Y_1 A_1^T V_n \quad V_{1b} = A_1^T V_n \quad V_{2b} = A_2^T V_n$$

- MNA retains the advantages of both the Nodal and the Tableau methods
- The above formulation of MNA can be applied to linear circuits only, but can be extended to nonlinear

A Nodal Formulation to Nonlinear Circuits

Suppose that the branch equations of all elements in a circuit accept a non-linear admittance representation,

$$\mathbf{I} = \mathbf{g}(\mathbf{V}_b)$$

From KVL and KCL: $\mathbf{V}_b = \mathbf{A}^T \mathbf{V}_n$ $\mathbf{A}\mathbf{I} = \mathbf{0}$

Then

$$\mathbf{A}(\mathbf{g}(\mathbf{A}^T \mathbf{V}_n)) = \mathbf{0}$$

(a system of n nonlinear equations with n unknowns)

MNA for Linear and Nonlinear Circuits

Split the circuit branches into three groups:

$$-\mathbf{Y}_1 \mathbf{V}_{1b} + \mathbf{I}_1 = \mathbf{J}_1 \quad \mathbf{Y}_2 \mathbf{V}_{2b} + \mathbf{Z}_2 \mathbf{I}_2 = \mathbf{W}_2 \quad \mathbf{I}_3 = \mathbf{g}(\mathbf{V}_{3b})$$

If \mathbf{A}_1 , \mathbf{A}_2 , and \mathbf{A}_3 are the corresponding incidence matrices for each group of elements, from KCL

$$\mathbf{A}_1 \mathbf{I}_1 + \mathbf{A}_2 \mathbf{I}_2 + \mathbf{A}_3 \mathbf{I}_3 = \mathbf{0}$$

From KVL,

$$\mathbf{V}_{1b} = \mathbf{A}_1^T \mathbf{V}_n \quad \mathbf{V}_{2b} = \mathbf{A}_2^T \mathbf{V}_n \quad \mathbf{V}_{3b} = \mathbf{A}_3^T \mathbf{V}_n$$

Substituting KVL into first three equations,

$$-\mathbf{Y}_1 \mathbf{A}_1^T \mathbf{V}_n + \mathbf{I}_1 = \mathbf{J}_1 \quad \mathbf{Y}_2 \mathbf{A}_2^T \mathbf{V}_n + \mathbf{Z}_2 \mathbf{I}_2 = \mathbf{W}_2 \quad \mathbf{I}_3 = \mathbf{g}(\mathbf{A}_3^T \mathbf{V}_n)$$

MNA for Linear and Nonlinear Circuits (cont.)

$$-Y_1 A_1^T V_n + I_1 = J_1 \quad Y_2 A_2^T V_n + Z_2 I_2 = W_2 \quad I_3 = g(A_3^T V_n)$$

Pre-multiplying by A_1 first above equation

$$-A_1 Y_1 A_1^T V_n + A_1 I_1 = A_1 J_1$$

Using $A_1 I_1 + A_2 I_2 + A_3 I_3 = 0$,

$$A_1 Y_1 A_1^T V_n + A_2 I_2 + A_3 g(A_3^T V_n) = -A_1 J_1$$

Solving $Z_2 I_2 = W_2 - Y_2 A_2^T V_n$ for I_2 at each iteration and substituting into the above equation yields a system of n nonlinear equations that can be solved for V_n

MNA Formulation Using Stamps

- The MNA equation

$$\begin{bmatrix} A_1 Y_1 A_1^T & A_2 \\ Y_2 A_2^T & Z_2 \end{bmatrix} \begin{bmatrix} V_n \\ I_2 \end{bmatrix} = \begin{bmatrix} -A_1 J_1 \\ W_2 \end{bmatrix} \quad HX = W$$

can be formulated without using oriented graphs or incidence matrices A_1 and A_2

- H and W can be directly formulated by inspection, using stamps
- Further, the L and C elements can be separated so that

$$(H_1 + sH_2)X = W$$

can also be written directly

MNA Formulation Using Stamps (cont.)

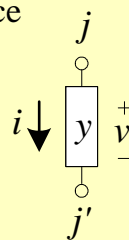
- Start by entering by inspection in a matrix \mathbf{H} all elements which have an admittance description
- Initially \mathbf{H} has an n by n size, where n is the number of ungrounded nodes
- Increase the size of \mathbf{H} whenever we enter an element which does not have an admittance description
- The constitutive voltage equations of an element without admittance description are attached as an extra row of \mathbf{H} , while the current flowing into that element is attached as an extra column in \mathbf{H}
- \mathbf{W} is generated according to the independent sources

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MNA Formulation Using Stamps (cont.)

Admittance

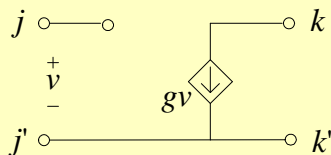


$$\begin{matrix} V_j & V_{j'} \\ j & \begin{bmatrix} y & -y \\ -y & y \end{bmatrix} \\ j' & \end{matrix}$$

$$I_j = y(V_j - V_{j'})$$

$$I_{j'} = -y(V_j - V_{j'})$$

VCCS



$$\begin{matrix} V_j & V_{j'} \\ k & \begin{bmatrix} g & -g \\ -g & g \end{bmatrix} \\ k' & \end{matrix}$$

$$I_j = I_{j'} = 0$$

$$I_k = g(V_j - V_{j'})$$

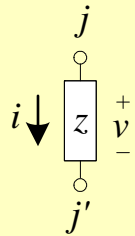
$$I_{k'} = -g(V_j - V_{j'})$$

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MNA Formulation Using Stamps (cont.)

Impedance



$$\begin{matrix} V_j & V_{j'} & I \\ j & \begin{bmatrix} & 1 \\ & -1 \end{bmatrix} \\ j' & \begin{bmatrix} 1 & -1 & -z \end{bmatrix} \end{matrix}$$

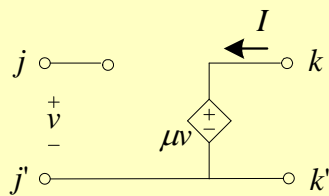
$$I_j = I$$

$$I_{j'} = -I$$

$$V_j - V_{j'} - zI = 0$$

MNA Formulation Using Stamps (cont.)

VCVS



$$\begin{matrix} V_j & V_{j'} & V_k & V_{k'} & I \\ j & & & & \\ j' & & & & \\ k & & & 1 & \\ k' & & & -1 & \\ & -\mu & \mu & 1 & -1 \end{matrix}$$

$$I_j = 0$$

$$I_k = I$$

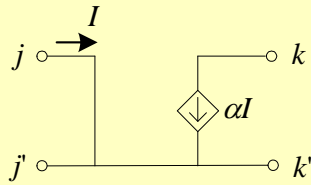
$$I_{j'} = 0$$

$$I_{k'} = -I$$

$$-\mu V_j + \mu V_{j'} + V_k - V_{k'} = 0$$

MNA Formulation Using Stamps (cont.)

CCCS



$$\begin{array}{c} V_j \quad V_{j'} \quad V_k \quad V_{k'} \quad I \\ \begin{array}{c} j \\ j' \\ k \\ k' \end{array} \left[\begin{array}{cccc|c} & & & & 1 \\ & & & & -1 \\ & & & \alpha & \\ & & & -\alpha & \\ 1 & -1 & & & \end{array} \right]$$

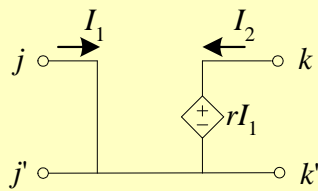
$$I_j = I \quad I_k = \alpha I$$

$$I_{j'} = -I \quad I_{k'} = -\alpha I$$

$$V_j - V_{j'} = 0$$

MNA Formulation Using Stamps (cont.)

CCVS



$$\begin{array}{c} V_j \quad V_{j'} \quad V_k \quad V_{k'} \quad I_1 \quad I_2 \\ \begin{array}{c} j \\ j' \\ k \\ k' \end{array} \left[\begin{array}{cccc|cc} & & & & 1 & \\ & & & & -1 & \\ & & & & & 1 \\ & & & & & -1 \\ & & 1 & -1 & -r & \\ 1 & -1 & & & & \end{array} \right]$$

$$I_j = I_1 \quad I_k = I_2$$

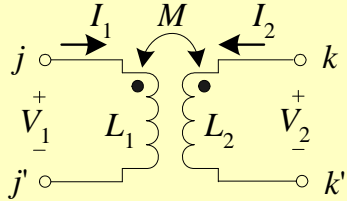
$$I_{j'} = -I_1 \quad I_{k'} = -I_2$$

$$V_j - V_{j'} = 0$$

$$V_k - V_{k'} - rI_1 = 0$$

MNA Formulation Using Stamps (cont.)

Transformer



$$I_j = I_1 \quad I_k = I_2$$

$$I_{j'} = -I_1 \quad I_{k'} = -I_2$$

$$\begin{matrix} V_j & V_{j'} & V_k & V_{k'} & I_1 & I_2 \\ j & \begin{bmatrix} 1 \\ -1 \end{bmatrix} & & & & \\ j' & & & & & \\ k & & & & 1 \\ k' & & & & -1 \end{matrix}$$

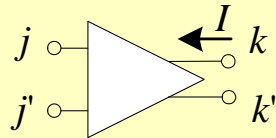
$$\begin{bmatrix} 1 & -1 & & -sL_1 & -sM \\ & & 1 & -1 & -sM & -sL_2 \end{bmatrix}$$

$$V_j - V_{j'} - sL_1 I_1 - sM I_2 = 0$$

$$V_k - V_{k'} - sL_2 I_2 - sM I_1 = 0$$

MNA Formulation Using Stamps (cont.)

Ideal Op-Amp



$$I_j = 0 \quad I_k = I$$

$$I_{j'} = 0 \quad I_{k'} = -I$$

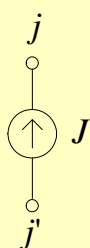
$$\begin{matrix} V_j & V_{j'} & V_k & V_{k'} & I \\ j & \begin{bmatrix} 1 \\ -1 \end{bmatrix} & & & \\ j' & & & & \\ k & & & 1 \\ k' & & & -1 \end{matrix}$$

$$\begin{bmatrix} 1 & -1 \end{bmatrix}$$

$$V_j - V_{j'} = 0$$

MNA Formulation Using Stamps (cont.)

Current Source



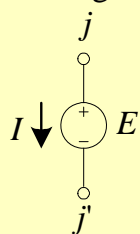
(source vector)

$$\begin{bmatrix} j \\ j' \end{bmatrix} \begin{bmatrix} J \\ -J \end{bmatrix}$$

$$I_j = -J$$

$$I_{j'} = J$$

Voltage Source



$V_j \quad V_{j'} \quad I$

(source vector)

$$\begin{bmatrix} j \\ j' \end{bmatrix} \begin{bmatrix} 1 & -1 \\ 1 & -1 \end{bmatrix}$$

$$\begin{bmatrix} E \end{bmatrix}$$

$$I_j = I$$

$$I_{j'} = -I$$

$$V_j - V_{j'} = E$$