

# SC200E&SC206E Series

# CAN Reference Design

**Smart Module Series**

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**Quectel Wireless Solutions Co., Ltd.**

Building 5, Shanghai Business Park Phase III (Area B), No.1016 Tianlin Road, Minhang District, Shanghai 200233, China

Tel: +86 21 5108 6236

Email: [info@quectel.com](mailto:info@quectel.com)

**Or our local offices. For more information, please visit:**

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# About the Document

## Revision History

Version	Date	Author	Description
-	2024-08-23	Kevin DING	Creation of the document
1.0	2024-08-26	Kevin DING	First official release

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# 1 Reference Design

## 1.1. Introduction

This document provides the reference design for Quectel SC200E&SC206E series module, including block diagram, power system block diagram, module interfaces, CAN design and USB interface.

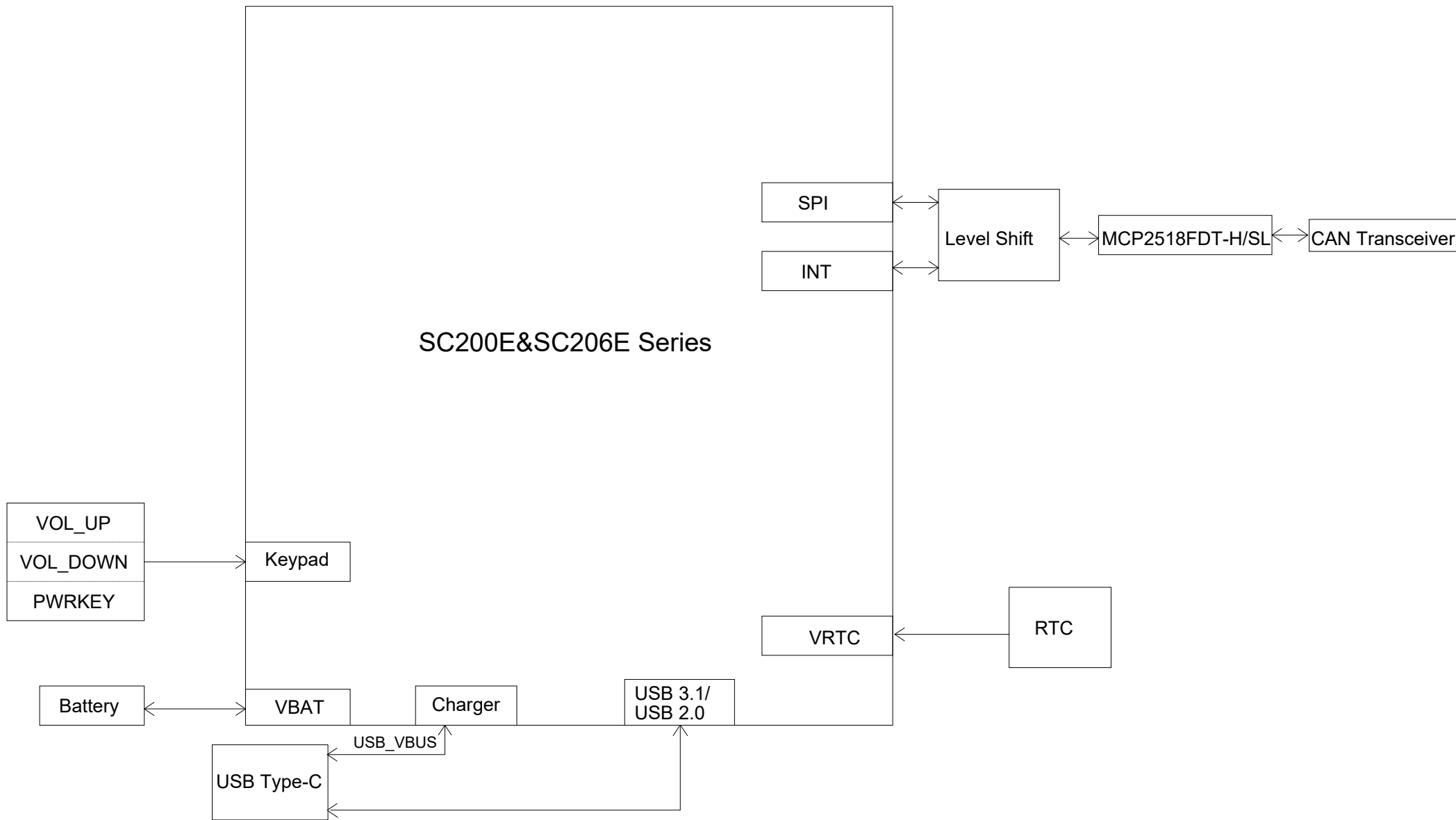
## 1.2. Schematics

The schematics illustrated in the following pages are provided for your reference only.

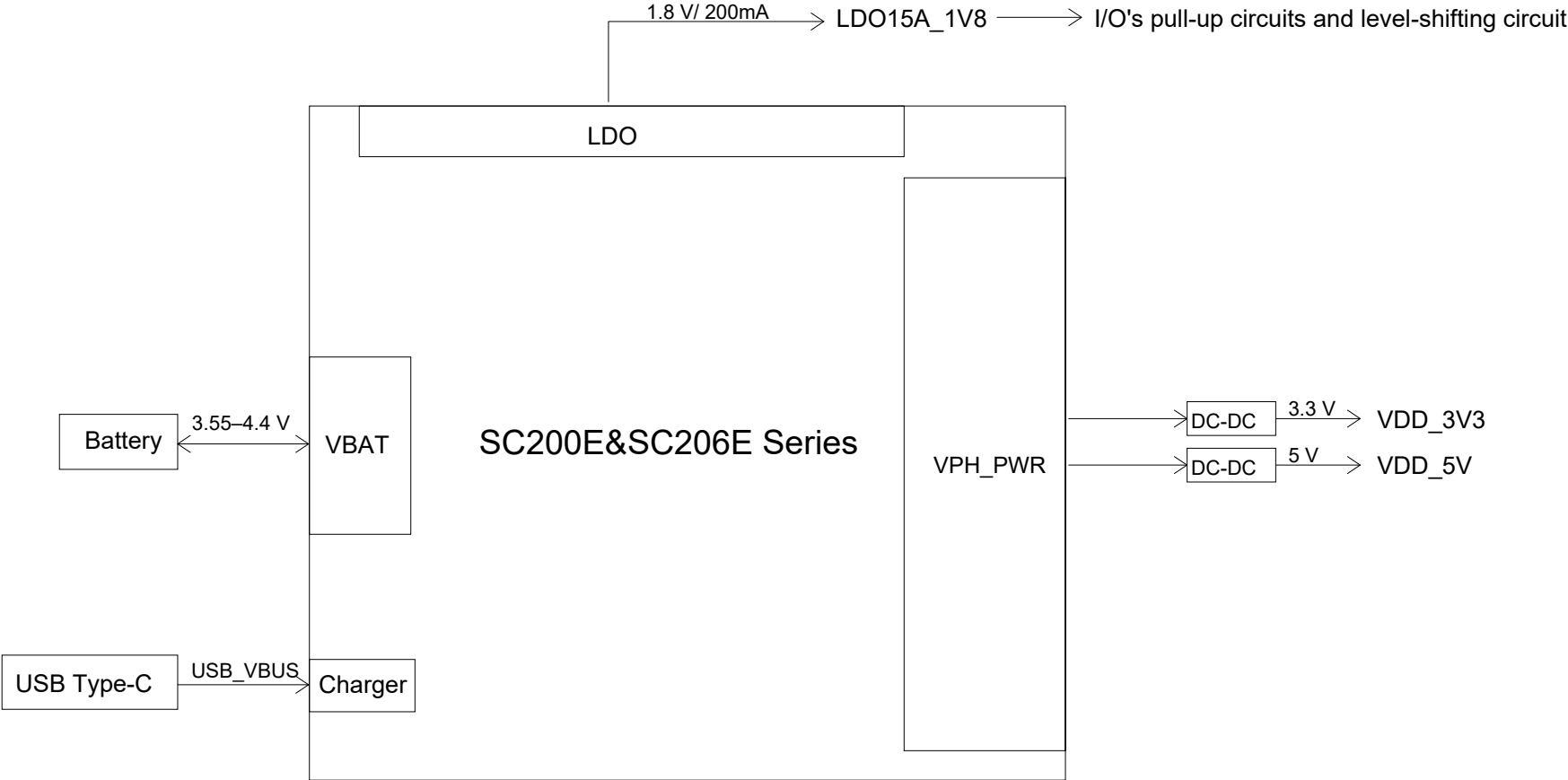
**NOTE**

It is required to confirm the applicability and price from the supplier about the IC involved in the reference design.

Block Diagram

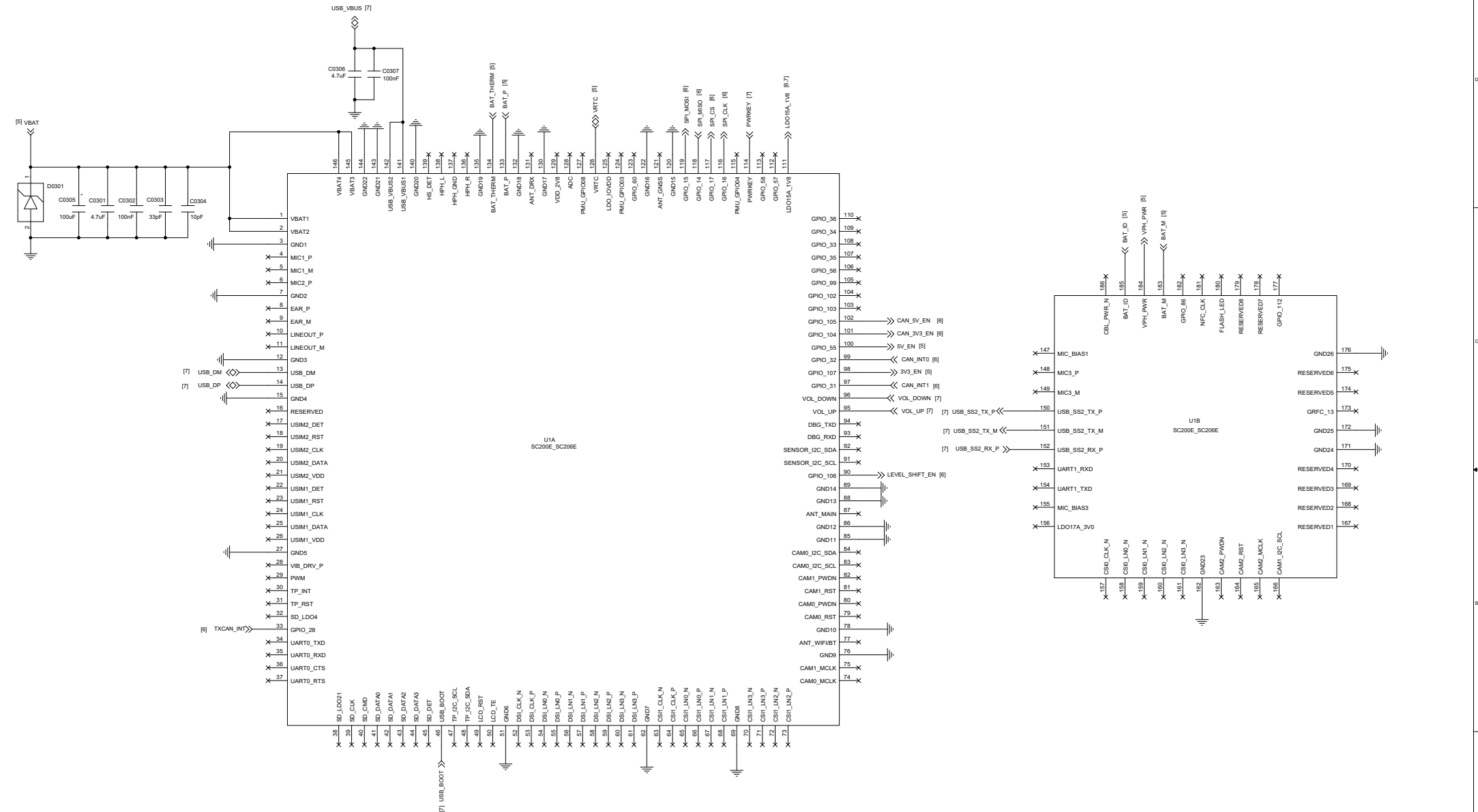


# Power System Block Diagram





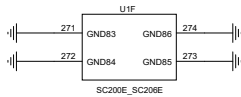
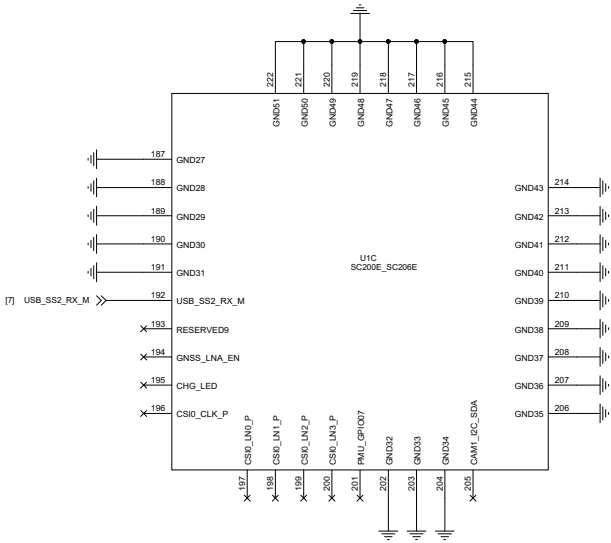
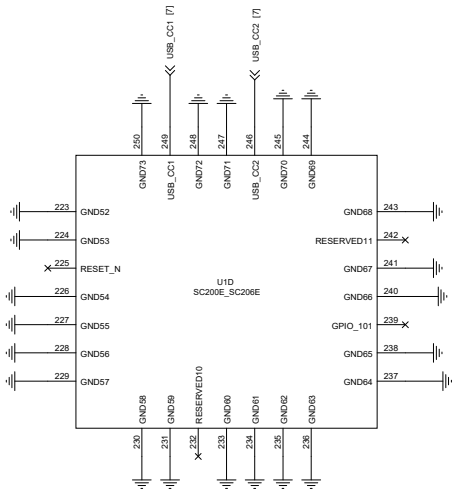
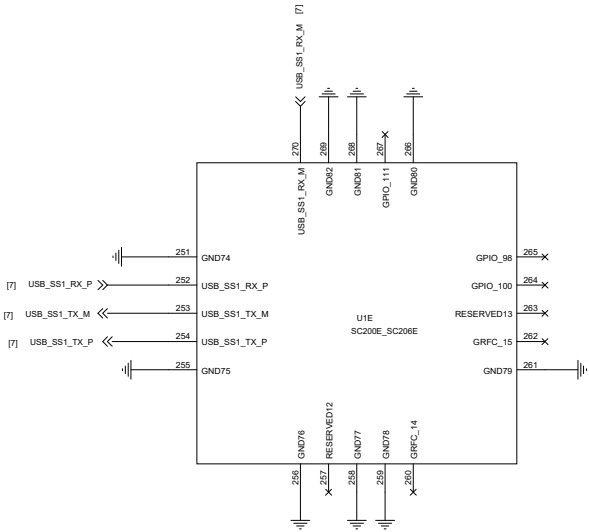
# Module Interfaces (1)



- NOTE:**
1. Place the TVS and capacitors close to VBAT pins.
  2. For DBG\_TXD and DBG\_RXD, test points must be reserved.
  3. The recommended parallel capacitors for pins 141 and 142 should be placed as close to the pins as possible.

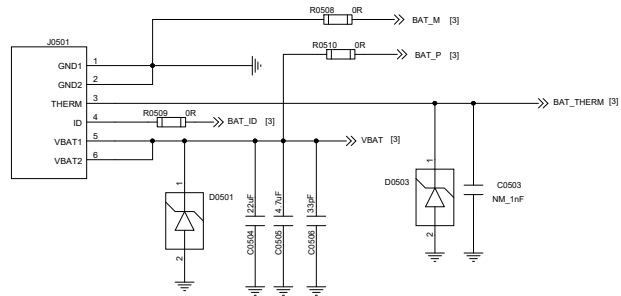
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Module Interfaces (2)

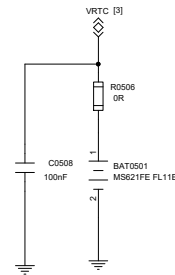


# Power Supply

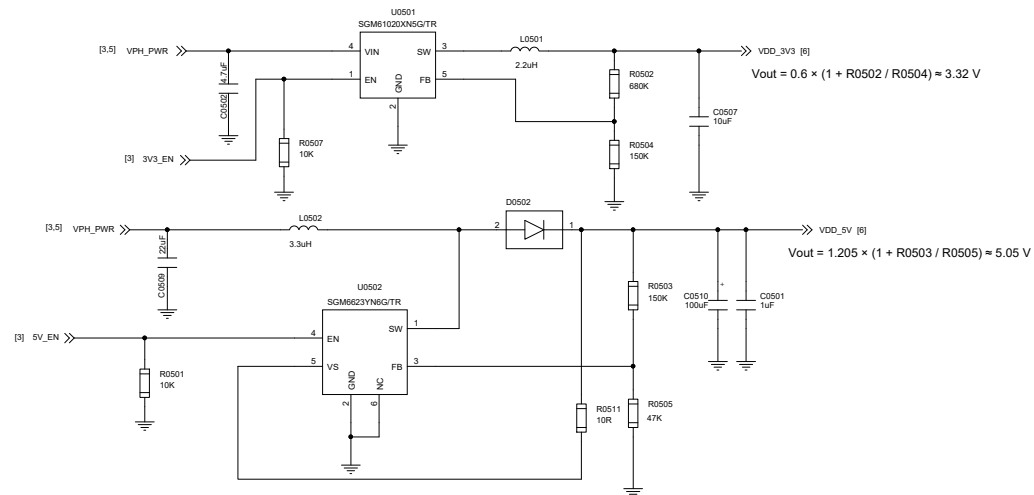
## Battery Application



## RTC Backup Battery



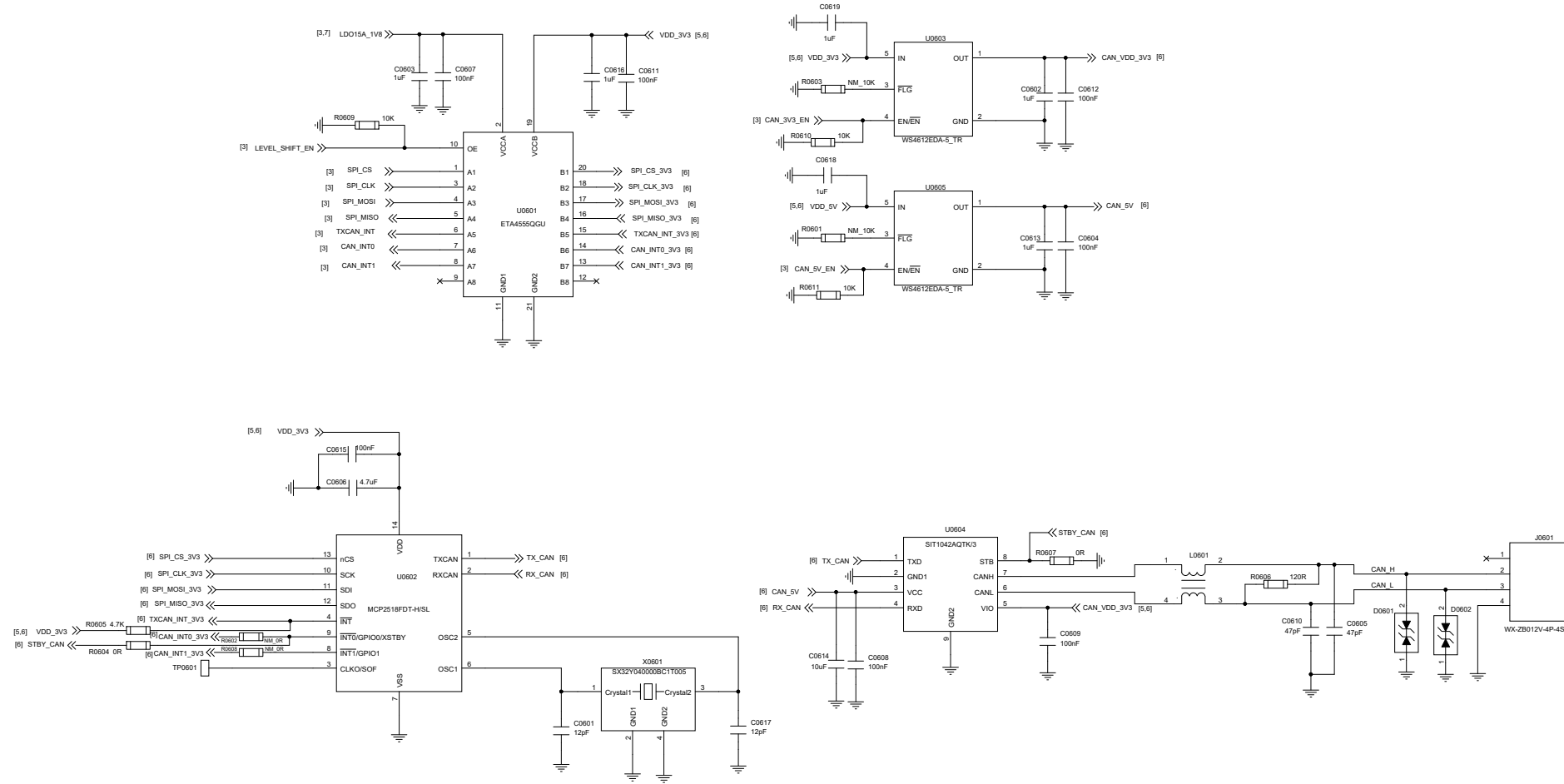
## DC-DC



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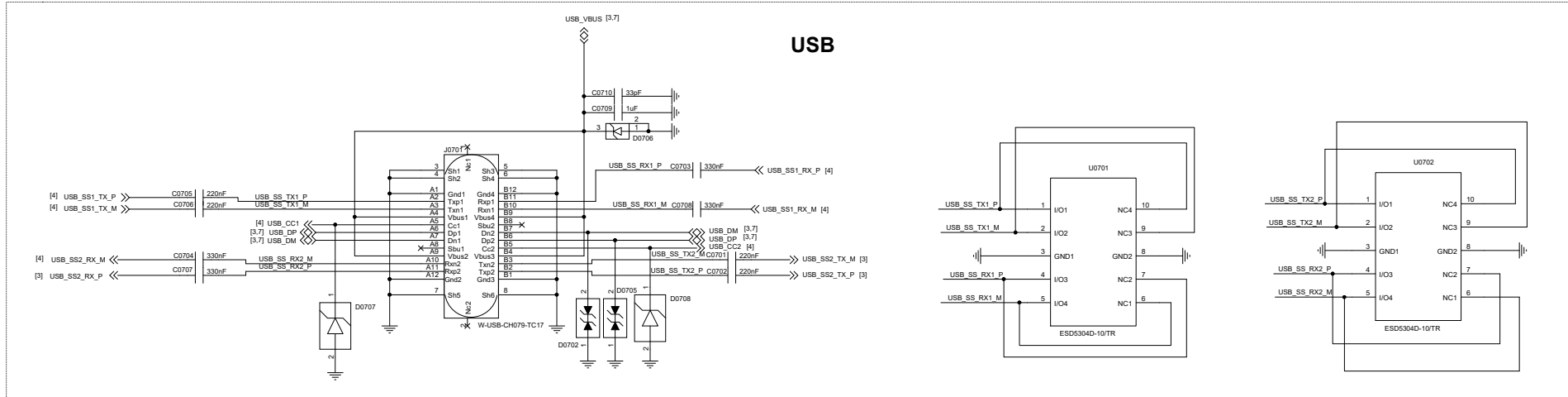
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# CAN

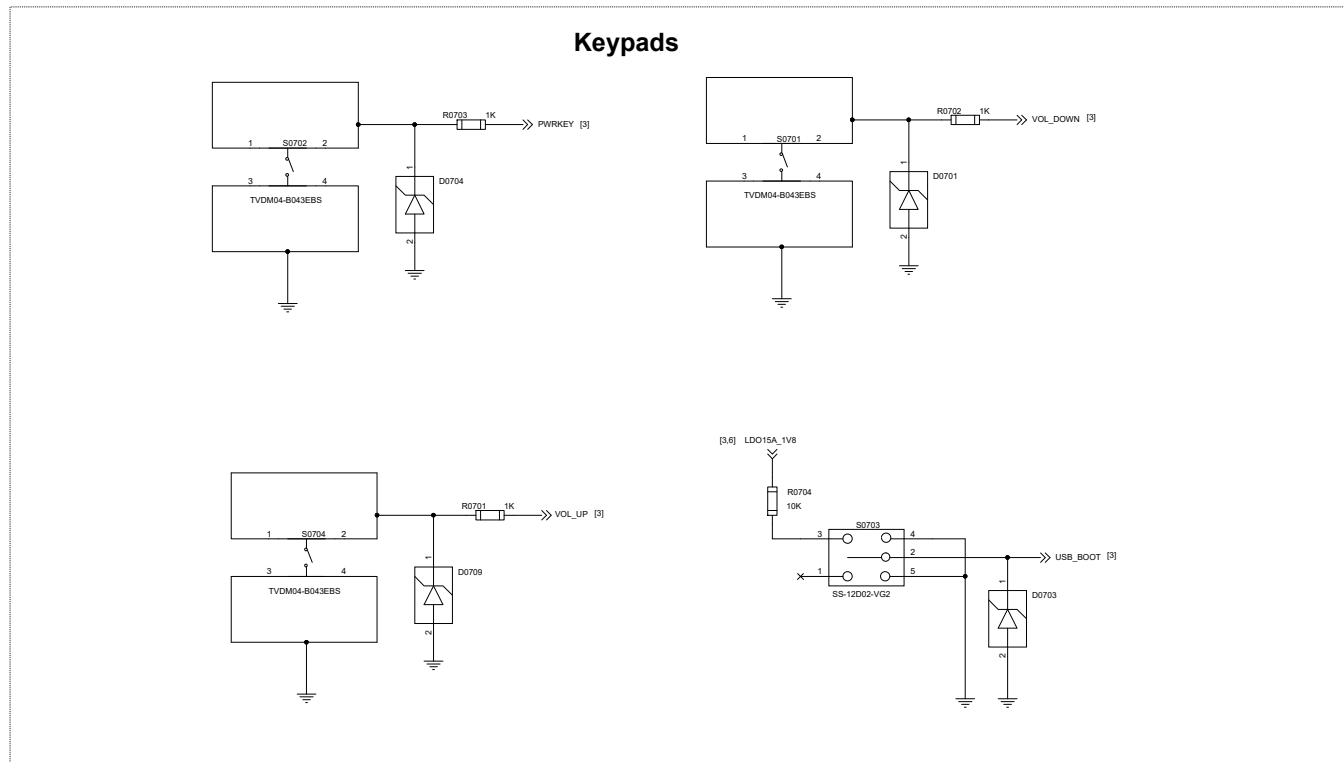


# USB & Keypads

## USB



## Keypads



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