

A

10



B

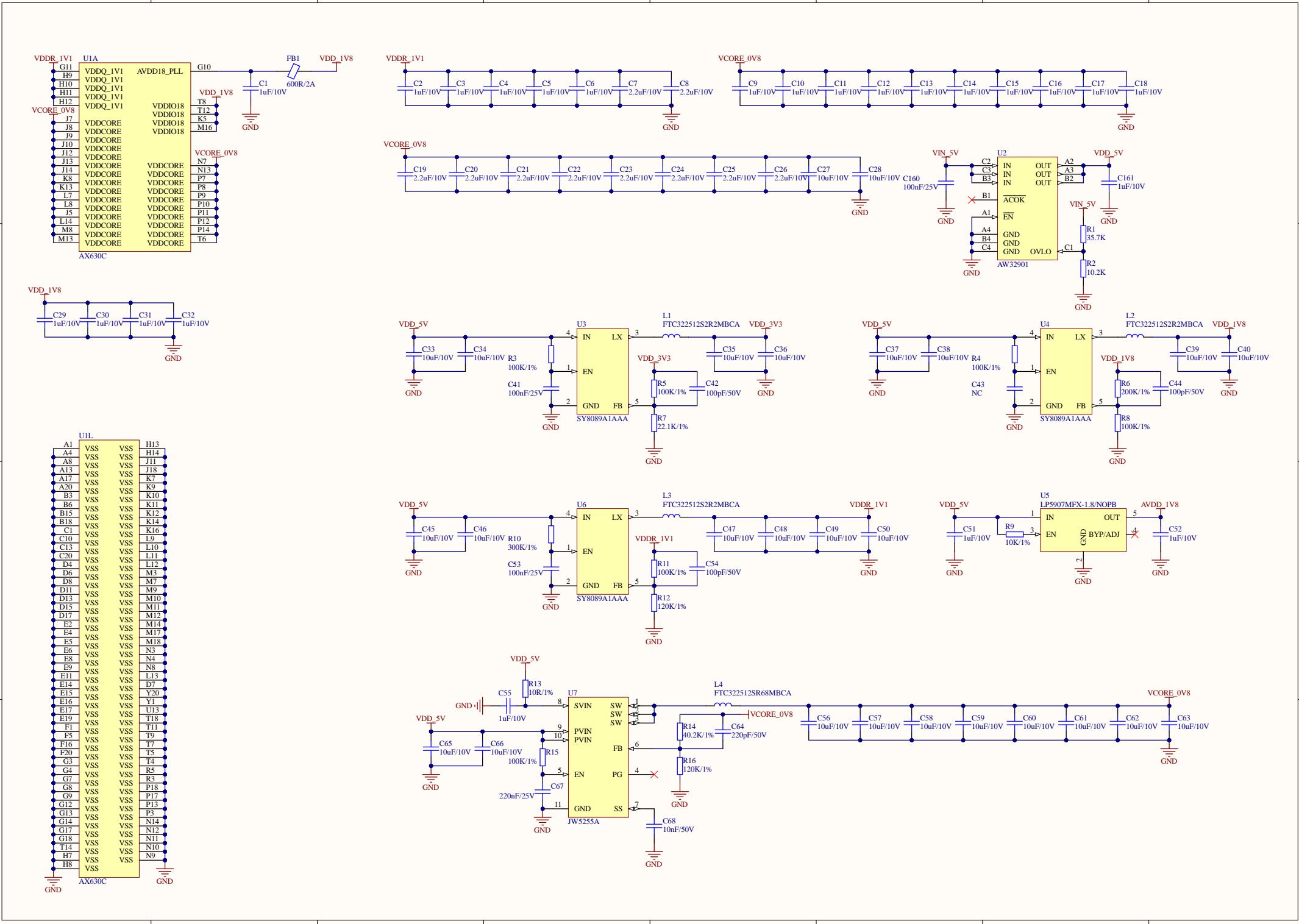
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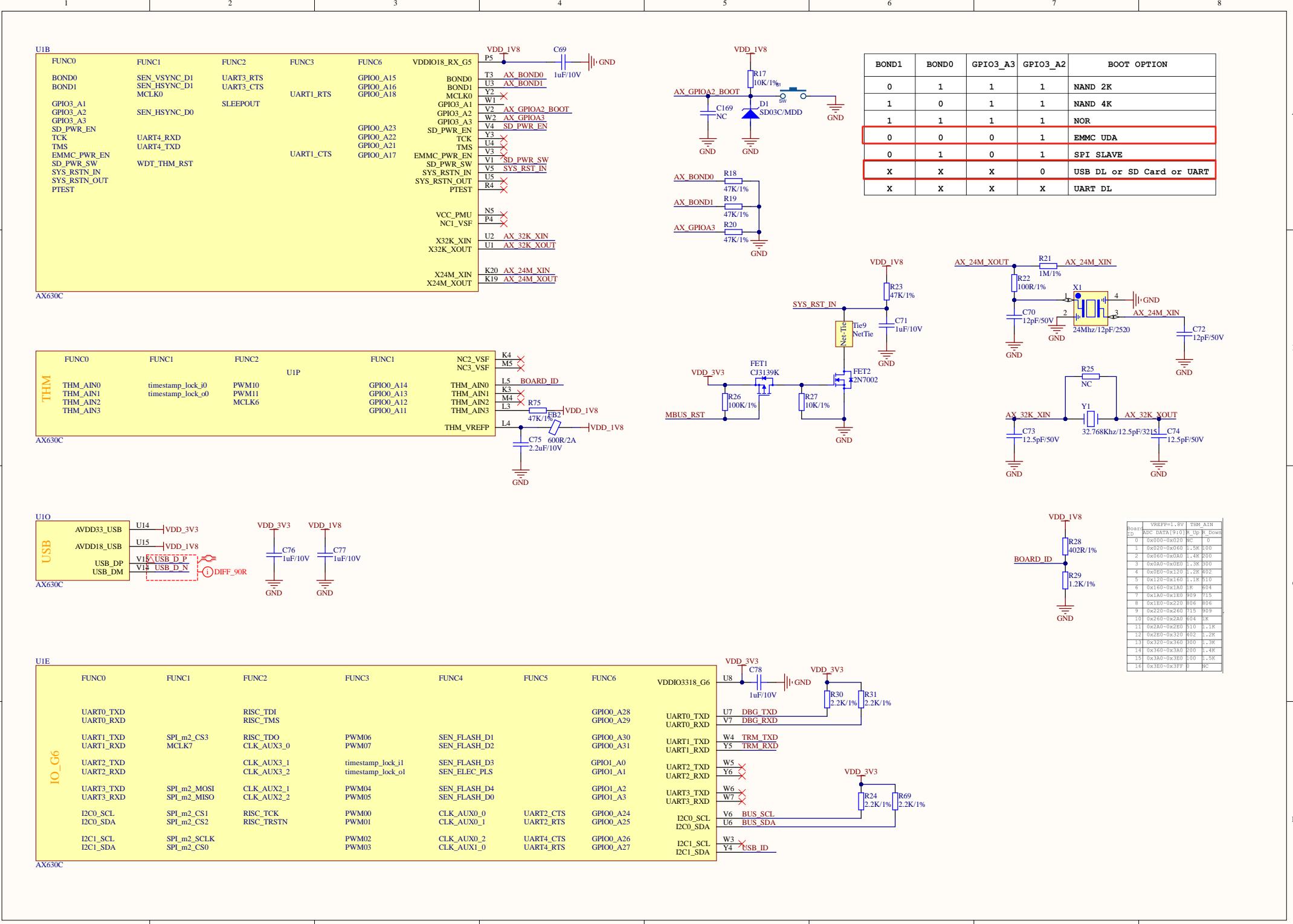
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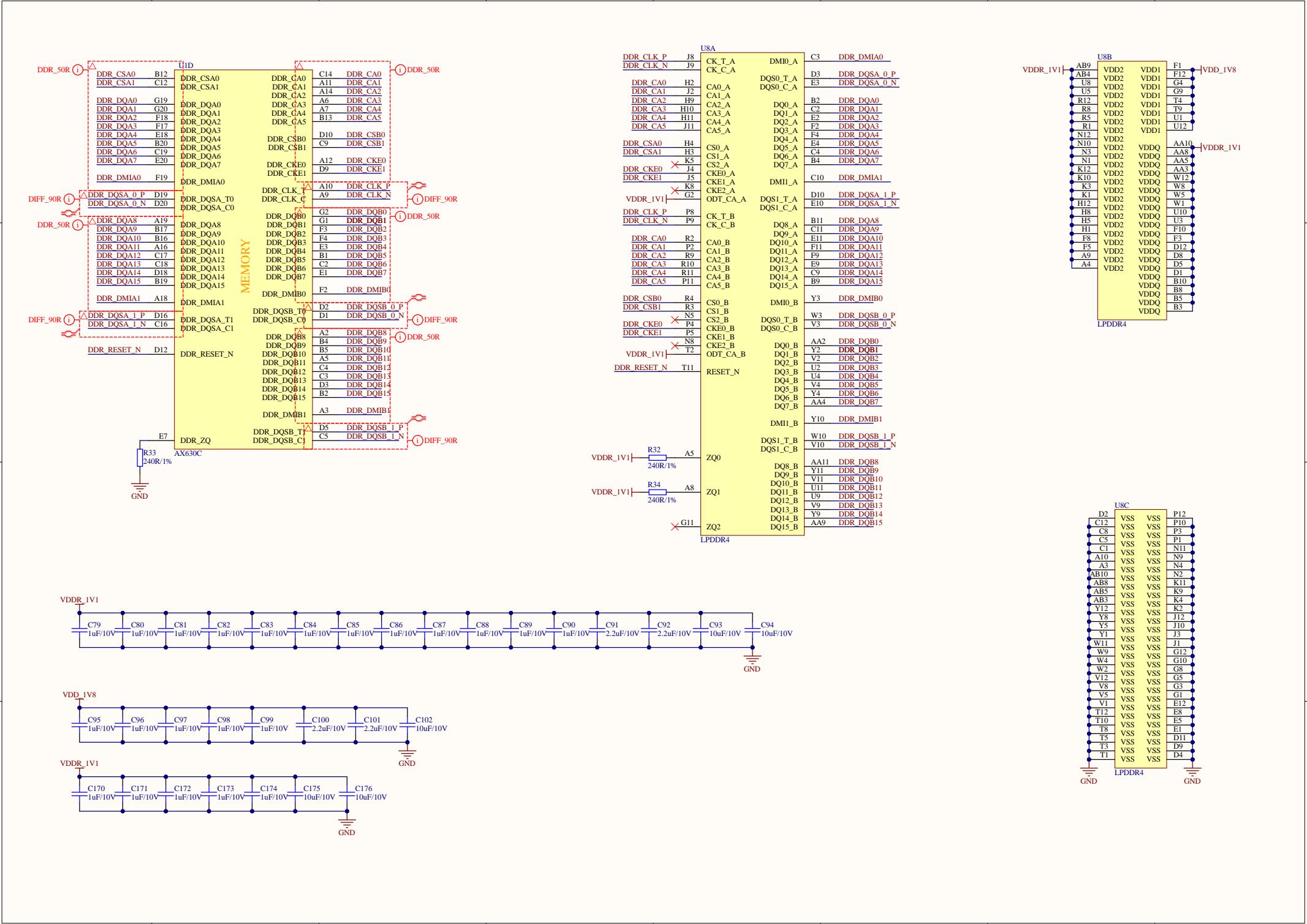
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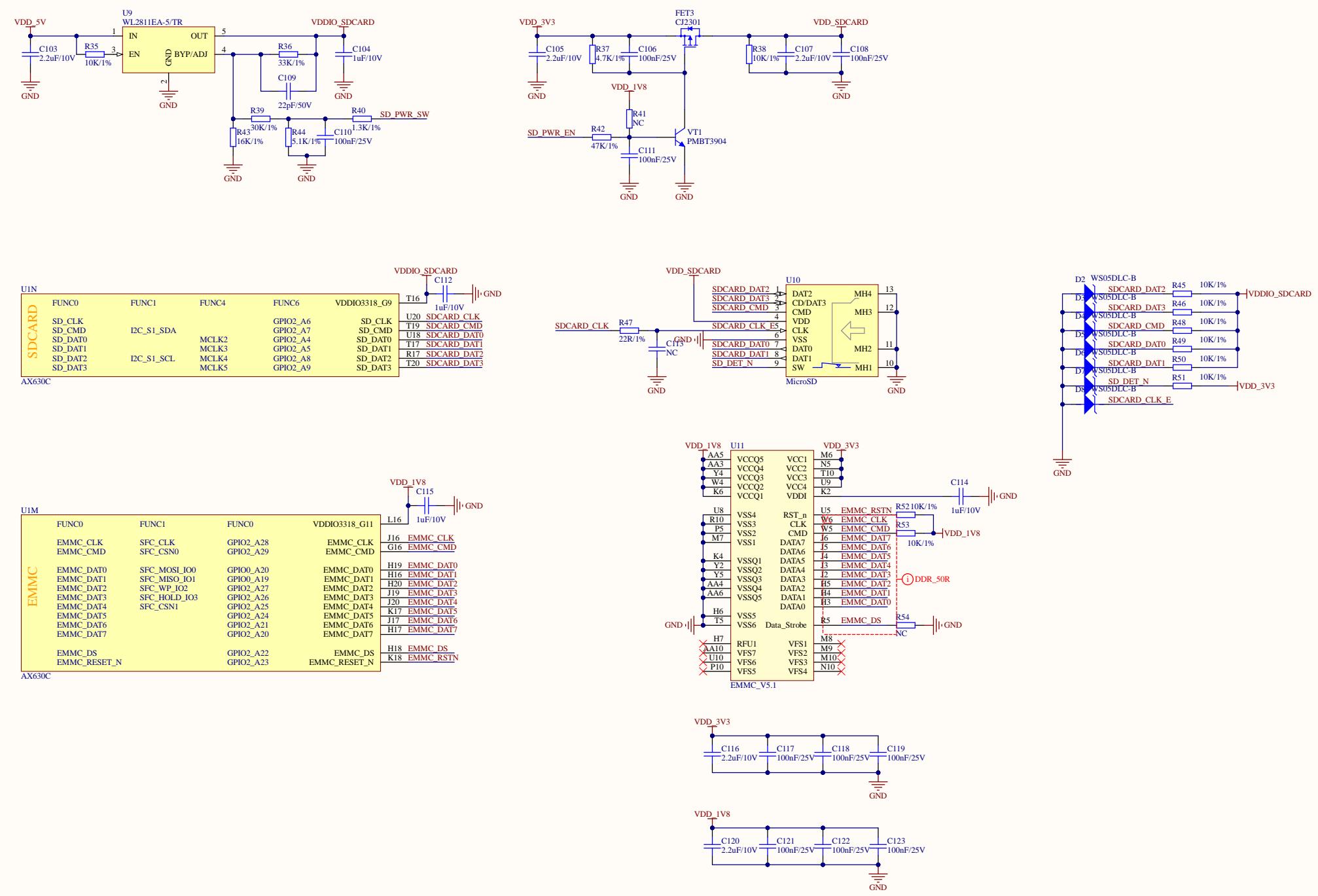
D

10

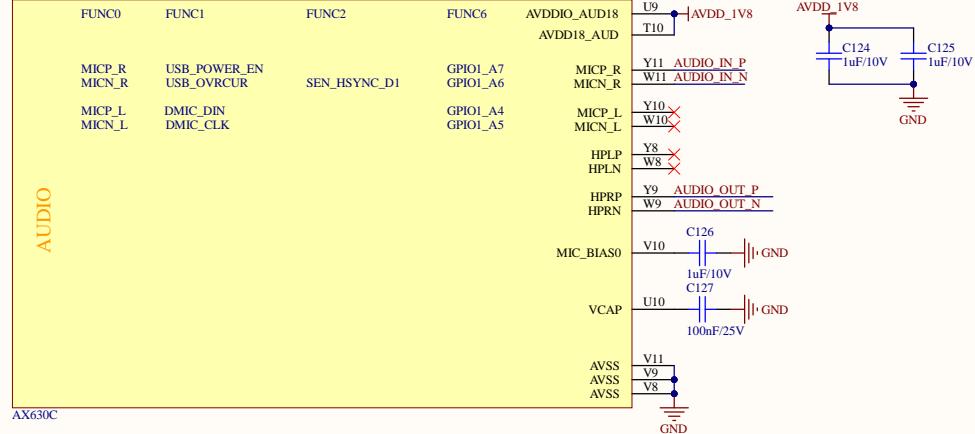








UIF

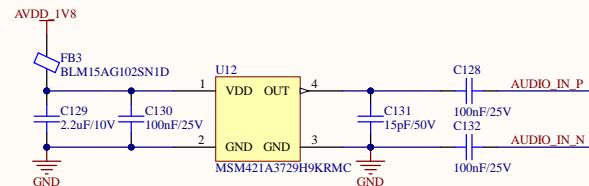


AX630C

A

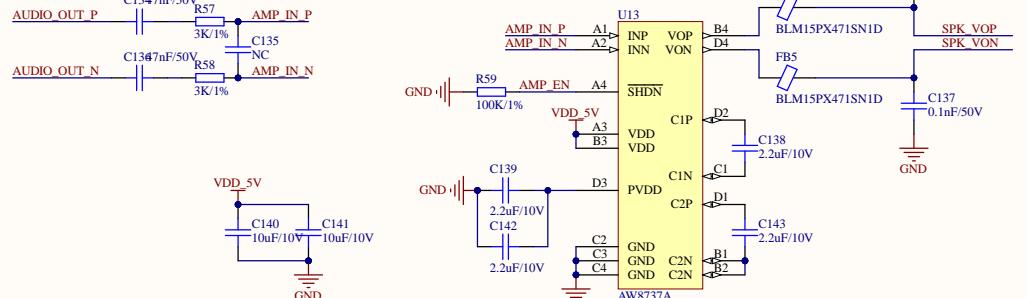
AUDIO

B



B

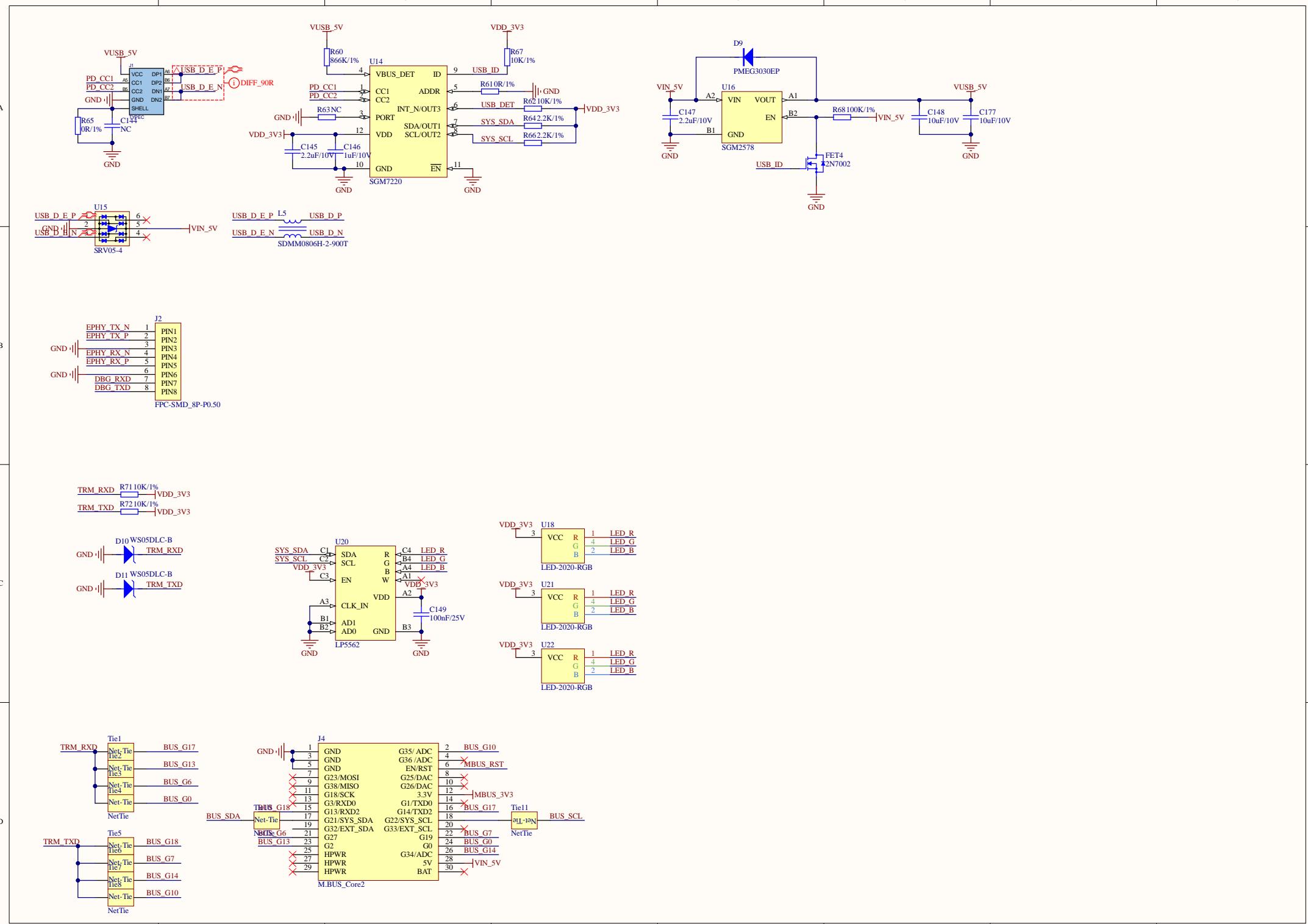
C



C

D

D

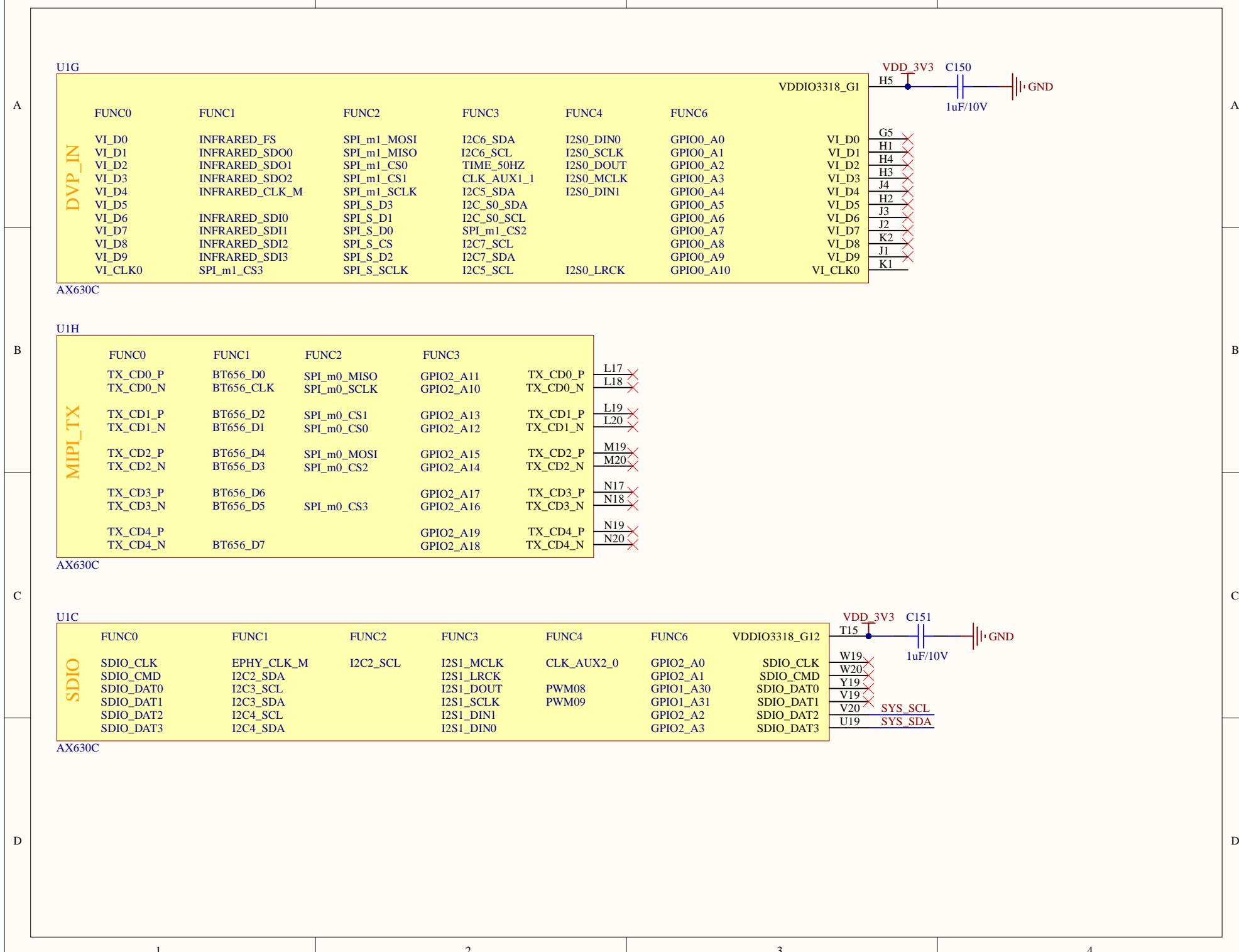


1

2

3

4



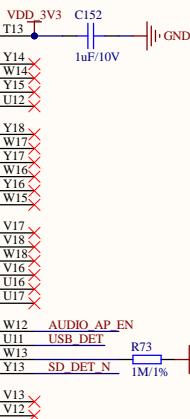
UIK

A

RGMII

AX630C

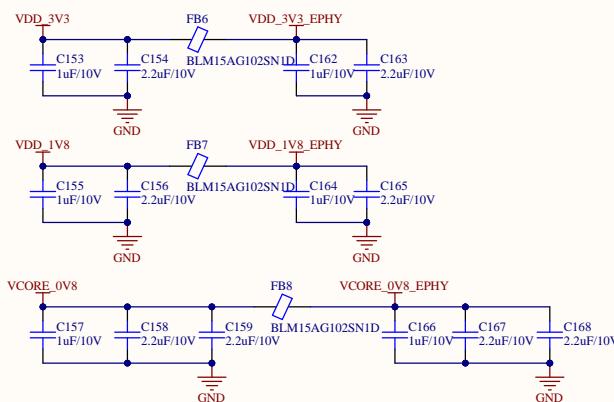
FUNC0	FUNC1	FUNC2	FUNC3	FUNC4	FUNC6	VDDIO3318_G8
RGMII_MDCK	DPLD1	PWM5_M	SEN_FLASH_D1_M		GPIO1_A24	RGMII_MDCK
RGMII_MDIO	DPLD2	PWM6_M	SEN_FLASH_D2_M		GPIO1_A25	RGMII_MDIO
EPHY_CLK	DPL_PCLK	CLK_AUX1_2	SEN_FLASH_D3_M		GPIO1_A26	EPHY_CLK
EPHY_RSTN	DPLD0	PWM7_M	SEN_FLASH_D4_M	SPI_m2_CS0_M	GPIO1_A27	EPHY_RSTN
RGMII_RXCLK	DPLD3				GPIO1_A15	RGMII_RXCLK
RGMII_RXDV	DPLD4				GPIO1_A14	RGMII_RXDV
RGMII_RXD0	DPLD5			UART0_CTS	GPIO1_A12	RGMII_RXD0
RGMII_RXD1	DPLD6			UART0 RTS	GPIO1_A13	RGMII_RXD1
RGMII_RXD2	DPLD7				GPIO1_A16	RGMII_RXD2
RGMII_RXD3	DPLD8				GPIO1_A17	RGMII_RXD3
RGMII_TXCLK	DPLD9		SEN_HSYNC_D1_M	SPI_m2_SCLK_M	GPIO1_A29	RGMII_TXCLK
RGMII_TXEN	DPLD10		SEN_VSYNC_D0_M	SPI_m2_CS1_M	GPIO1_A21	RGMII_TXEN
RGMII_TXD0	DPLD11		MCLK1	SPI_m2_MOSI_M	GPIO1_A18	RGMII_TXD0
RGMII_TXD1	DPLD12		SEN_HSYNC_D0_M	SPI_m2_MISO_M	GPIO1_A19	RGMII_TXD1
RGMII_TXD2	DPLD13	PWM3_M	SEN_VSYNC_D1_M		GPIO1_A22	RGMII_TXD2
RGMII_TXD3	DPLD14	PWM4_M	SEN_FLASH_D0_M		GPIO1_A23	RGMII_TXD3
EMAC_PTP_PPS0	DPLD17	PWM0_M	UART5_CTS	SEN_FLASH_D5_M	GPIO1_A8	EMAC_PTP_PPS0
EMAC_PTP_PPS1	DPLD16	PWM1_M	UART5_RTS	SEN_VSYNC_D0	GPIO1_A9	EMAC_PTP_PPS1
EMAC_PTP_PPS2	DPLD18	SD_CARD_DETECT_N	UART5_RXD		GPIO1_A10	EMAC_PTP_PPS2
EMAC_PTP_PPS3	DPLD15	PWM2_M	UART5_RXD		GPIO1_A11	EMAC_PTP_PPS3
EPHY_LED0		RGMII_MDCK_M	TE0	SEN_FLASH_D5_M	GPIO1_A28	EPHY_LED0
EPHY_LED1		RGMII_MDIO_M	TE1	SEN_ELEC_PLS_M	GPIO1_A29	EPHY_LED1



B

EPHY

AX630C



D

A

B

C

D

