Miguel (Mike) Campos

(949) 633-8995   mcmikey.mail@gmail.com
26881 Carlota Dr
Mission Viejo CA 92691

Education

California State Polytechnic University, San Luis Obispo CA   Expected Graduation: June 2026
Bachelor of Science in Computer Engineering   GPA: 3.5
Relevant coursework stated under Experience.

Skills

- Can develop small programs and troubleshoot problems in Python and Java based on knowledge from computer science fundamentals, data structures, and object-oriented programming(OOP)
- Can work with logic gates, circuits, and knowledge covered in digital and computer design classes, along with the fundamentals of assembly languages
- Able to analyze and design intermediate circuits with memory/memoryless, linear/nonlinear Components
- Knowledge of Python, Java, Git/Github, Verilog/System Verilog, Vivado, RISC-V Assembly Language, Circuit Design, Microsoft Suite, Google Suite, AI
- Personal skills include but are not limited to being outgoing, patient, friendly, hardworking, and willing to work independently or with others.

Experience

Joven Q LLC   Summer 2023 & 2024
- Worked as a webmaster and logistics technician, helping set up the business website through Shopify, and setting up and maintaining third-party software including accounts on social media

Lantronix, Irvine   Summer 2022
- Worked as an IT specialist intern, handling any necessary work both physical and technical, dealt with some server set-up and setting up laptops for employees, selecting and replacing parts in computers and laptops, and general technical issues.

California State Polytechnic University   2022 – Present
- Completed a data structures class starting with different data types and going up to binary trees(including red and black trees), arrays, and hash tables (in Python)
- Completed an OOP class where we learned the essential tenants of OOP and certain programming strategies and shortcuts; demonstrated this through a final project implementing a small 2D world with entities and real-time response.
- Taken a computer design and assembly language programming class where we built on digital design principles of a previous class (logic gates, digital circuits), working with different types of memory, state machines, and addresses. Learned to utilize these concepts and components through Verilog/system Verilog and RISC-V Assembly Language
- Designed and implemented a fully functional RISC-V MCU/CPU with interrupts using Verilog and assembly for testing and implementation
- Completed a series of circuit design classes and labs with multi-loop, nonlinear circuits, dealing with real and complex poles;
- Designed and implemented a frequency-shift-keying transmitter-receiver pair as a final project using memory and memoryless, linear and nonlinear components of both active and passive variety
- Created a simple program in Python that scrapes data off a website in real-time and alerts user using a Raspberry Pi and peripherals
- Created a dictionary backed by hashing used to create a lempel ziv welch encoding and decoding program, a q sort function, and a directory lister program

Abiding Savior, Lake Forest   2017-Present
- Audio/video board technician - working with a small team of people to operate a sound and visual system