

College of DuPage
CIS2420 – Exam 1 – Part 2 of 2 (F)
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- 1) In your own words describe the difference between little endian order and big endian order.
Describe when endianness would be used by a computer system. Name the endian order used on the x86 processor.

- 1) Calculate the exact value for the largest possible 6-bit signed integer.

- 2) Create a truth table to show all possible inputs and outputs for the Boolean function $A \wedge \neg B$
Create a truth table to show all possible inputs and outputs for the Boolean function $\neg(\neg A \vee B)$
Describe the results of the two truth tables. Who derived the theorem describing what you see?

A **B**

7) Calculate the **pure binary** representation of each fractional decimal number

$$6 + (7/8) \text{ OR } (6.875_{10})$$

$$5 + (1/2) \text{ OR } (5.50_{10})$$

8) How many bits would be needed to represent the decimal number 1.70 in binary?

9) Given 8 binary bits of space after the decimal point, best approximate the decimal number 1.70

1. _ _ _ _ _ _ _ _

10) List three CPU status flags that are **neither the carry flag nor the parity flag**. Then briefly describe the purpose of each of your listed flags.

1) _____

2) _____

3) _____

11) Circle the correct answer. Variables (like BYTE, WORD, or DWORD) defined in the data segment of an assembly language program are by default: **SIGNED** **UNSIGNED**