

# RF Circuit Design

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L16

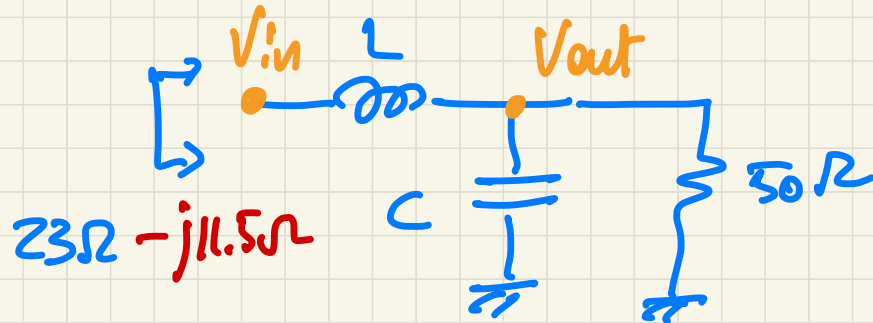
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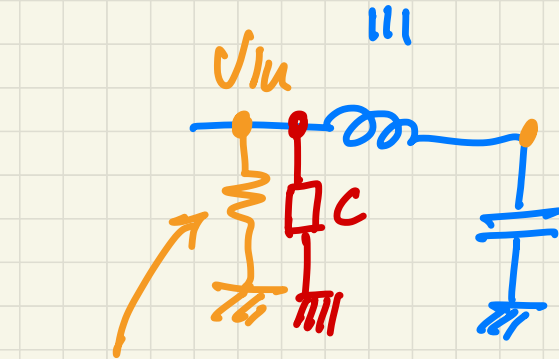
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$$f_0 = 2.56 \text{ Hz}$$

$$|V_{out}/V_{in}| = 1.32$$



$$\frac{23^2 + 11.5^2}{23} = 29\Omega$$

$$\frac{|V_{out}|^2}{50\Omega} = \frac{|V_{in}|^2}{29\Omega}$$

$\text{Re } Z_L$

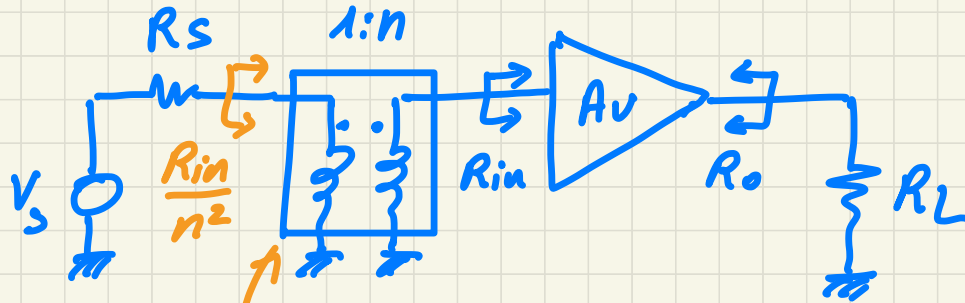
$$|V_{out}/V_{in}| = \sqrt{\frac{50}{29}}$$

$$= \frac{|V_{in}|^2}{29\Omega}$$

$\text{Re } Z_{in}$

$$= \sqrt{\frac{50}{29}} = 1.32$$

# Impedance Matching



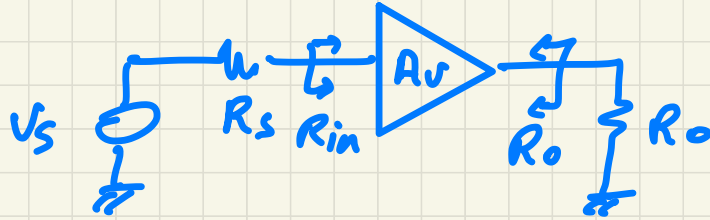
impedance transf.  
network  
(model)

$$\frac{V_{out}}{V_s} = \frac{\cancel{\frac{R_{in}}{n^2}}}{\cancel{\frac{R_{in}}{n^2}} + R_s n^2} \cdot n \cdot A_v \cdot \frac{R_L}{R_L + R_o}$$

unloaded  
voltage gain

$\alpha$  (input voltage division)

## \*\* Voltage amplifier design



$$\frac{V_{out}}{V_S} = \underbrace{\frac{R_{in}}{R_{in} + R_S}} \cdot A_v \cdot \underbrace{\frac{R_L}{R_o + R_L}} = \alpha \cdot A_v \cdot \frac{R_L}{R_o + R_L}$$

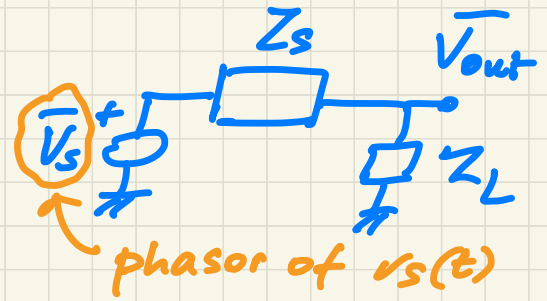
to maximize gain :  $R_{in} \gg R_S$   
 $R_L \gg R_o$

$$\left. \frac{V_{out}}{V_S} \right|_{max} \rightarrow A_v \quad (\text{unloaded gain})$$

## \*\* Max. power transfer theorem

For a given source impedance  $Z_s$   
max. power at load is obtained

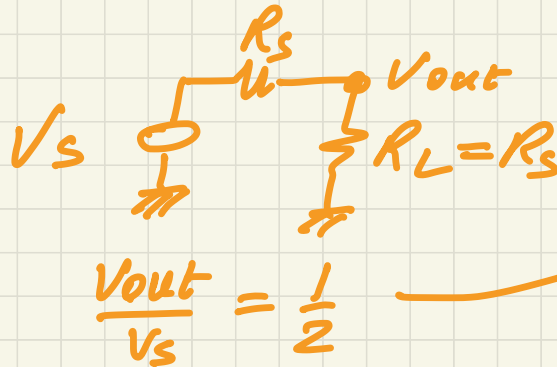
with  $Z_L = Z_s^*$  (conjugate matching)

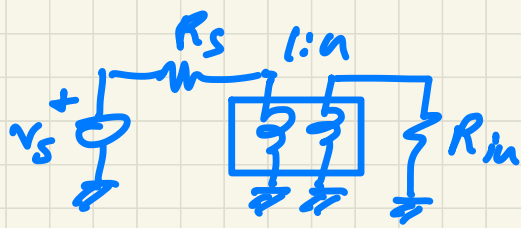


Available power:

$$Z_L = R_L + jX_L$$

$$P_{L,av} = \frac{|\bar{V}_{out}|^2}{2R_L} = \frac{|\bar{V}_s|^2}{8R_L}$$





maximize  $\alpha$  :

$$\alpha = \frac{n R_{in}}{R_{in} + n^2 R_s}$$

$$\frac{\partial \alpha}{\partial n} = \frac{\underbrace{R_{in}^2 + n^2 R_{in} R_s}_{R_{in}(R_{in} + n^2 R_s)} - \underbrace{n R_{in} \cdot 2n R_s}_{2n^2 R_{in} R_s}}{(R_{in} + n^2 R_s)^2}$$

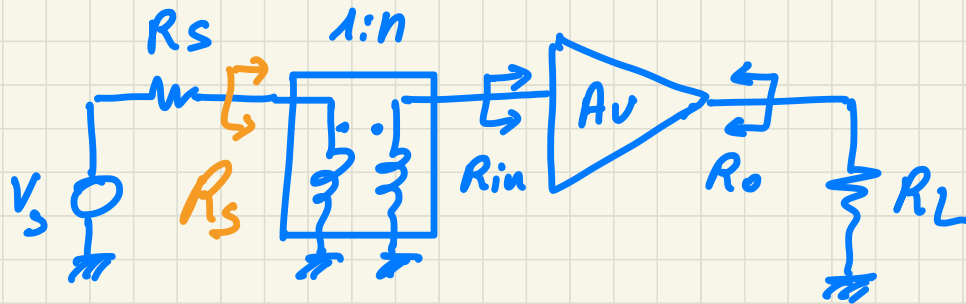
$$\frac{\partial \alpha}{\partial n} = 0 \quad \Leftrightarrow \quad R_{in}^2 = n^2 R_{in} R_s \quad \Leftrightarrow \quad n_{opt} = \sqrt{\frac{R_{in}}{R_s}}$$

(hyp.  $R_{in} \neq 0$ )

maximum of  $\alpha$  :  $\alpha_{max} = \frac{n R_{in}}{2 R_{in}} = \frac{1}{2} \sqrt{\frac{R_{in}}{R_s}}$

$$\boxed{\frac{R_{in}}{n^2} = R_s}$$

Transformed input.  
MATCHED to the  
source impedance



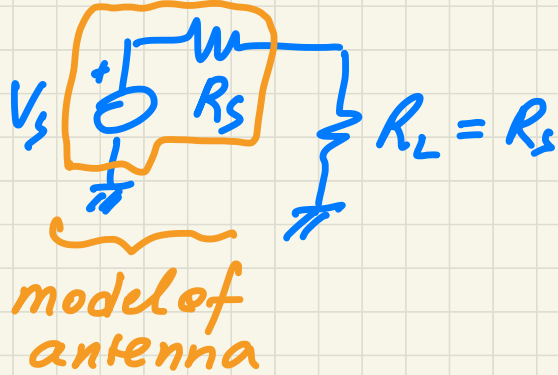
input matching  
maximizes voltage  
gain

$$\frac{V_{out}}{V_s} \Big|_{max} = \underbrace{\frac{1}{2} \cdot n_{opt}}_{\alpha_{max}} \cdot A_v \cdot \frac{R_L}{R_L + R_o}$$

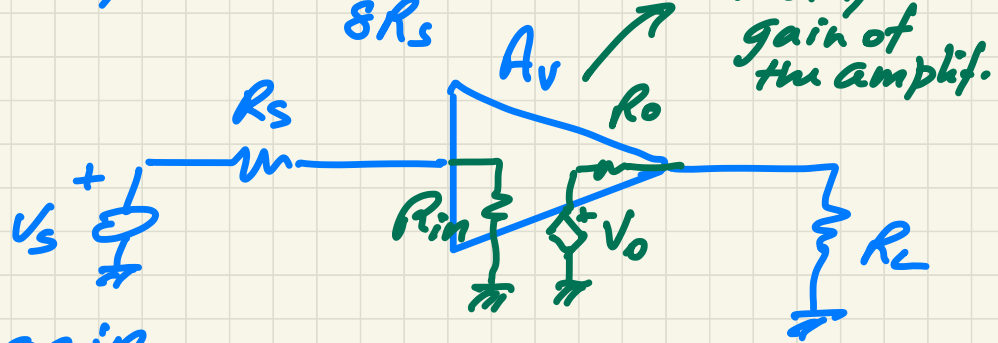
e.g.  $R_s = 50 \Omega$   
 $R_{in} = 1 k\Omega$

$$n_{opt} = \sqrt{\frac{1k}{50}} = 4.47 \quad \Rightarrow \quad \alpha_{max} = \frac{1}{2} \cdot 4.47 = 2.24$$

# Power Gain



$$P_{out, av} = \frac{V_s^2}{8R_s}$$



Available power gain  
(max. achievable power gain)

$$G_A = \frac{P_{out, av}}{P_{in, av}} = \frac{V_o^2 / 8R_o}{V_s^2 / 8R_s} = \left( \frac{V_o}{V_s} \right)^2 \cdot \frac{R_s}{R_o}$$



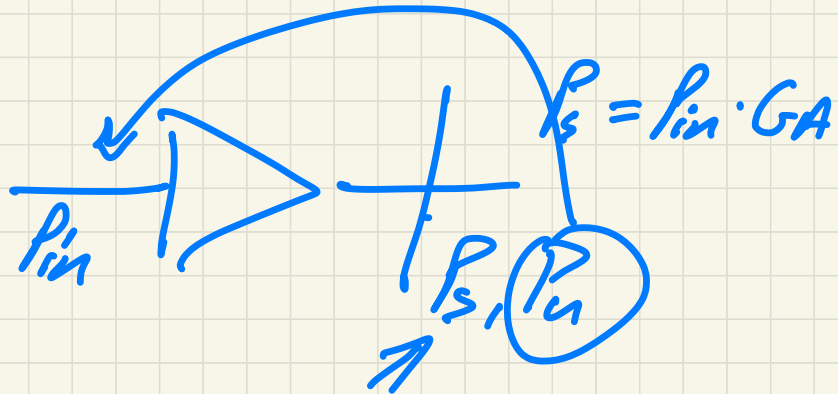
$$\Rightarrow \text{av. power gain} \quad G_A = (\alpha \cdot A_V)^2 \cdot \frac{R_S}{R_O} =$$

$$= A_O^2 \cdot \frac{R_S}{R_O}$$

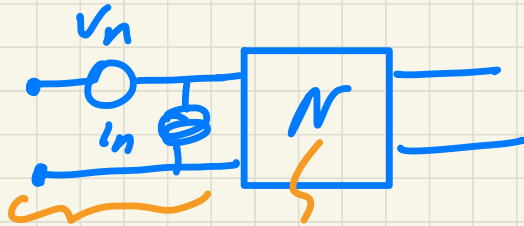
In general: a. power gain  $\neq$  the square of voltage gain. This is true ONLY when  $R_S = R_O$ :

$$G_A = A_O^2$$

$$\text{SNR} = \frac{P_{in}}{(P_u/G_A)}$$



# Effects of Noise

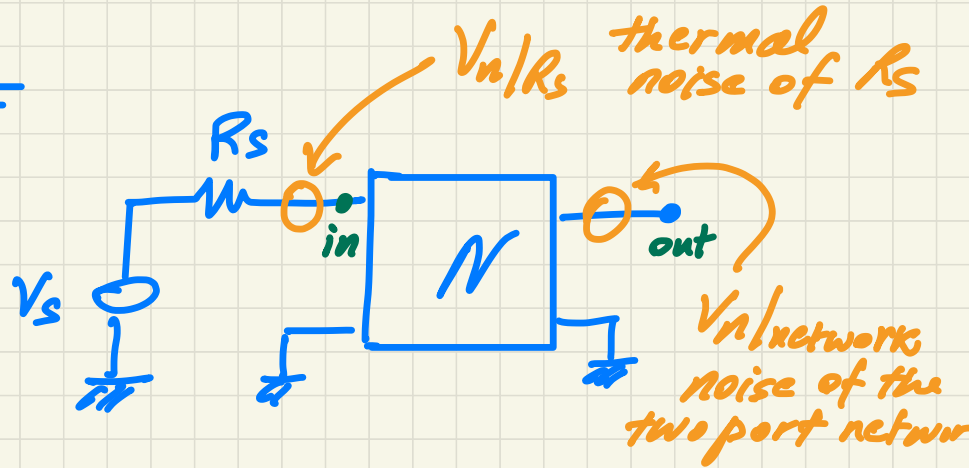


Two port network

2 equivalent generators modelling noise of the network

$$= \frac{\overline{V_{sin}^2}}{\overline{V_{nin}^2}} \cdot \frac{\overline{V_{nout}^2}}{\overline{V_{sout}^2}}$$

$$A_0 = V_{sout} / V_{sin}$$



Noise Figure

$$NF \triangleq \frac{SNR_{in}}{SNR_{out}} =$$

$$= \frac{1}{A_0^2} \cdot$$

$$\frac{\overline{V_{nnetwork}^2} + A_0^2 \overline{V_{nRs}^2}}{\overline{V_{nRs}^2}}$$

$$NF = \frac{1}{A_o^2} \cdot \left( \frac{\overline{V_{n\text{network}}^2}}{\overline{V_{nR_s}^2}} + A_o^2 \right) =$$

① →

$$= 1 + \frac{\overline{V_{n\text{network}}^2} / A_o^2}{\overline{V_{nR_s}^2}} =$$

noise of the network (input-referred)

noise of source resistance

② →

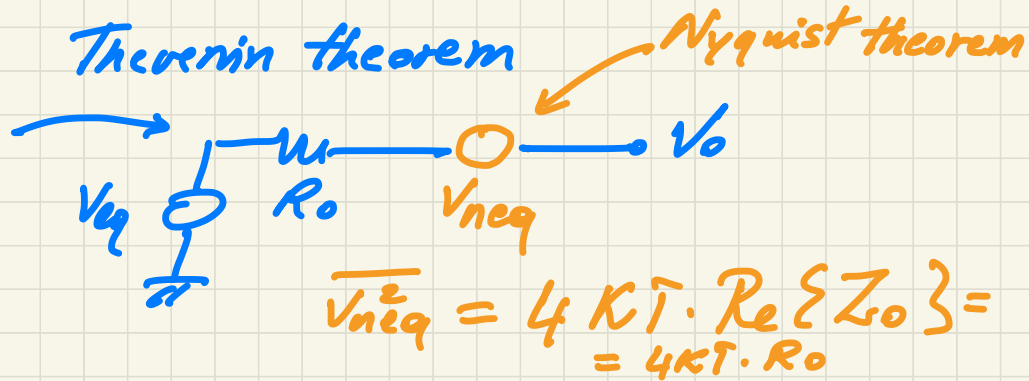
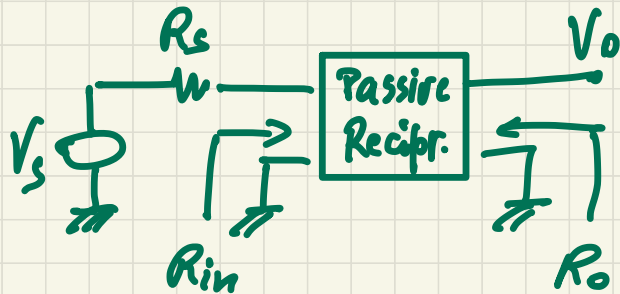
$$= \frac{\overline{V_{n\text{out, total}}^2} / A_o^2}{\overline{V_{nR_s}^2}}$$

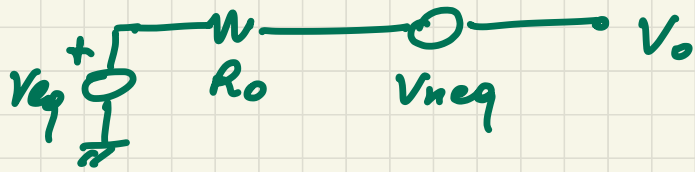
noise of network + source resistance (input-referred)

$$\frac{\overline{V_{out}^2}_{total}}{A_o^2} = \underbrace{NF}_{\text{factor that multiplies source noise to get total noise (input referred)}} \cdot \overline{V_{n_s}^2}$$

$$\Rightarrow PSD_{RX(\text{input ref.})} = 4kTR_s \cdot NF_{RX}$$

NOISE FIGURE of a lossy circuit (eg passive filter)





$$A_o = \frac{V_{eq}}{V_s}$$

$$NF = \frac{\overline{V_{n_{out\ total}}^2} / A_o^2}{\overline{V_{n_{Rs}}^2}} = \frac{\overline{V_{neg}^2}}{A_o^2} \cdot \frac{1}{\overline{V_{n_{Rs}}^2}} =$$

$$= \frac{\cancel{4kTR_o}}{A_o^2} \cdot \frac{1}{\cancel{4kTR_s}} =$$

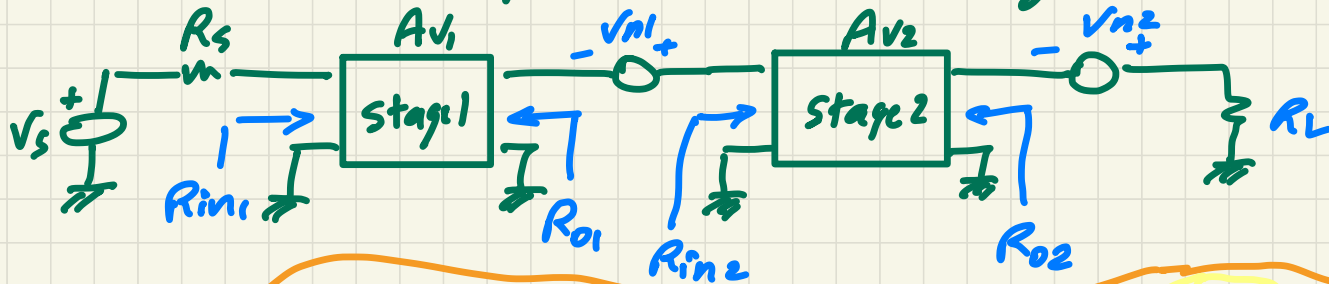
$$= \frac{1}{A_o^2 \cdot \frac{R_s}{R_o}} = \frac{1}{G_A} = L_A \text{ available power loss}$$

available power gain  $G_A$

⇒ Noise figure of a lossy circuit is given by its available power loss

eg. filter with 2 dB power loss → NF = 2 dB

NOISE FIGURE of cascaded stages

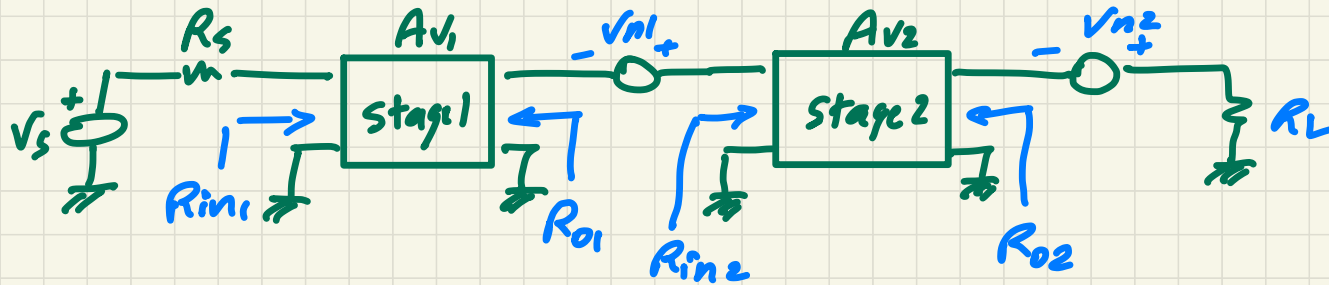


$$NF = 1 + \underbrace{\frac{\overline{V_{n1}^2}}{\alpha_1^2 A_{v1}^2}}_{A_{o1}^2} \cdot \frac{1}{4kTR_s} + \frac{\overline{V_{n2}^2}}{\alpha_1^2 A_{v1}^2 \cdot \alpha_2^2 A_{v2}^2} \cdot \frac{1}{4kTR_s}$$

$NF_1$

$$\alpha = \frac{R_{in1}}{R_{in1} + R_s}$$

$$\alpha_2 = \frac{R_{in2}}{R_{in2} + R_{o1}}$$



$$NF_2 \Big|_{R_{o1}} = 1 + \frac{\overline{V_{n2}^2}}{\alpha_2^2 \cdot A_{v2}^2} \cdot \frac{1}{4KT R_{o1}}$$

$$\Rightarrow NF = NF_1 + \frac{(NF_2|_{R_{o1}} - 1) \cdot \cancel{4KT R_{o1}}}{\alpha_1^2 A_{v1}^2 \cdot \cancel{4KT R_s}}$$

$$= NF_1 + \frac{NF_2|_{R_{o1}} - 1}{GA_1}$$

$$\alpha_1^2 A_{v1}^2 \cdot \frac{R_s}{R_{o1}} = GA_1$$

available power gain of stage 1

$$\begin{aligned}
 \Rightarrow NF = 1 + & \underbrace{(NF_1 - 1)}_{\text{stage 1}} + \underbrace{\frac{NF_2 |_{R_{o1}} - 1}{GA_1}}_{\text{stage 2}} + \\
 & + \underbrace{\frac{NF_3 |_{R_{o2}} - 1}{GA_1 GA_2}}_{\text{stage 3}} + \dots
 \end{aligned}$$

$\Rightarrow$  First stages most critical for noise figure