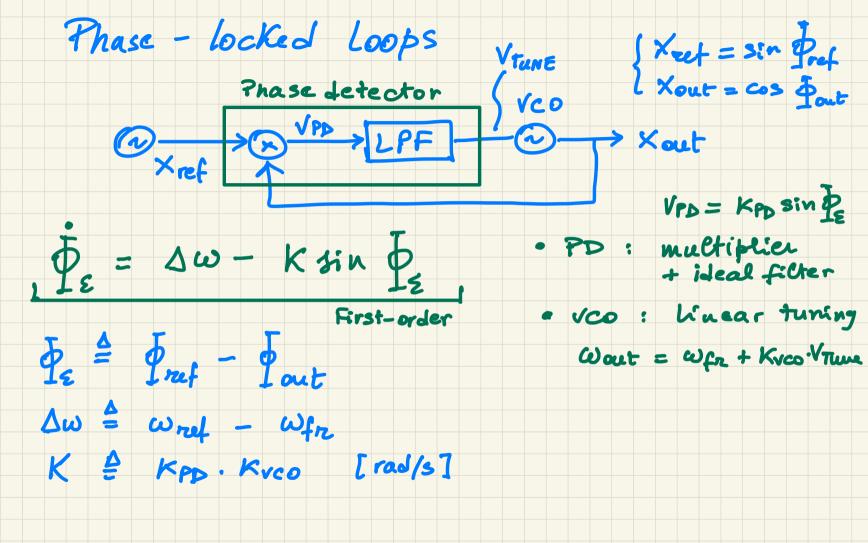
RF Circuit Design

<u>L</u>#



$$\frac{1}{4} = 0 \qquad \text{Equibrium} \qquad (\text{ phase error is no longer } Variable \Rightarrow \text{ it is constain})$$

$$\omega_{\mathcal{E}} = \frac{1}{4} = 0 \qquad (\text{ frequency error : } \omega_{\text{ref}} - \omega_{\text{out}})$$

$$\Rightarrow PLL \text{ is in "lock" State } (\omega_{\text{out}} = \omega_{\text{ref}})$$

$$\frac{1}{4} = \frac{1}{4} =$$

wout (t) = wfr + Krco (VTune) = wfr + K sin \(\overline{\psi}_{\mathscr{E}}(t) \)

If
$$\Delta\omega > 1 \Rightarrow \Phi_{\varepsilon} > 0$$
 what $\Delta\omega = \omega_{fr} + \kappa$ sin Φ_{ε} in $\Phi_$

LOCK RANGE:
$$\Delta\omega_L = k$$

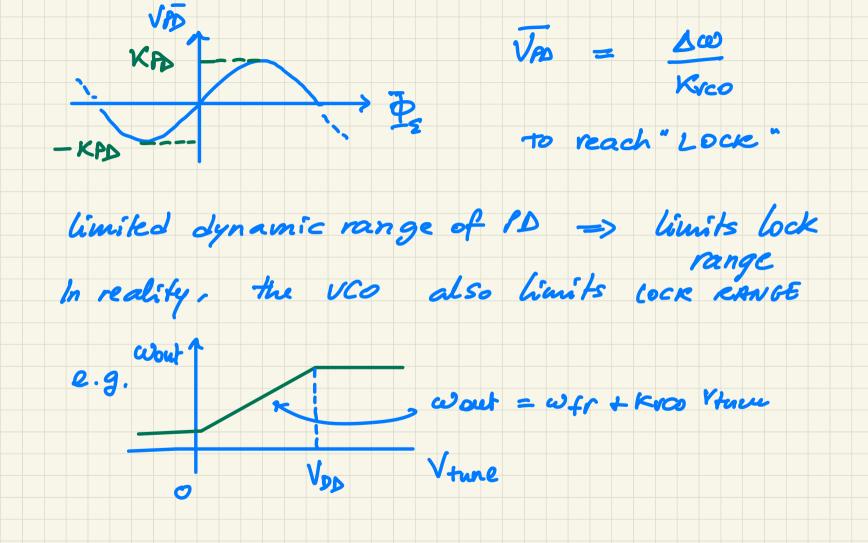
Intuitive interpretation (2) Xreet / Vp 1 PF KVCO CO > Vrune = VPD INPOSING "LOCK" imposing equality

at skeady

state

= wref wout = wfr + Kveo Voure wref - wfr Vtune = $\sqrt{p_D} = Kp_D \cdot \sin \bar{\Phi}_{\mathcal{E}} = \Delta \omega$; $\sin \bar{\Phi}_{\mathcal{E}} = \Delta \omega$ Kinco

Kinco \Rightarrow $\sin \frac{\pi}{2} = \frac{\Delta \omega}{\kappa}$



: linearitation Perturbation analysis J_ε = Δω - κ sin F_ε $\left|\frac{\Delta\omega}{\kappa}\right|<1$ stable equil. exists IF Δω = 0 ; · Δ Ф 2 « 1 rad Small $\Phi_{\varepsilon} = - \kappa \sin \Phi_{\varepsilon}$ perturbation ON DE $\overline{\phi}_{\xi}(t) = \varphi_{\xi_0} e^{-kt}$ a'mari ration $\Phi_{\xi} = -\kappa \Phi_{\xi}$ φ_{ε} φ_{ε} T = 1/k is the time constant of the system

Laplace transformation
$$\dot{\Phi}_{\varepsilon} = -K \dot{\Phi}_{\varepsilon}$$
• Input - to - Output

transfer function of PU
$$\dot{\Phi}_{\varepsilon} = -K \dot{\Phi}_{\varepsilon}$$
• Input - to - Output
$$\dot{\Phi}_{\varepsilon} = -K \dot{\Phi}_{\varepsilon}$$
• Pole at $S = -K$

Wout = C for + C vo Vtune (t) =
$$\dot{\Phi}_{\varepsilon} = -K \dot{\Phi}_{\varepsilon}$$
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$$\frac{1}{ABSOUTE} = \int_{-\infty}^{t} \omega_{out}(t') dt' = ABSOUTE$$

$$\frac{1}{PHASE} = \omega_{out}, o \cdot t + P_{out}(t)$$

$$\frac{1}{Excess}$$

$$\frac{1}{PHASE}$$

$$\frac{1}{Phase}$$

$$\frac{1}{Pout}(t) = Kveo \cdot KPD \left[P_{ref}(t) - P_{out}(t) \right]$$

$$\frac{1}{K}$$

$$\frac{1}{ABSOUTE}$$

$$\frac{1}{PHASE}$$

$$\frac{1}{Phase}$$

$$\frac{1}{Phase}$$

$$\frac{1}{Pout}(t) = Kveo \cdot KPD \left[P_{ref}(t) - P_{out}(t) \right]$$

$$\frac{1}{K}$$

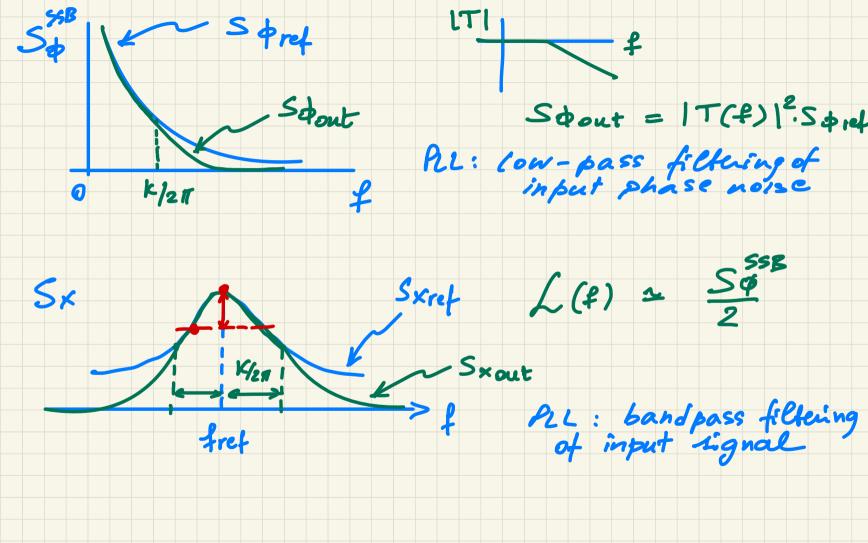
$$\frac{1}{Pout} = K \left[\frac{1}{Pref}(s) - \frac{1}{Pref}(s) \right]$$

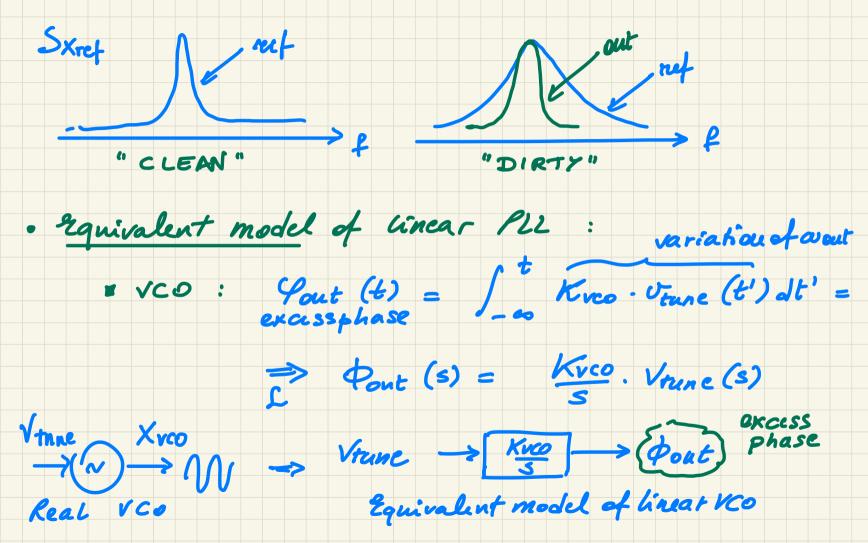
$$\frac{1}{Pout} = \frac{1}{Pref}(s)$$

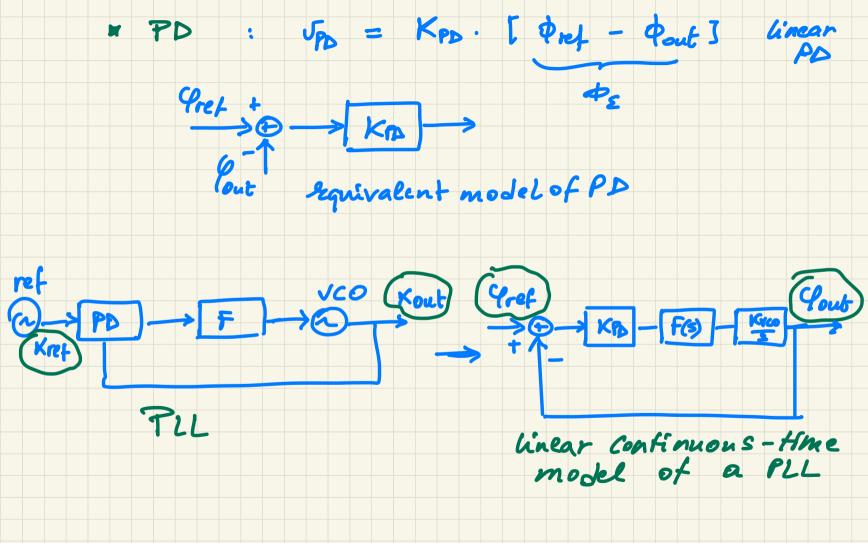
$$\frac{1}{Pout} = \frac{1}{Pref}(s)$$

1 K lwl $T(s) = \frac{K}{1+K}$ T(s) = Pout = s. Pout = wowt

Pref s. Pref Wref in this PLL, the vco "follows" Interpretation: the phase and the frequency of the reference clock with wow = K Only slow variations of pref (or wref) are followed by the VCO







Second - order PLLs

$$F(s) = \frac{1}{1+s^2} \Rightarrow LG(s) = \frac{K}{3} \cdot \frac{1}{(1+s^2)}$$

[16] Bode diagram

$$Pref \rightarrow LG(s) \Rightarrow Pout$$

$$T(s) = \frac{Pout}{Pref} \Rightarrow LG(s) \Rightarrow Re(s)$$

$$-\frac{1}{1+LG(s)}$$

$$T(s) = \frac{K}{s} \frac{1}{1+s\pi}$$

$$1 + \frac{K}{s} \frac{1}{1+s\pi}$$

$$2 + \frac{K}{s} \frac{1}{s\pi}$$

$$2 + \frac{K}{s\pi}$$

$$3 + \frac{K}{s\pi}$$

$$4 + \frac{K}$$