Critical path estimation in heterogeneous scheduling heuristics

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1 Introduction

Recall from previous chapters that the Heterogeneous Earliest Finish Time (HEFT) heuristic prioritizes all tasks t_i , $i=1,\ldots,n$, by recursively computing a corresponding sequence of numbers u_i which are intended to represent critical path lengths from each task to the end. As noted previously, however, the concept of the critical path is not clearly defined for heterogeneous target platforms: DAG weights are not fixed at this stage so there are multiple ways we could define a longest (i.e., costliest) path. Consider for example the simple DAG shown in Figure 1, where the labels represent all the possible weights each task/edge may take on a two-processor target platform; specifically, the labels (W_i^1, W_i^2) near the nodes represent the computation costs on processors P1 and P2, respectively, while the edge labels $(0 = W_{ik}^{11} = W_{ik}^{22}, W_{ik}^{12}, W_{ik}^{21})$ represent the possible communication costs. What is the longest path through this graph?

It is not obvious how this should be defined. The HEFT approach, as described in previous chapters, is to use mean values over all sets of possible costs to fix the DAG weights and then compute the critical path lengths in a standard dynamic programming manner. For the example, this gives

$$u_6 = 2.5, u_5 = 7, u_4 = 7.75, u_3 = 16,$$

 $u_2 = 5.75, u_1 = 16.25, and u_0 = 24.75,$

giving a scheduling priority list of $\{0, 1, 3, 4, 5, 2, 6\}$. The schedule length obtained by HEFT with these priorities is 22, so note in particular that u_0 , the rank of the single entry task, is not a lower bound on this value, unlike in the homogeneous case. The question then is, what quantity do the u_i values actually represent? The most intuitive interpretation is perhaps that the HEFT ranks are estimates

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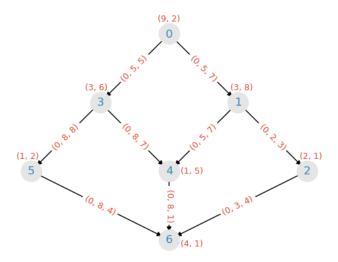


Figure 1: Simple task graph with costs on a two-processor target platform.

of what the critical paths are *likely* to be but there is no robust mathematical justification for believing that such a definition is truly the most useful.

Given this ambiguity, alternative ways to define the critical path in HEFT have been considered before, most notably by Zhao and Sakellariou [10], who empirically compared the performance of HEFT when averages other than the mean (e.g., median, maximum, minimum) are used to compute upward (or downward) ranks. Their conclusions were that using the mean is not clearly superior to other averages, although none of the other options were consistently better. Indeed, perhaps the biggest takeaway from their investigation was that HEFT is very sensitive to how priorities are computed, with significant variation being seen for different graphs and target platforms. In this chapter we undertake a similar investigation with the aim of establishing if there are choices which do consistently outperform the standard HEFT task ranking phase.

This will be an empirically-driven study, as is common in this area. To facilitate this investigation we created a software package that simulates heterogeneous scheduling problems, much like that described in the previous chapter, although not restricted to accelerated target platforms. As before, the (Python) source code for this simulator can be found on Github¹ and all of the results presented here can be re-run from scripts contained therein.

2 Optimistic bounds

Functionally, the critical path is used in HEFT as a lower bound on the makespan, so that minimizing the critical path gives us the most scope to minimize the

¹https://github.com/mcsweeney90/critical-path-estimation

Table 1: Upward and optimistic ranks.

Task	Upward rank	Optimistic rank
0	24.75	16
1	16.25	8
2	5.75	2
3	16	8
4	7.75	5
5	7	3
6	2.5	1

makespan (assuming we make good use of our parallel resources). With this in mind, there are many different ways we can define the critical path so that it gives a lower bound on the makespan of any possible schedule. The most straightforward approach would be to just set all weights to their minimal values but a tighter bound can be computed in the following manner. First, define O_i^a for all tasks t_i and processors p_a to be the critical path length from t_i to the end (inclusive), assuming that it is scheduled on processor p_a . These values can easily be computed recursively by setting $O_i^a = W_i^a \, \forall a$ for all exit tasks then moving up the DAG and setting

$$O_i^a = W_i^a + \max_{k \in S_i} \left(\min_{b=1,\dots,q} \left(O_k^b + W_{ik}^{ab} \right) \right) \quad \forall a \tag{1}$$

for all other tasks. Then for each $i = 1, \ldots, n$,

$$O_i = \min_{a=1,\dots,q} O_i^a \tag{2}$$

gives a true lower bound on the remaining cost of any schedule once the execution of task t_i begins. These O_i values could be useful as alternative task priorities in HEFT, especially since the cost of computing all of the O_i^a in this manner is only $O(m+n) \approx O(n^2)$ so in particular is no more expensive than the usual HEFT prioritization phase. For example, for the simple graph shown in Figure 1, we find that the O_i values are as given in Table 1 (with the u_i included for comparison). Interestingly, we see that tasks 1 and 3 have the same optimistic rank (8) and the performance of the alternative ranks relative to the standard u_i sequence in HEFT depends on which is chosen to be scheduled first; if task 1, the priority list does not change so the schedule makespan is 22, but if task 3 is selected instead, the final schedule makespan is 20—smaller than the original.

Of course, this is only one example: it should be emphasized here that there is absolutely no mathematically valid reason to suppose that using the O_i sequence

instead of u_i as the task ranks in HEFT will actually lead to superior performance in general. Still, it seems worthwhile to investigate this empirically using our simulator, which we do in Section 4.

(Note that the optimistic critical path defined here is extremely similar to the optimistic cost used in the PEFT heuristic; this will be discussed further in Section 5.)

3 Stochastic interpretation

In this section we propose a family of alternative task ranking phases in HEFT based on the following interpretation of the standard ranking phase. First, note that by using average values over all sets of possible task and edge costs, HEFT is implicitly assuming that any member of any set is just as likely to be incurred as any other; conceptually, HEFT is attempting to account for the uncertainty of the processor selection phase by assuming that for any given task all processors are equally likely to ultimately be chosen. So, effectively, at the prioritization phase HEFT views the node and edge weights as independent discrete random variables (RVs) with associated probability mass functions (pmfs) given by the aforementioned assumption. More precisely, let m_i be the pmf corresponding to the task weight variable w_i and m_{ik} that for the edge weight w_{ik} , then

$$m_i(W_i^a) := \mathbb{P}[w_i = W_i^a] = \frac{1}{n_p} \quad \forall a$$

and

$$m_{ik}(W_{ik}^{ab}) = m_i(W_i^a) \cdot m_k(W_k^b)$$
$$= \frac{1}{n_p^2} \quad \forall a, b.$$

Note that the expected values of the node and edge weights are therefore given by

$$\mathbb{E}[w_i] = \sum_{\ell \in L_i} \ell m_i(\ell) = \frac{1}{n_p} \sum_a W_i^a \tag{3}$$

$$\mathbb{E}[w_{ik}] = \sum_{\ell \in L_{ik}} \ell m_{ik}(\ell) = \frac{1}{n_p^2} \sum_{a,b} W_{ik}^{ab}.$$
 (4)

In particular, this means that $\mathbb{E}[w_i] = \overline{w_i}$ and $\mathbb{E}[w_{ik}] = \overline{w_{ik}}$ so that the computation of the upward ranks u_i can instead be done by setting $u_i = \mathbb{E}[w_i]$ for all exit tasks, then moving up the DAG and recursively computing

$$u_i = \mathbb{E}[w_i] + \max_{k \in S_i} \left(u_k + \mathbb{E}[w_{ik}] \right) \tag{5}$$

for all other tasks.

In summary, since all possible node and edge weights are known but their actual values at runtime aren't (at least without restricting the processor selection phase), HEFT estimates critical path lengths from all tasks in a task graph G through a two-step process:

- 1. An associated graph G_s —referred to as stochastic because all of its weights are RVs—is implicitly constructed with node and edge pmfs m_i and m_{ik} as defined above.
- 2. The numbers u_i are recursively computed for all tasks in G_s using (5), and taken as the critical path lengths from the corresponding tasks in G.

In the following two sections, we propose modifications of both steps so as to obtain different critical path estimates that may be used as task ranks in HEFT. The performance of these will then be evaluated through extensive numerical simulations in Section 4.

3.1 The critical path of G_s

A natural question arises from the interpretation outlined in the previous section: what is the relationship between the sequence of numbers u_i as defined by (5) and the critical path of the stochastic graph G_s ? (Of course, since all of the weights are RVs, the critical path of G_s is itself stochastic.) In fact, it has long been known in the context of Program Evaluation and Review Technique (PERT) network analysis that the numbers u_i are lower bounds on the expected value of the critical path lengths of the stochastic DAG. This result dates back at least as far as Fulkerson [4], who referred to it as already being widely-known and gave a simple proof. This prompts another question: does using the actual expected values of the critical path lengths as the task priories in HEFT lead to superior performance?

Unfortunately, computing the moments of the critical path length of a graph whose weights are discrete RVs was shown to be a #P-complete problem by Hagstrom [5]. This means that it is generally impractical to compute the true expected values. However, efficient methods which yield better approximations than the u_i numbers are known; we discuss examples in the following two sections.

3.1.1 Monte Carlo sampling

Monte Carlo (MC) methods have a long history as a means of approximating the longest path distribution for PERT networks, dating back to at least the early 1960s [9]. The idea is to simulate the realization of all RVs (according to their pmfs) and then evaluate the critical path of the resulting deterministic graph. This is done repeatedly, giving a set of critical path instances whose

Table 2: Monte Carlo estimates of critical path lengths for example graph.

Task	u_i	MC1	MC10	MC100	MC1000
0	24.75	29	26.8	29.9	28.9
1	16.25	22	17.0	17.4	17.0
2	5.75	3	5.4	6.0	5.8
3	16	20	18.9	19.0	18.6
4	7.75	14	7.2	7.9	7.8
5	7	6	5.3	7.1	7.0
6	2.5	1	1.9	2.5	2.5

empirical distribution function is guaranteed to converge to the true distribution by the Glivenko-Cantelli theorem [1]. Furthermore, analytical results allow us to quantify the approximation error for any given the number of realizations—and therefore the number of realizations needed to reach a desired accuracy.

Table 2 illustrates how our estimate of the expected critical path lengths of the stochastic graph in Figure 1 evolve as the number of realizations increases; the corresponding u_i numbers are included as well to show that they do indeed appear to be a lower bound on the values that the Monte Carlo method appear to be converging toward. More importantly, we see that somewhere between 1 and 10 realizations, the critical path length estimate for task 3 begins to exceed that of task 1 so it would have a higher priority if these estimates are taken as task ranks in HEFT; as noted in the previous section, interchanging these two tasks—and keeping the same ordering for all others—leads to a smaller schedule makespan (20) compared to the standard ranking (22). Of course, this is only one example, but it does serve to illustrate that in some cases at least a tighter bound on the critical path of G_s can be useful.

The downside of Monte Carlo sampling is its cost. While modern architectures are well-suited to this approach because of their parallelism, this approach still may be impractical in the context of a scheduling heuristic; we often found this to be the case in the examples discussed in Section 4. Hence in this report we typically only use the Monte Carlo method as a means of obtaining a reference solution; see, for example, the following section.

3.1.2 Fulkerson's bound

For all i = 1, ..., n, let c_i be the critical path length from task t_i to the end and let $e_i = \mathbb{E}[c_i]$ be its expected value. In addition to proving that the u_i sequence defines lower bounds on the critical path lengths, i.e., $u_i \leq e_i \ \forall i$, Fulkerson also showed how an alternative sequence of numbers which give tighter bounds can

easily be constructed.

First we assume that G_s is expressed in an equivalent formulation without node weights. The most straightforward way to do this is to simply redefine the edge weights so that they also include the computation cost of the parent task and, if the child task is an exit, the computation cost of the child as well. More precisely, we define a new set of possible edge weights \tilde{L}_{ik} by

$$\tilde{L}_{ik} = \{\tilde{W}_{ik}^{ab} := W_i^a + \delta_k W_k^b + W_{ik}^{ab}\}_{a,b=1,\dots,q},$$

where $\delta_k = 1$ if t_k is an exit task and zero otherwise. We also define a new edge weight variable $\tilde{w}_{ik} \in \tilde{L}_{ik}$ and new edge pmfs \tilde{m}_{ik} for which $\tilde{m}(\tilde{W}_{ik}^{ab}) \equiv m(W_{ik}^{ab})$. Note that removing the node weights is not strictly necessary but simply makes the elucidation cleaner so we should emphasize that all of the following still holds, with only minor adjustments, if this is not done.

Now, for i = 1, ..., n, define Z_i to be the set of all weight RVs corresponding to edges downward of t_i (i.e., the remainder of the graph). Let R_i be the set of all possible realizations of the RVs in Z_i . Given a realization $z_i \in R_i$, let $\ell(z_i)$ be the critical path length from task t_i to the end. Then by the definition of the expected value we have

$$e_i = \sum_{z_i \in R_i} \mathbb{P}[Z_i = z_i] \ell(z_i). \tag{6}$$

Let $C_i := \{w_{ik}\}_{k \in S_i}$ be the set of all the weight RVs corresponding to edges which connect task t_i to its children. Note that

$$Z_i = C_i \cup_{k \in S_i} Z_k$$

and any realization $z_i \in R_i$ can therefore be expressed as $z_i = c_i \cup_{k \in S_i} z_k^i$, where $z_k^i \in R_k$ and $c_i = \{z_{ik}^i\}_{k \in S_i}$ is the set of realizations of the edge weight RVs in C_i . In particular, this means that we can write

$$\ell(z_i) = \max_{k \in S_i} \{\ell(z_k^i) + z_{ik}^i\}.$$

Furthermore, by the independence assumptions made, we have that

$$\mathbb{P}[Z_i = z_i] = \mathbb{P}[C_i = c_i] \prod_{k \in S_i} \mathbb{P}[Z_k = z_i^k].$$

This means that we can rewrite equation (6) as

$$e_{i} = \sum_{z_{i} \in R_{i}} \mathbb{P}[Z_{i} = z_{i}]\ell(z_{i})$$

$$= \sum_{c_{i}} \sum_{\substack{z_{k} \in R_{k}, \\ k \in S_{i}}} \mathbb{P}[C_{i} = c_{i}]\mathbb{P}[Z_{k} = z_{k}] \max_{k \in S_{i}} \{\ell(z_{k}) + z_{ik}\},$$
(7)

where z_{ik} is the realization of the edge weight RV w_{ik} defined by the set of realizations z_k .

It is relatively straightforward to show that the identity $u_i \leq e_i$ holds by manipulating equation (7); the reader is directed to Fulkerson's original paper for details [4]. Moreover, suppose we define a sequence of numbers by $f_i = 0$, if t_i is an exit task, and

$$f_{i} = \sum_{z_{i} \in R_{i}} \mathbb{P}[Z_{i} = z_{i}] \max_{k \in S_{i}} \{f_{k} + z_{ik}\}$$

$$= \sum_{c_{i}} \sum_{\substack{z_{k} \in R_{k}, \\ k \in S_{i}}} \mathbb{P}[C_{i} = c_{i}] \mathbb{P}[Z_{k} = z_{k}] \max_{k \in S_{i}} \{f_{k} + z_{ik}\},$$
(8)

for all other tasks, then Fulkerson showed that $u_i \leq f_i \leq e_i$ also holds—i.e, the f_i give a tighter bound on the expected values of the critical path lengths.

To compute each of the f_i using (8) we need to do an awful lot of work: suppose t_i has K children, then for any one of them t_k we need to do $O(|\tilde{L}_{ik}|)$ operations, i.e., $O(|\tilde{L}_{ik}|^K)$ in total. In general, $|\tilde{L}_{ik}|$ can be $O(p^2)$ and K can be $O(n^2)$ so computing the f_i in this manner is not always practical. Fortunately, a more efficient method was given by Clingen [2] in the context of extending Fulkerson's method to the case where edge weights are modeled as continuous random variables, although here we follow the slightly more compact notation of Elmaghraby [3].

It is well-known that the cumulative probability mass function of the maximum of a finite set of discrete RVs is equal to the product of the individual cumulative pmfs of the RVs. Let M_{ik} be the cumulative pmf along edge (t_i, t_k) , so that $M_{ik}(x) = \mathbb{P}[\tilde{w}_{ik} \leq x]$. Define the related function $M_{ik}^*(x) = \mathbb{P}[\tilde{w}_{ik} < x]$. Let Z_i be the set of all possible values of $f_k + \tilde{w}_{ik}$, for $k \in S_i$, and let z run over all elements of Z_i . For $i = 1, \ldots, n$, define

$$\alpha_i = \max_{k \in S_i} (f_k + \min(\tilde{L}_{ik})). \tag{9}$$

Then, with the cost independence assumptions we have already made, we can rewrite equation (8) as

$$f_{i} = \sum_{z > \alpha_{i}} z \left(\prod_{k \in S_{i}} M_{ik}(z - f_{k}) - \prod_{k \in S_{i}} M_{ik}^{*}(z - f_{k}) \right).$$
 (10)

A complete description of a practical procedure for computing the Fulkerson numbers f_i is given in Algorithm 1. At first blush this may not appear to be any simpler than before but, crucially, the number of operations required to compute each of the f_i is now $O(|\tilde{L}_{ik}| \cdot K)$, where K is the number of child tasks, rather than the first term being exponential in the second as before. Of course, it should also be noted that this procedure is still more expensive than computing the u_i sequence.

Table 3: Fulkerson numbers for example graph.

Task	u_i	f_i	MC1000
0	24.75	28.2	
1	16.25	16.9	
2	5.75	5.75	
3	16	18.1	
4	7.75	7.75	
5	7	7	
6	2.5	0	

Once all of the f_i have been computed, they can be taken as alternative task ranks in HEFT (or indeed any other listing heuristic). In Table 3 we compare the f_i values to the standard u_i ones for the small DAG from Figure 1; also included is MC1000, the critical path estimates based on 1000 Monte Carlo realizations (see Section 3.1.1), as a reference for the true critical path of the stochastic graph. We see that the f_i values do indeed give tighter bounds than the u_i and, more pertinently, if we use the former as task ranks then task 3 is scheduled before task 1 and we obtain the smaller schedule makespan of 20 rather than the standard HEFT makespan of 22.

Again, this is a single example, deliberately chosen for this behavior: although the f_i give tighter bounds on the critical path lengths of the stochastic DAG G_s there is absolutely no guarantee that this will lead to superior performance in general. After all, G_s itself is only a model of how we expect the processor selection phase to proceed—one that we know is inaccurate since, for example, it implicitly assumes that all task and edge weights are independent. Indeed, without this independence assumption it is well-known that the relation $u_i \leq f_i$ does not necessarily hold even for G_s ; Fulkerson himself presented examples [4]. Still, we think this is a reasonable enough basis for an alternative ranking method in HEFT, so we investigate its performance compared to the usual u_i ranks via numerical simulation in Section 4.

Two refinements of Fulkerson's method were proposed by Elmaghraby [3]. The first involves computing each of the f_i numbers in the aforementioned manner and then reversing the direction of the remaining subgraph in order to calculate an intermediate result which can be used to improve the quality of the bound. The second is a more general approach based on using two or more point estimates of e_i , rather than just f_i , a method that was later generalized by Robillard and Trahan [6]. In both cases Elmagharaby proved that the new number sequences achieve tighter bounds on e_i than the Fulkerson numbers f_i . However, small-scale experimentation suggested that the improvement of Elmaghraby's new bounds

over Fulkerson's were typically minor compared to the improvement of the latter over the standard HEFT u_i sequence so we chose to only evaluate here whether tightening the bounds at all is useful.

Algorithm 1: Computing the Fulkerson numbers using Clingen's method.

```
1 for i = n, ..., 1 do
           f_i = 0, \ \alpha_i = 0, \ Z_i = \{\}
           for k \in S_i do
                \ell_m = \infty
 4
                for \ell \in \tilde{L}_{ik} do
 5
                      \ell_m \leftarrow \min(\ell_m, \ell)
 6
                      if f_k + \ell \notin Z_i then
 7
                       Z_i \leftarrow Z_i \cup \{f_k + \ell\}
 8
                      end
 9
10
                end
                \alpha_i \leftarrow \max(\alpha_i, f_k + \ell_m)
11
12
           for z \in Z_i do
13
                if z \geq \alpha_i then
14
                      g = 1, q = 1
15
                      for k \in S_i do
16
                       g \leftarrow g \times M_{ik}(z - f_k)q \leftarrow q \times M_{ik}^*(z - f_k)
17
18
19
                      f_i \leftarrow f_i + z \times (g - q)
20
                end
\mathbf{21}
          end
22
23 end
```

3.2 Adjusting the pmfs

In some sense, the purpose of the node and edge pmfs m_i and m_{ik} is to simulate the dynamics of the processor selection phase of HEFT—i.e., $m_i(W_i^a)$ should represent the probability that task t_i is scheduled on processor p_a , and so on. In HEFT, tasks are assigned to the processor that is estimated to complete their execution at the earliest time and attempting to model this accurately beforehand can quickly get messy and expensive—especially given the interaction between the two phases of the algorithm. However, a sensible idea may be to simply weight the processor selection probabilities according to their respective task computation costs: if, say, a task is 10 times faster on one processor than another then it seems

more likely it will be scheduled on the former than the latter, even once the effect of contention is taken into account. More precisely, for all tasks t_i let

$$s_i = \sum_a \frac{1}{W_i^a}$$

and define a new set of pmfs by

$$\hat{m}_i(W_i^a) = \frac{1/W_i^a}{s_i} \ \forall i, a$$

and

$$\begin{split} \hat{m}_{ik}(W_{ik}^{ab}) &= \hat{m}_i(W_i^a) \cdot \hat{m}_k(W_k^b) \\ &= \frac{1/W_i^a W_k^b}{s_i s_k} \quad \forall i, k, a, b. \end{split}$$

Note that we take the reciprocal of the costs in order to reflect the idea that processors with smaller costs are more likely to be chosen than larger ones.

These modified pmfs can be used in conjunction with either upward ranking, as defined by equation (5), Fulkerson's bound, or Monte Carlo methods. For example, in the first instance, the expectations used simply become

$$\mathbb{E}[w_i] = \sum_{\ell \in L_i} \ell \hat{m}_i(\ell) = \frac{n_p}{s_i},\tag{11}$$

$$\mathbb{E}[w_{ik}] = \sum_{\ell \in L_{ik}} \ell \hat{m}_{ik}(\ell) = \frac{\sum_{a,b} W_{ik}^{ab} / (W_i^a W_k^b)}{s_i s_k}, \tag{12}$$

and these can be used with equation (5) to compute an alternative sequence of task ranks \hat{u}_i ; of course, this is slightly more computationally expensive than computing the standard u_i ranks but only by a constant factor. Similarly, by using the modified pmfs in conjunction with equation (10) we can define alternative Fulkerson numbers \hat{f}_i , and by sampling realizations according to \hat{m} rather than m we can incorporate this idea into the Monte Carlo approach for finding longest paths. All three of these possibilities are evaluated as alternative task ranks for HEFT in the following section.

4 Experimental comparison of rankings

In order to evaluate the alternative task prioritization phases in HEFT that we have outlined so far, we make use of our software simulator. However, first we need to define a suitably large and diverse set of graphs to compare them on.

4.1 Set up

We decided to evaluate the new rankings using the same two types of graphs as in the previous chapter: a set of Cholesky graphs, with real costs based on timings from an accelerated machine that we have access to, and several sets of randomly-generated graphs based on topologies provided by the STG [7]. The former are identical to those described in Section X so note that in particular they target only the two accelerated platforms detailed there: one comprising 1 GPU and 7 CPU cores and the other 4 GPUs and 28 CPU cores.

Since we are now interested in more general heterogeneous platforms we consider multiple sets of graphs based on randomly-generated graph topologies from the STG which differ from those in the previous chapter. In particular, a set is defined by the following parameters:

- $n \in \{100, 500, 1000\}$, the number of non-entry/exit tasks in each of the graphs. Each graph also has a single entry and exit task so that altogether it has e.g., 102 tasks rather than 100. Once this parameter has been specified, we use the topologies of the corresponding set of that size from the STG.
- $n_p \in \{2, 4, 8\}$, the number of processors in the target platform.
- $\beta \in \{0.1, 1, 10\}$, the computation-to-communication ratio (CCR). Defined as before, although the manner used to generate costs that give the target CCR differs (see below).
- $h \in \{1.0, 2.0\}$, the heterogeneity factor of the processors. Basically, the relative powers of the processors to one another.

Once all of these parameters are chosen, costs are generated in the following manner. First, an average power \overline{p} across the set of processors is chosen uniformly at random from the interval [1, 100]. Then for each processor p_a , $a = 1, \ldots, n_p$, its power r_a is chosen uniformly at random from the interval

$$[\overline{p} \times (1 - h/2), \ \overline{p} \times (1 + h/2)].$$

Now an average task cost \bar{t} is also chosen uniformly at random from the interval [1,100]. For each task t_i , $i=1,\ldots,n$, choose $x_i \in [0,2\bar{t}]$ uniformly at random and for all $a=1,\ldots,n_p$, realize $g_a \sim \Gamma(1,r_a)$ (i.e., choose g_a from a Gamma distribution with mean and variance r_a). Finally, let $W_i^a = x_i g_a$ be the computation cost of task t_i on processor p_a . This method is similar to that used in the original HEFT paper [8] except that we use the processor power attribute to reflect the fact that the ratios of task computation costs on different processors are often very similar for different tasks. Multiplication by a RV from a Gamma distribution is used to ensure that the computation costs are not entirely determined by the power; this distribution was chosen because it is always positive and

heavy-tailed, so has roughly the shape we're after. Note that we also considered the completely unrelated alternative and results were very similar... Continuity with original HEFT paper...

Communication costs are generated in a similar manner. First, an average edge cost \overline{e} is computed such that the specified CCR is (approximately) achieved. Then for all edges (t_i, t_k) , we choose $\overline{w_{ik}}$ uniformly at random from the interval $[0, 2\overline{e}]$. Then specific communication costs between the tasks on all possible pairs of distinct processors are chosen uniformly at random from the interval

$$\left[\overline{w_{ik}} \times (1 - h/2), \ \overline{w_{ik}} \times (1 + h/2)\right].$$

(Recall that costs are assumed to be zero when both tasks are scheduled on the same processor.) Note that the randomness here means that sometimes the target CCR is not precisely achieved, although it is usually acceptably close.

4.2 Benchmarking

First we want to see how well HEFT actually does compared to a random sort... Comments: difference between related and unrelated methods of generating costs (why? closer to MSP? Or something deeper?), failures, any differences in magnitude of improvement over random sort (e.g., size).

4.3 Evaluation of proposed rankings

We compare the following alternative ranking phases....

- Normal HEFT.
- Different tiebreaking criterion.
- Fulkerson.
- Weighted mean.
- Fulkerson-weighted mean.
- Lower bound.

Only considered Fulkerson for the smaller DAG sizes since two expensive—conclusion: doesn't appear to do any better, even the weighted version. But maybe discuss example for CPU-GPU. Monte Carlo results, same thing.

4.4 Conclusions

Only weighted mean showed any consistent improvement...

5 Processor selection

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