# Mission 5: Real-Time project on a "naked" computer

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# 1 User manual

Figure 1 depicts the router configuration. Three components are required to use the board as a DHCP (**D**ynamic **H**ost **C**onfiguration **P**rotocol) relay:

- DHCP server, assigns IP addresses to clients;
- DHCP client, requests an IP address;
- DHCP relay, bridges between the client and the server.

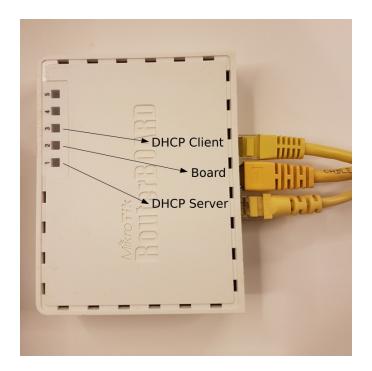


Figure 1: Router wiring

Server and clients are supposed to belong to two different networks (otherwise a relay would not be necessary, as they could communicate "directly"). The server must be connected to Port 1. The relay and the client(s) must be connected to ports from 2 to 5. A user can use these ports interchangeably. In Figure 1, relay and client are connected to Port 2 and 3, respectively.

You should set you Ethernet interface to require an IP address assigned dynamically (in other words, you should not specify a static IP address).

The board (henceforth called *relay*) displays a number of messages to let the client know what is going on. For instance, *Send to client* means it is dispatching a packet (coming from the server) to you. General purpose messages, such as the previous one, are displayed on the first line of the

LCD screen. Assigned IP addresses, on the other hand, are displayed on the second line. Seeing such an address means that you are about to receive it.

When started up, the LCD screen reads *Waiting client* on the first line and the relay's IP address on the second. Such a configuration means you can connect your computer and wait for an address.

The right-most LED blinks to signal that the relay is up and running.

# 2 Documentation for system engineers

## 2.1 Compilation and download

The program comes with a Makefile that can be used to compile the program. To this end, the right command to use is make. It will generate some files in the current directory (.) and in ./Objects/. In order to download the program into the naked computer, the user should follow the following steps (supposing the router is correctly configured):

- Run tftp 192.168.97.60 in the same directory as DHCPRelay.c. The tftp environment will start;
- binary, to send the program as binary;
- trace, to see what happens;
- verbose, to see more information;
- put DHCPRelay.hex.

The last command has to be run only when the board is ready to receive the program. This happens during the three seconds after its reboot.

The board comes with a RouterBoard. It is configured with two different networks in order to test the relay. A LAN (Local Area Network) comprises Ports 2, 3, 4, and 5; a WAN (Wide Area Network) is set at Port 1. The DHCP server should be connected to the WAN, while both relay and clients should be on Ports from 2 to 5. The router built-in DHCP server should be disabled in order to let the relay work properly. Once disabled, it is not possible to communicate neither with the router itself nor with the board. Every device should be assigned a static IP address for communication purposes (meaning configuring the router and sending the program to the board). Examples are as follows:

- 192.168.97.15 to send to program to the board;
- 192.168.88.10 to configure the router.

#### 2.2 DHCP server configuration

A DHCP server must be added to the network and configured to assign a free IP address within a certain range to the clients in the same subnet of the relay. On UNIX-based systems, the *Internet Systems Consortium DHCP Server* (dhcpd) can be used as a daemon which provides this service; Listing 1 shows a sample configuration using the addresses specified in the project.

```
default-lease-time 600;
max-lease-time 7200;

subnet 192.168.10.0 netmask 255.255.255.0 {
    # No IP address provided
}

subnet 192.168.97.0 netmask 255.255.255.0 {
    option routers 192.168.97.1;
    option subnet-mask 255.255.255.0;
    option broadcast-address 192.168.97.255;

range 192.168.97.150 192.168.97.250;
}
```

Listing 1: Sample dhcpd.conf

# 2.3 Debug mode

If something is not working properly a special mode can be activated which allows to view information messages during critical phases of the program execution. The **UART interface** (Universal **A**synchronous **R**eceiver-**T**ransmitter) is used to transmit the debug messages to an external terminal (typically a PC); the receiver must be connected to the RS232 port of the Olimex board and it must be set with these parameters:

Baud rate: 9600Data bits: 8Stop bits: 1Parity: Odd

The debug mode is **disabled by default**; it can be activated adding the option <code>-DUART\_DEBUG\_ON</code> to the CFLAGS in the Makefile and recompiling the code. Some predefined macros are used to print single characters (<code>DEBUGCHAR</code>), blocks of characters of any length (<code>DEBUGBLOCK</code>) and strings (<code>DEBUGMSG</code>) throughout the code; these macros are only effective in debug mode.

Please note that this debug mode only works in a "blocking" mode, and may therefore result in additional delays while executing the software.

# 3 Documentation for programmers

# 3.1 Specification

The program implements a DHCP relay. Figure 2 depicts how it works.

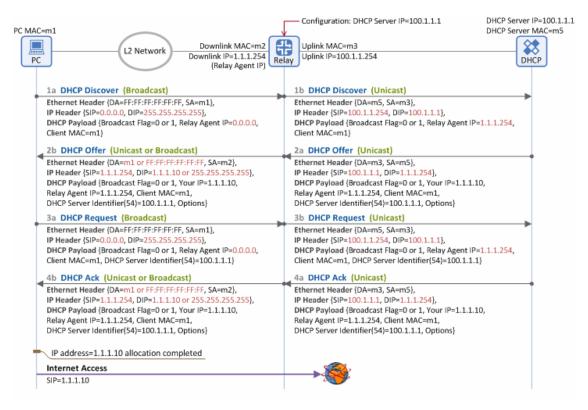


Figure 2: Example of DHCP protocol communication<sup>1</sup>

Basically, a DHCP relay is a "man in the middle" between the client and the DHCP server. It helps the client to contact the server in order to obtain an IP address. It does so receiving the broadcast packets sent by clients and forwarding them in an unicast connection to one (or more) DHCP servers, and vice versa, forwarding the server responses in broadcast to the clients.

A relay slightly modify each packet. The actual changed fields depend on where the packet is coming from, as follows:

#### ullet Server o Client:

#### - Ethernet Payload

- \* Destination MAC Address: DHCP Relay Uplink MAC  $\rightarrow$  Broadcast
- \* Source MAC Address: DHCP Server MAC  $\rightarrow$  DHCP Relay MAC Address

#### - IP Payload

- \* Source IP Address: DHCP Server IP Address  $\rightarrow$  DHCP Relay Downlink IP
- $\ast$  Destination IP Address: DHCP Relay Downlink IP  $\rightarrow$  Broadcast

#### • Client $\rightarrow$ Server:

#### - Ethernet Payload

- \* Destination MAC Address: Broadcast  $\rightarrow$  DHCP Server MAC
- \* Source MAC Address: PC MAC Address  $\rightarrow$  DHCP Relay Uplink MAC

<sup>1</sup>https://www.netmanias.com/en/?m=view&id=techdocs&no=6000

#### - IP Payload

- \* Source IP Address: 0.0.0.0 (no IP address)  $\rightarrow$  DHCP Relay Uplink IP
- \* Destination IP Address: Broadcast  $\rightarrow$  DHCP Server IP

#### - DHCP Payload

\* Gateway IP Address (GIADDR):  $0.0.0.0 \rightarrow DHCP$  Relay Downlink IP

#### 3.2 Design

Our ASG (Asynchronous State Graphs) diagram is depicted in Figure 3.

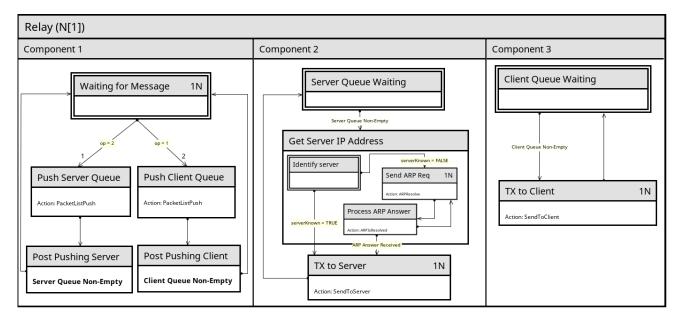


Figure 3: ASG diagram modeling a DHCP relay

The DHCP relay consists of three parallel components following a producer-consumer pattern. Component 1 waits for a packet, either from a client (broadcast) or a server (unicast). Components 2 and 3 then push the modified (note the modification is intended to be a consumer's responsibility, but this is not mandatory) packet in a queue (two different queues are used, respectively for the server and clients). The corresponding consumer polls on its queue in order to detect a new packet. The polling has not to be confused with active waiting, since components are supposed to be parallel: if the queue is empty the corresponding producer simply releases the processor, allowing other components to be dispatched.

Component1's WAITING FOR PACKETS state has got two outgoing transitions. If both of them may be crossed, the "server" one goes first. This means in our design the server has a higher priority than the clients. We consider the former to be more important than the latter, since it plays a significant role in the DHCP handshake. The protocol cannot proceed unless at least one server offers an IP address, and clients may retransmit their requests if no answer is received. We assign 1 as a priority to the server transition and 2 to the client one.

Component 2 and 3 are similar in their behavior. The only difference is the ARP request made by Component2 in order to get to know the server MAC address. This is necessary only the first time a client requires an IP address. Afterwards, a boolean flag, serverKnown in the diagram, becomes TRUE, meaning that no ARP requests are needed anymore. Even if this approach only works with one server, it can easily be further extended to work with as many servers as needed, just by using a list or an array.

A different approach might be issuing the ARP request(s) at initialization time. In this way, transmitting a packet to the server would not require an "on the fly" request. This is a reasonable

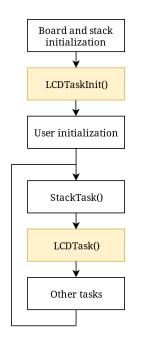
solution as soon as the DHCP server(s) is(/are) static. It is our belief that DHCP servers might change dynamically during the lifetime of the relay. Issuing ARP request during the initialization would require a new initialization if a DHCP server is dynamically added. Our solution, on the other hand, does not require any changes or restarts. Supposing to have a procedure to add a server, it would be enough for that procedure to set to FALSE the corresponding serverKnown flag for that server. A correct ARP request will be therefore issued for that server only.

One last point worthy to be explained is the resource N (going for Network). The board may only transmit in a half-duplex manner, and it is therefore necessary to allow only one substate at a time to use the network. This is the meaning of N. Each substate accessing the network must acquire N before being allowed to receive or transmit. Every time this happens, N is set to TRUE and the other substates cannot acquire it (thus it is a mutual exclusive resource). If a component  $C_1$  needs the network, but the latter is being used by another component  $C_2$ ,  $C_1$  has to wait until  $C_2$  sets N to FALSE again. There is no risk of deadlock, because there cannot be a circular wait (we only use one resource). In a "usual" priority-based scheduling there could be the risk of starvation, meaning that  $C_1$  never has the possibility to use the network because a higher priority component acquires it beforehand. Nonetheless, in our design and with our scheduler this cannot happen. For an informal proof of such a claim see 3.4.4.

# 3.3 LCD non-blocking module

The key concept in multitasking real-time systems is that tasks must be "small enough", in order not to prevent the processor for executing other operations. In this project it is required to use the LCD display to print information messages, but the C functions provided by Microchip are not optimized for multitasking and take a non negligible time to be processed; the LCDBlocking module has been thus converted in LCDNonBlocking and integrated into the TCP/IP stack to provide a better display handling in an environment with different tasks.

The most time-demanding functions in the LCD library (LCDInit, LCDErase, LCDUpdate) have been **split in smaller states**. When called, the LCD task can only execute a part of a function: the following ones are executed at successive task calls; furthermore, each state can be accessed only if a sufficient amount of time has passed from the previous one, in line with what happened in the original file where some delays were placed between instructions.



The main functions of the new library are LCDTaskInit and LCDTask. The first one is an initialization function which is used to assign default values to control variables and configure the resources needed for the correct operation handling. The latter is the proper task, which runs inside the cooperative multitasking loop.

These functions are called in the main entity as a design choice, but they could have also been integrated into the StackTsk file of the TCP/IP stack.

The big difference between the new library and the old one is that the operations are not executed immediately but are "appended" somewhere, so that the task can pick and perform them in successive steps without losing information about other operation requests happening in the meanwhile.

In order to do this, a **circular list** (actually, a circular array of structures) has been implemented: each time an *Init*, *Erase* or *Update* operation is required, the list is filled with a code representing that operation and the text to write, if needed; this guarantees that the display always reflects the correct history of operations regardless of the state of the shadow copy.

The circular list static allocation required the availability of more than 256 bytes in memory, that is the maximum dimension allowed by default due to internal division of databanks in the PIC18. To overcome this limitation, the linker script was modified to create a single databank of twice the size and a #pragma directive was introduced to memorize the list in that exact memory location; the solution was fully tested and does not introduce any kind of issue.

The delay handling is entrusted to **Timer 1** (external 32.768 kHz oscillator) because it is not used by any other part of the system; the timer is in a bounded configuration and the initial value of the register is calculated according to the necessary minimum delay for each stage. If such a waiting condition is active, the LDC task checks for an overflow of the timer register before proceeding to the execution.

The instruction flow of the LCD task is represented in the next page; it is a simple diagram which is not meant to explain the detailed content of each block but the general behavior of the module. For in-depth analysis it is possible to read the code.

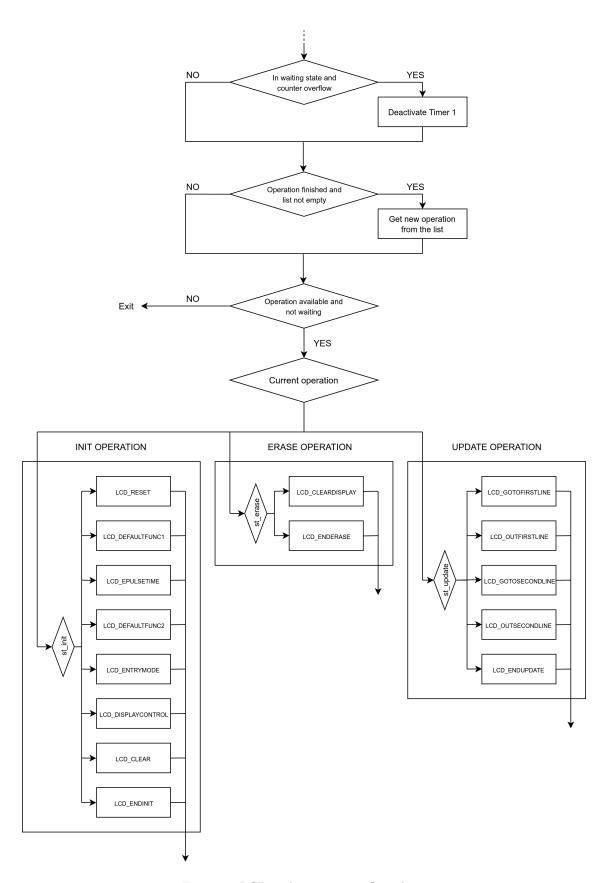


Figure 4: LCD task instruction flow chart

#### 3.4 Relay

The main entry point for the relay is the file DHCPRelay.c, containing the implementation. Function signatures and enum definitions are in its header file, DHCPRelay.h. The definitions here contained implement the cooperative scheduling. Relay is refined in three main parallel components, representing, respectively, the wait for a message (both form clients and server) and the transmissions. Each enum type corresponds to a component in the ASG diagram. Thus, Component1(), Component2(), and Component3() define the proper actions to be taken when the corresponding component is dispatched. It is not the aim of this document to explain how an ASG diagram should be translated into code, but, in brief, the three function aforementioned should contain a switch checking for the current state and managing actions and transitions accordingly. The C file contains the actual implementations.

Our implementation makes use of two queue in order to store packets incoming both from the server and clients (ClientMessages and ServerMessages). We implement them using a circular list. Seen the limitations in allocating more that 256 bytes and seen the size of the information we store, we decided to set the queues size to 5 (the maximum allowed is 7). An implementation with queues makes the entire managing slightly more difficult. We not only store the DHCP header, but some information used to compose the forwarded message, too. This additional data is the MessageType (i.e. the content of the MessageType option), and the accepted IP address, if any. A boolean flag (IPAddressNotNull) is used to make the distinction. If TRUE, the field RequiredAddress is meaningful. Otherwise, its content is just random bytes.

With fixed-sized queues we may get in trouble when the two ratios (sending are receiving) are not balanced. We decided to manage them with the following policy: if a queue Q is full when a packet is to be pushed (meaning it has already been received) we discard the oldest packet in Q. We do not expect this policy to lead to packets loss, since clients usually retransmit their packets if they receive no answer. On the other hand, we do not expect the server queue to be overflowed frequently, since the relay interfaces with only one server.

We take advantage of PacketCircularList's PacketListIsEmpty method to simplify the translation from ASG to C. We remove POST PUSHING SERVER and POST PUSHING CLIENT in favor of a direct check on the queue size. This simplifies our program's structure and reduces the overhead (even if small) caused by the "canonical" translation of those two states. Hence, the rendez-vous Server Queue Non-Empty and Client Queue Non-Empty are to be translated, respectively, with PacketListIsEmpty(&ServerMessages) and PacketListIsEmpty(&ClientMessages).

Please note that the Mikrotik TCP IP stack allows us to "directly" modify only the DHCP header. The other modifications the relay should do occur at transmission time.

## 3.4.1 Waiting component

The waiting component is basically a polling on the two open sockets (in the basic implementation we assume there is only one server and client). If enough bytes are written (and ready to be read) in the socket we start the reading procedure, actually implemented by GetPacket(). This function takes two parameters: a pointer to the variable to store the read packet in and the socket to read from. It basically checks if something is waiting in the socket buffer, and, if so, reads the packet (performing a basic check on the hardware type and length). It then reads the options, taking into account only the MessageType and the RequestedIPAddress. If everything works, it returns 0. Otherwise, an error code is given, as follows:

- -1 if no packet is available on the selected socket, meaning there are less than 241 bytes in its buffer;
- -2, wrong hardware type
- -3, wrong hardware length

#### • -4, parameters are invalid

The waiting state checks the client and the server socket. It might happen that both of them contain packets ready to be read. In such a scenario, the server as a higher priority and is served first. A flag, prevfromClient, is set to TRUE and checked when the component comes again in the waiting state. If it is TRUE, a client is served; otherwise, both the sockets are checked again. This rules client starvation out.

The final step is pushing the packet into the corresponding queue.

#### 3.4.2 Transmission to the server

Transmitting a packet to the DHCP server has two prerequisites: the queue must be not empty and the server MAC address should be known. The former condition becomes TRUE if and only if a packet has not only been received, but pushed into the queue, too. Once a packet is ready, if the server MAC address is not known (meaning serverKnown == FALSE) an ARP request is issued to get to know the address. When a response is received, serverKnown becomes TRUE and no more ARP requests will be issued. Afterwards, the packet is sent to the server via the function SendToServer().

SendToServer first checks if the server socket contains enough free space. Socket's remote IP address is set to the server one, and so is the MAC. It pops a packet from its queue and modifies only the GIADDR field, using its own IP address. It sets to 0 every unused field and the magic cookie to the value reported in RFC 1533. The other fields are either taken from the popped packet or generated on the fly. An example of the former is every data contained in the DHCP header, as well as the accepted client IP address, if any. An example of the latter is the subnet mask. The minimum size of such a packet is 300 bytes, in order to ensure compatibility with old DHCP relays that discard packets smaller than 300 octets.

#### 3.4.3 Transmission to the client

Transmitting a packet to a client is simpler than transmitting to a server. It is indeed not necessary to know any MAC addresses because the packet is sent in broadcast to the client network. Thus, the only prerequisite for this action is a non empty queue. When this is the case, SendToClient() is invoked.

This function operates more or less as SendToServer does, with a few slight differences. The first is the socket remote IP address, which is here set to broadcast (remember the client has not got an IP address unless the very end of the DHCP "handshake"). The MAC address is correctly set to be the one of the client. The latter value is taken from CHADDR. The second is the magic cookie, set to 0x63538263.

#### 3.4.4 Starvation

Suppose Component1 has acquired the network in order to receive a packet, and suppose Component 2 and 3 are waiting for the network in their "idle" state. In such a scenario, their queues are not empty. Component1 receives the packet, since it is allowed to use both the network and the processor. In this sense, the receiving operation may be considered as atomic: no one can do anything else since GetPacket never releases the processor and the scheduler is a non-preemptive one. It then executes to completion, and releases both the network (meaning it sets N to FALSE) and the processor (i.e. it lets another component execute). At this point, Component2 has the possibility to acquire the network, and it does so. This means neither Component1 nor Component3 may go ahead with their network-based operations (note that Component1 is now one of its "push" substates, which do not require the network). Component2 also does not release neither the network nor the processor during the transmission. It afterwards releases both of them, and Component3 has its chance to proceed.

If either Component2 or Component3 is not ready to transmit (its queue is empty), it does not compete for N.

This reasoning also applies considering Component2 or Component3 as a starting point. The scheduler always schedule these components in a "circular" manner (indeed, it is a round robin one), and there is no chance that a component never executes.

## 3.4.5 Enabling the relay mode

In order to enable the DHCP Relay mode, one should configure the router as said and do small modifications to the definitions provided in the Mikrotik TCP IP stack. In particular, STACK\_USE\_DHCP\_CLIENT and STACK\_USE\_DHCP\_SERVER should be disabled, while STACK\_USE\_DHCP\_RELAY should be enabled. These definitions can be found in TCPIP.h.

# 4 Program listings

This section contains the code of the most relevant files produced for this project. Rest of the code can be read in the src folder.

# 4.1 LCD non-blocking

This section lists the non-blocking LCD code. A blocking, functionally-equivalent version was provided by Mikrotik.

```
#define __LCDNONBLOCKING_C
  #include "../Include/TCPIP_Stack/Delay.h"
5 #define ___18F97J60
#define __SDCC__

#include "../Include/HardwareProfile.h"

#include "../Include/TCPIP_Stack/TCPIP.h" //ML
10 #ifdef USE_LCD
12 //#define FOUR_BIT_MODE
13 #define SAMSUNG_S6A0032
                              // This LCD driver chip has a different means of entering 4-
       bit mode.
14 #define ROWCHARS 16
16 // LCDText is a 32 byte shadow of the LCD text. Write to it and
_{
m 17} // then call LCDUpdate() to copy the string into the LCD module.
18 BYTE LCDText[ROWCHARS*2+1];
19
20 static BYTE LCDi, LCDj;
22 // States of the initialization function
23 static enum LCDInit_states
24 {
       LCD_RESET,
25
26
       LCD_DEFAULTFUNC1,
    LCD_EPULSETIME,
27
      LCD_DEFAULTFUNC2,
28
       LCD_ENTRYMODE,
29
      LCD_DISPLAYCONTROL,
30
31
      LCD_CLEAR,
32
    LCD_ENDINIT
33 } st_init;
35 // States of the erase function
36 static enum LCDErase_states
37 {
       LCD_CLEARDISPLAY,
38
39
      LCD_CLEARLOCAL
40 } st_update;
41
42 // States of the update function
43 static enum LCDUpdate_states
44 {
       LCD_GOTOFIRSTLINE,
      LCD_OUTFIRSTLINE,
46
47
      LCD_GOTOSECONDLINE,
      LCD_OUTSECONDLINE,
48
   LCD ENDUPDATE
49
50 } st_erase;
51
52
53 // Control flags
54 static char LCDWaiting;
static char LCDOpInProgress;
56 // Current operation info
57 static char LCDCurrentOrder;
```

```
58 static char LCDCurrentText[ROWCHARS*2+1];
60 static char tmpBuf[33];
61
62 /***
                      static void LCDWrite(BYTE RS, BYTE Data)
63 * Function:
64
                       RS - Register Select - 1:RAM, 0:Config registers
65
   * Input:
                        Data - 8 bits of data to write
66
67
   * Output:
                        None
68
69
                   Controls the Port I/O pins to cause an LCD write
70
   * Overview:
71
72 static void LCDWrite(BYTE RS, BYTE Data)
73 {
     #if defined(LCD_DATA_TRIS)
74
75
      LCD_DATA_TRIS = 0 \times 00;
76
     #else
      LCD_DATAO_TRIS = 0;
77
      LCD_DATA1_TRIS = 0;
78
       LCD_DATA2_TRIS = 0;
79
80
       LCD_DATA3_TRIS = 0;
       #if !defined(FOUR_BIT_MODE)
81
82
       LCD\_DATA4\_TRIS = 0;
83
       LCD_DATA5_TRIS = 0;
      LCD_DATA6_TRIS = 0;
84
85
      LCD_DATA7_TRIS = 0;
86
       #endif
     #endif
87
     LCD_RS_TRIS = 0;
88
     LCD_RD_WR_TRIS = 0;
89
90
     LCD_RD_WR_IO = 0;
91
     LCD_RS_IO = RS;
92
     #if defined(FOUR_BIT_MODE)
93
      #if defined(LCD_DATA_IO)
94
         LCD_DATA_IO = Data>>4;
95
96
       #else
97
        LCD_DATA0_IO = Data & 0x10;
         LCD_DATA1_IO = Data & 0x20;
98
         LCD_DATA2_IO = Data & 0x40;
99
         LCD_DATA3_IO = Data & 0x80;
100
       #endif
                        // Wait Data setup time (min 40ns)
102
       Nop();
       Nop();
103
104
       LCD_E_IO = 1;
105
       Nop();
                        // Wait E Pulse width time (min 230ns)
106
       Nop();
107
       Nop();
       Nop();
108
109
       Nop();
110
       Nop();
       Nop();
111
112
       Nop();
       Nop();
113
      LCD_E_IO = 0;
114
115
     #endif
116
     #if defined(LCD_DATA_IO)
117
       LCD_DATA_IO = Data;
118
     #else
119
120
      LCD_DATAO_IO = ((Data \& 0x01) == 0x01);
       LCD_DATA1_IO = ((Data \& 0x02) == 0x02);
121
       LCD_DATA2_IO = ((Data & 0x04) == 0x04);
122
       LCD_DATA3_IO = ((Data \& 0x08) == 0x08);
123
124
       #if !defined(FOUR_BIT_MODE)
       LCD_DATA4_IO = ((Data \& 0x10) == 0x10);
125
       LCD_DATA5_IO = ((Data \& 0x20) == 0x20);
LCD_DATA6_IO = ((Data & 0x40) == 0x40);
```

```
LCD_DATA7_IO = ((Data \& 0x80) == 0x80);
     #endif
130
131
     Nop();
                       // Wait Data setup time (min 40ns)
     Nop();
132
     LCD_E_IO = 1;
133
                       // Wait E Pulse width time (min 230ns)
134
     Nop();
135
136
     Nop();
137
     Nop();
     Nop();
138
139
     Nop();
140
     Nop();
     Nop();
141
     Nop();
142
     LCD_E_IO = 0;
143
144
145
     // // Uncomment if you want the data bus to go High-Z when idle
     // // Note that this may make analog functions work poorly when using // // Explorer 16 revision 5 boards with a 5V LCD on it. The 5V LCDs have
146
147
     // // internal weak pull ups to 5V on each of the I/O pins, which will
148
     // // backfeed 5V weekly onto non-5V tolerant PIC I/O pins.
149
     // #if defined(LCD_DATA_TRIS)
150
          LCD_DATA_TRIS = 0xFF;
151
152
     // #else
         LCD_DATA0_TRIS = 1;
LCD_DATA1_TRIS = 1;
153
154
         LCD_DATA2_TRIS = 1;
          LCD_DATA3_TRIS = 1;
#if !defined(FOUR_BIT_MODE)
156
157
158
     // LCD_DATA4_TRIS = 1;
          LCD_DATA5_TRIS = 1;
LCD_DATA6_TRIS = 1;
159
160
161
     // LCD_DATA7_TRIS = 1;
           #endif
162
     // #endif
163
   // LCD_RS_TRIS = 1;
164
     // LCD_RD_WR_TRIS = 1;
165
166 }
167
168 /************
* * Function: static void LCDInitExec()
170 *
171 * Input:
                         None
172
* Output:
                         None
174 *
175
   * Overview:
                    LCD INIT operation execution function
176
177 static void LCDInitExec(void)
178 {
     switch (st_init)
179
180
       case LCD_RESET:
181
182
         // ---- Part moved to LCDTaskInit ----
         //memset(LCDText, ' ', sizeof(LCDText)-1);
183
         //LCDText[sizeof(LCDText)-1] = 0;
184
185
         // Setup the I/O pins
         LCD_E_IO = 0;
186
         LCD_RD_WR_IO = 0;
187
          #if defined(LCD_DATA_TRIS)
188
           LCD_DATA_TRIS = 0 \times 00;
189
190
          #else
            LCD_DATAO_TRIS = 0;
191
           LCD_DATA1_TRIS = 0;
192
193
            LCD_DATA2_TRIS = 0;
            LCD_DATA3_TRIS = 0;
194
195
            #if !defined(FOUR_BIT_MODE)
           LCD\_DATA4\_TRIS = 0;
          LCD_DATA5_TRIS = 0;
197
```

```
LCD_DATA6_TRIS = 0;
198
           LCD_DATA7_TRIS = 0;
           #endif
200
201
          #endif
          LCD_RD_WR_TRIS = 0;
202
          LCD_RS_TRIS = 0;
203
          LCD\_E\_TRIS = 0;
204
          // Waiting time: 40 ms
205
          LCDWaiting = 1;
206
                 = (0x10000 - 1311) & 0xff;
= (0x10000 - 1311) >> 8;
207
          TMR1L
          TMR1H
208
209
         T1CONbits.TMR1ON = 1;
210
          // Next state
         st_init = LCD_DEFAULTFUNC1;
211
212
       break;
213
       case LCD_DEFAULTFUNC1:
214
215
        // Set the default function
          // Go to 8-bit mode first to reset the instruction state machine
216
          // This is done in a loop 3 times to absolutely ensure that we get
217
          // to 8-bit mode in case if the device was previously booted into
218
          // 4-bit mode and our PIC got reset in the middle of the \ensuremath{\mathsf{LCD}}
219
220
          // receiving half (4-bits) of an 8-bit instruction
          LCD_RS_IO = 0;
221
          #if defined(LCD_DATA_IO)
222
            LCD_DATA_IO = 0x03;
223
          #else
224
           LCD_DATA0_IO = 1;
225
226
            LCD_DATA1_IO = 1;
           LCD DATA2 IO = 0;
227
           LCD_DATA3_IO = 0;
228
            #if !defined(FOUR_BIT_MODE)
229
           LCD_DATA4_IO = 0;
230
           LCD_DATA5_IO = 0;
231
            LCD_DATA6_IO = 0;
232
233
            LCD_DATA7_IO = 0;
            #endif
234
          #endif
235
236
          //Nop();
                            // Wait Data setup time (min 40ns)
          //Nop();
237
          // Next state
238
239
          st_init = LCD_EPULSETIME;
         LCDi = 0;
240
241
       break;
242
       case LCD_EPULSETIME:
243
244
         LCD\_E\_IO = 1;
245
          Delay10us(1);
                             // Wait E Pulse width time (min 230ns)
         LCD_E_IO = 0;
246
247
          // Cyclic access
          LCDi++;
248
          if (LCDi >= 3u)
249
           st_init = LCD_DEFAULTFUNC2;
250
          // Waiting time: 2 ms
251
252
          LCDWaiting = 1;
          TMR1L = (0x10000 - 66) & 0xff;
253
                    = (0x10000 - 66) >> 8;
          TMR1H
254
255
          T1CONbits.TMR1ON = 1;
256
       break:
257
       case LCD_DEFAULTFUNC2:
258
         #if defined(FOUR_BIT_MODE)
259
260
            #if defined(SAMSUNG_S6A0032)
              // Enter 4-bit mode (requires only 4-bits on the S6A0032)
261
              #if defined(LCD_DATA_IO)
262
263
                LCD_DATA_IO = 0x02;
              #else
264
               LCD_DATA0_IO = 0;
265
                LCD_DATA1_IO = 1;
               LCD_DATA2_IO = 0;
267
```

```
LCD_DATA3_IO = 0;
268
             #endif
              Nop();
                               // Wait Data setup time (min 40ns)
270
271
              Nop();
              LCD\_E\_IO = 1;
272
              Delay10us(1);
                                // Wait E Pulse width time (min 230ns)
273
274
              LCD_E_IO = 0;
275
            #else
              \ensuremath{//} Enter 4-bit mode with two lines (requires 8-bits on most LCD controllers)
276
277
              LCDWrite(0, 0x28);
            #endif
278
279
          #else
            // Use 8-bit mode with two lines
280
            LCDWrite(0, 0x38);
281
          #endif
282
          // Waiting time: 61 us
283
          LCDWaiting = 1;
284
          TMR1L = (0x10000 - 2) \& 0xff;
TMR1H = (0x10000 - 2) >> 8;
285
286
287
          T1CONbits.TMR1ON = 1;
         // Next state
288
         st_init = LCD_ENTRYMODE;
289
290
        break;
291
292
       case LCD_ENTRYMODE:
         LCDWrite(0, 0x06); // Increment after each write, do not shift
293
          // Waiting time: 61 us
294
295
          LCDWaiting = 1;
          TMR1L = (0x10000 - 2) & 0xff;
TMR1H = (0x10000 - 2) >> 8;
296
          TMR1H
297
298
         T1CONbits.TMR1ON = 1;
299
          // Next state
         st_init = LCD_DISPLAYCONTROL;
300
301
       break;
302
        case LCD_DISPLAYCONTROL:
303
         LCDWrite(0, 0x0C); // Turn display on, no cusor, no cursor blink
304
          // Waiting time: 61 us
305
306
          LCDWaiting = 1;
         TMR1L = (0x10000 - 2) & 0xff;
TMR1H = (0x10000 - 2) >> 8;
307
308
309
          T1CONbits.TMR1ON = 1;
         // Next state
310
311
         st_init = LCD_CLEAR;
312
        break;
313
314
       case LCD_CLEAR:
315
         LCDWrite(0, 0x01);
          // Waiting time: 2 ms
316
317
         LCDWaiting = 1;
          TMR1L = (0x10000 - 66) & 0xff;
TMR1H = (0x10000 - 66) >> 8;
318
319
         T1CONbits.TMR1ON = 1;
320
          // Next state
321
322
         st_init = LCD_ENDINIT;
       break;
323
324
325
        case LCD_ENDINIT:
         // End of operation
326
         LCDOpInProgress = 0;
327
          // Next state
328
         st_init = LCD_RESET;
329
330
       break;
331
       default:
332
333
       // Do nothing
334
       break;
335 }
336 }
337
```

```
static void LCDEraseExec()
339 * Function:
340 *
341
   * Input:
                      None
342 *
343 * Output:
                      None
* Overview: LCD ERASE operation execution function
346 * **
347 static void LCDEraseExec(void)
348 {
349 switch(st_erase)
350
      case LCD_CLEARDISPLAY:
351
352
       // Clear display
        LCDWrite(0, 0x01);
353
        // Waiting time: 2 ms
354
355
        LCDWaiting = 1;
        TMR1L = (0x10000 - 66) & 0xff;
TMR1H = (0x10000 - 66) >> 8;
356
357
       T1CONbits.TMR1ON = 1;
358
       // Next state
359
360
        st_erase = LCD_CLEARLOCAL;
     break;
361
362
363
        case LCD_CLEARLOCAL:
        // ---- Do not execute: done at invoke time -----
364
        // Clear local copy
// memset(LCDText, ' ', 32);
365
366
        // End of operation
367
368
        LCDOpInProgress = 0;
369
        // Next state
        st_erase = LCD_CLEARDISPLAY;
370
371
      break;
372
373
      default:
     // Do nothing
374
375
     break;
376
377 }
378
380 * Function:
                     static void LCDUpdateExec()
381 *
   * Input:
                      None
382
383
384 * Output:
                     None
385
                     LCD UPDATE operation execution function
386 * Overview:
387 * *
388 static void LCDUpdateExec(void)
389 {
390    switch(st_update)
391 {
392
      case LCD_GOTOFIRSTLINE:
       DEBUGMSG("-> SENT TEXT: ");
393
        DEBUGBLOCK(LCDCurrentText, 33, 1);
394
        DEBUGMSG("\r\n");
395
        // Go home
396
        LCDWrite(0, 0x02);
397
         // Waiting time: 2 ms
398
        LCDWaiting = 1;
399
        TMR1L = (0x10000 - 66) & 0xff;
TMR1H = (0x10000 - 66) >> 8;
400
401
        T1CONbits.TMR1ON = 1;
402
403
        st_update = LCD_OUTFIRSTLINE;
404
        LCDi = 0;
405
      break;
dot case LCD_OUTFIRSTLINE:
```

```
// Erase the rest of the line if a null char is
408
          // encountered (good for printing strings directly)
          if (LCDCurrentText[LCDi] == 0u)
410
411
           LCDWrite(1, ' ');
412
            for(LCDj=LCDi; LCDj < 16u; LCDj++)</pre>
413
414
415
            LCDCurrentText[LCDj] = ' ';
           }
416
417
         } else
           LCDWrite(1, LCDCurrentText[LCDi]);
418
          // Cyclic access
419
         LCDi++;
420
         if (LCDi >= 16u)
421
           st_update = LCD_GOTOSECONDLINE;
422
          // Waiting time: 61 us
423
         LCDWaiting = 1;
424
         TMR1L = (0x10000 - 2) \& 0xff;
TMR1H = (0x10000 - 2) >> 8;
425
426
427
         T1CONbits.TMR1ON = 1;
        break;
428
429
430
          case LCD_GOTOSECONDLINE:
          // Set the address to the second line
431
         LCDWrite(0, 0xC0);
432
433
          // Waiting time: 61 us
         LCDWaiting = 1;
434
                 = (0x10000 - 2) & 0xff;
= (0x10000 - 2) >> 8;
435
          TMR1I
436
          TMR1H
          T1CONbits.TMR1ON = 1;
437
438
         st_update = LCD_OUTSECONDLINE;
439
         LCDi = 16;
440
        break;
441
          case LCD_OUTSECONDLINE:
442
          // Erase the rest of the line if a null char is
443
          // encountered (good for printing strings directly)
          if (LCDCurrentText[LCDi] == 0u)
445
446
          LCDWrite(1, ' ');
447
            for(LCDj=LCDi; LCDj < 32u; LCDj++)</pre>
448
449
             LCDCurrentText[LCDj] = ' ';
450
451
           }
          } else
452
           LCDWrite(1, LCDCurrentText[LCDi]);
453
          // Cyclic access
454
455
          LCDi++;
          if (LCDi >= 32u)
456
457
           st_update = LCD_ENDUPDATE;
          // Waiting time: 61 us
458
          LCDWaiting = 1;
459
          TMR1L = (0x10000 - 2) \& 0xff;
TMR1H = (0x10000 - 2) >> 8;
461
462
          T1CONbits.TMR1ON = 1;
        break;
463
464
465
          case LCD_ENDUPDATE:
         DEBUGMSG("-> PARSED TEXT: ");
466
         DEBUGBLOCK(LCDCurrentText, 33, 1);
467
          DEBUGMSG("\r\n");
468
         LCDOpInProgress = 0;
469
470
         st_update = LCD_GOTOFIRSTLINE;
471
       break;
472
473
       default:
474
       // Do nothing
475
       break;
476 }
477 }
```

```
478
480 /*
481
   * Function: void LCDTaskInit(void)
482
   * Input:
                       None
483
    * Output:
                       None
485
486
487
    * Overview:
                       Initialization of the entities used in the LCD task
488
489 void LCDTaskInit(void)
490 {
     // Initial states
491
     st_init = LCD_RESET;
492
     st_update = LCD_GOTOFIRSTLINE;
st_erase = LCD_CLEARDISPLAY;
493
494
495
     // Initial values
     LCDWaiting = 0;
496
497
     LCDOpInProgress = 0;
     LCDi = 0;
498
     LCDj = 0;
499
500
     // Circular list initialization
     LCDListInit();
501
502
     // Timer1 configuration
     T1CONbits.TMR1ON = 0;
                                // disable timer1
503
     T1CONbits.RD16 = 1;
                               // use timer1 16-bit counter
504
     T1CONbits.T1CKPS0 = 0;
                               // prescaler set to 1:1
505
506
     T1CONbits.T1CKPS1 = 0;
     T1CONbits.T1OSCEN = 1;
                                // timer1 oscillator enable
507
508
     T1CONbits.TMR1CS = 1;
                             // external clock selected
                             // clear timer1 overflow bit
     PIR1bits.TMR1IF = 0;
509
     // Clear LCDText
510
     memset(LCDText, ' ', sizeof(LCDText)-1);
511
     LCDText[sizeof(LCDText)-1] = 0;
512
513 }
514
515
516 void LCDTask(void)
517 {
518
     if (PIR1bits.TMR1IF && LCDWaiting) // Time expired
519
      LCDWaiting = 0;
520
       T1CONbits.TMR1ON = 0;
521
       PIR1bits.TMR1IF = 0;
522
523
                                  // No operations being executed
524
       if (!LCDOpInProgress)
525
       if (!LCDListIsEmpty())
                                     // The list contains at least one operation
526
527
         LCDListPop(&LCDCurrentOrder, LCDCurrentText); // Retrieve the operation to
528
       execute
        LCDOpInProgress = 1;
                                  // Set the execution flag
         DEBUGMSG("POPPED: ");
530
531
         ultoa(LCDCurrentOrder, tmpBuf,10);
         DEBUGMSG (tmpBuf);
532
         DEBUGMSG("\r\n");
533
534
535
     if (!LCDWaiting && LCDOpInProgress) // Not waiting for timers
536
537
       switch (LCDCurrentOrder)
538
539
                                // Init operation
540
         case 1:
          LCDInitExec();
541
542
         break;
543
         case 2:
                               // Erase operation
544
          LCDEraseExec();
        break;
546
```

```
547
       case 3:
                            // Update operation
          LCDUpdateExec();
549
550
        break;
551
        default:
552
553
         // Do nothing
554
        break;
555
     }
556
557 }
558
559
560 /**********
561 * Function:
                    void LCDInit(void)
562 *
   * Input:
                     None
563
564 *
   * Output:
565
                     None
566
567 * Overview:
                  Add an INIT operation to the LCD circular list
568 **
569 void LCDInit(void)
570 {
571
    LCDListPush(1, "");
572
    DEBUGMSG("PUSHED: 1\r\n");
573 }
574
575 /************
* Function: void LCDErase(void)
577 *
   * PreCondition: LCDInit() must have been called once
578
579
580 * Input:
                     None
581
582
   * Output:
                      None
583
                     Add an ERASE operation to the LCD circular list
584 * Overview:
                     and clean the LCDText shadow copy
586 ****
587 void LCDErase(void)
589 LCDListPush(2, "");
DEBUGMSG("PUSHED: 2\r\n");
memset(LCDText, '', 32);
592 }
593
594 /***
* Function: void LCDUpdate(void)
596 *
   * PreCondition: LCDInit() must have been called once
597
598
   * Input:
599
600
601
   * Output:
                      None
602
                 Add an UPDATE operation to the LCD circular list
603 * Overview:
604
605 void LCDUpdate (void)
606 {
     LCDListPush(3, LCDText);
607
   DEBUGMSG("PUSHED: 3\r\n");
608
609 }
610
#endif //#ifdef USE_LCD
```

Listing 2: LCDNonBlocking.c

# 4.2 DHCP Relay

This section lists the code implementing the relay functionality.

#### 4.2.1 Header

Listing 3 lists the relay header file.

```
2 * FileName: DHCPRelay.h
2 * FileName:
3 * Dependencies: Compiler.h
4 * Processor: PIC18, PIC24F, PIC24H, dsPIC30F, dsPIC33F, PIC32
5 * Compiler: Microchip C32 v1.05 or higher
             Microchip C30 v3.12 or higher
               Microchip C18 v3.30 or higher
              HI-TECH PICC-18 PRO 9.63PL2 or higher
#ifndef _DHCPRELAY_H
#define _DHCPRELAY_H
12 #define STACK_USE_DHCP_RELAY
#include "GenericTypeDefs.h"
#include "TCPIP_Stack/TCPIP.h"
16 //#define BAUD_RATE
                             (19200)
                                       // bps
17
18 #if !defined(THIS_IS_STACK_APPLICATION)
extern BYTE ANOString[8];
20 #endif
21
22 //MLvoid DoUARTConfig(void);
24 //ML#if defined(EEPROM_CS_TRIS) || defined(SPIFLASH_CS_TRIS)
25 //ML void SaveAppConfig(void);
26 //ML#else
#define SaveAppConfig()
28 //ML#endif
29
30 //MLvoid SMTPDemo(void);
31 void PingDemo(void);
32 //MLvoid SNMPTrapDemo(void);
33 //MLvoid GenericTCPClient (void);
34 //MLvoid GenericTCPServer(void);
35 //void BerkeleyTCPClientDemo(void);
36 //void BerkeleyTCPServerDemo(void);
37 //void BerkeleyUDPClientDemo(void);
38
39 #ifdef STACK_USE_DHCP_RELAY
      // enum representing the current relay component on the processor
40
41
       typedef enum {
           42
43
          COMP2, // sending to server COMP3 // sending to client
44
45
      } CURRENT_COMPONENT;
46
47
      typedef enum {
48
                                    // Polling for packets
          WAITING FOR MESSAGE,
49
           /*SERVER_MESSAGE_T,
50
           CLIENT_MESSAGE_T,
51
52
          FROM_SERVER,
          FROM_SERVER_T, */
53
          PUSH_SERVER_QUEUE,
                                     // push in the server queue
54
55
          PUSH_SERVER_QUEUE_T,
           /*FROM_CLIENT,
56
          FROM_CLIENT_T, */
57
           PUSH_CLIENT_QUEUE,
                                     // push in the client queue
58
           PUSH_CLIENT_QUEUE_T,
59
60
       } COMPONENT1;
```

```
typedef enum {
           SERVER_QUEUE_WAITING,
                                    // wait for a packet to be sent
           SERVER_QUEUE_WAITING_T,
64
65
           GET_SERVER_IP_ADDRESS, // issue an ARP request
           GET_SERVER_IP_ADDRESS_T,
66
           IDENTIFY_SERVER_TO_TX,
67
68
           TX_TO_SERVER,
                                    // actually transmit the packet
           TX_TO_SERVER_T
69
      } COMPONENT2;
70
71
       typedef enum {
72
           CLIENT_QUEUE_WAITING,
73
                                    //wait for a packet to be sent
           CLIENT_QUEUE_WAITING_T,
74
           TX_TO_CLIENT,
                                    // transmit the packet
75
           TX_TO_CLIENT_T
76
77
      } COMPONENT3;
78
79
       typedef enum {
           IDENTIFY_SERVER,
                                    // check if a DHCP server is known
80
           IDENTIFY_SERVER_TO_ARP,
81
           SEND_ARP_REQUEST,
                                    // issue the ARP request
82
           SEND_ARP_REQUEST_T,
83
84
           PROCESS_ARP_ANSWER,
                                    // get the answer or reissue the request
           PROCESS_ARP_ANSWER_T
85
      } GET_SERVER_IP_ADDRESS_COMP;
86
      static int DHCPRelayInit();
88
89
      static void DHCPRelayTask();
90
       static int GetServerPacket();
       static int GetClientPacket();
91
      static void SendToServer();
       static void SendToClient();
93
       static void Component1();
94
       static void Component2();
      static void Component3();
96
97 #endif
99 // An actual function defined in DHCPRelay.c for displaying the current IP
100 // address on the LCD.
#if defined(__SDCC__)
       void DisplayIPValue(DWORD IPVal);
102
103
       void DisplayString(BYTE pos, char* text);
       void DisplayWORD(BYTE pos, WORD w);
104
105 #else
      void DisplayIPValue(IP_ADDR IPVal);
106
107 #endif
109 #endif // _DHCPRELAY_H
```

Listing 3: DHCPRelay.h

## 4.2.2 Implementation

Listing 4 lists the actual relay implementation.

```
1 /*********************************
2
3
   * Main Application Entry Point for the DHCPRelay.
6
  * This symbol uniquely defines this file as the main entry point.
   * There should only be one such definition in the entire project,
9
   \star and this file must define the AppConfig variable as described below.
_{11} _{\,\star} The processor configuration will be included in HardwareProfile.h
* if this symbol is defined.
13 */
#define THIS_INCLUDES_THE_MAIN_FUNCTION
#define THIS_IS_STACK_APPLICATION
17 // define the processor we use
18 #define ___18F97J60
^{19} // define the compiler we use
20 #define __SDCC_
21
22 // inlude all hardware and compiler dependent definitions
#include "Include/HardwareProfile.h"
_{\rm 24} // Include all headers for any enabled TCPIP Stack functions
#include "Include/TCPIP_Stack/TCPIP.h"
27 // Include functions specific to this stack application
#include "Include/DHCPRelay.h"
29
30 #if !defined(STACK_CLIENT_MODE)
      #define STACK_CLIENT_MODE
31
32 #endif
33
                                  0xFFFFFFFF // broadcast address
34 #define BROADCAST
35 // server's IP address
36 #define SERVER_IP_ADDR_BYTE1
                                  (192ul)
37 #define SERVER_IP_ADDR_BYTE2
                                  (168ul)
38 #define SERVER_IP_ADDR_BYTE3
39 #define SERVER_IP_ADDR_BYTE4
                                   (1ul)
41 // Declare AppConfig structure and some other supporting stack variables
42 APP_CONFIG AppConfig;
43 BYTE ANOString[8];
44
45 // sockets
46 UDP_SOCKET serverToClient;
47 UDP_SOCKET clientToServer;
49 // components needed for the cooperative scheduling
50 CURRENT_COMPONENT currentComponent;
51 COMPONENT1 comp1;
52 COMPONENT2 comp2;
53 COMPONENT3 comp3;
54 GET_SERVER_IP_ADDRESS_COMP comp2_2;
_{\rm 56} // queues to store packets coming from server and clients
57 PacketList ServerMessages;
58 PacketList ClientMessages;
60 // temporary variables used to store the packets before pushing
_{61} // them in the corresponding queue
62 PACKET_DATA serverPacket;
63 PACKET_DATA clientPacket;
65 IP_ADDR RequiredAddress; // IP address accepted by te server
```

```
67 BOOL serverTurn; // used to alternate server and client listening
^{68} //used to store whether the packet contained an accepted IP address
69 BOOL IPAddressNotNull;
70 BOOL N; // network resource
71 BOOL serverKnown; // TRUE once the server's MAC address has been resolved
72 BOOL prevFromServer;
73 BOOL prevFromClient;
75 NODE_INFO ServerInfo; // server's IP and MAC addresses
77 // Private helper functions.
_{78} // These may or may not be present in all applications.
79 static void InitAppConfig(void);
80 static void InitializeBoard(void);
81 void DisplayWORD(BYTE pos, WORD w); //write WORDs on LCD for debugging
82
83 //
84 // PIC18 Interrupt Service Routines
85 //
_{86} // NOTE: Several PICs, including the PIC18F4620 revision A3 have a RETFIE
87 // FAST/MOVFF bug
_{88} // The interruptlow keyword is used to work around the bug when using C18
90 //LowISR
91 #if defined(__18CXX)
       #if defined(HI_TECH_C)
92
           void interrupt low_priority LowISR(void)
93
94
       #elif defined(__SDCC__)
95
           void LowISR(void) __interrupt (2) //ML for sdcc
       #else
96
97
           #pragma interruptlow LowISR
           void LowISR(void)
98
99
       #endif
100
     TickUpdate();
101
       #if !defined(__SDCC__) && !defined(HI_TECH_C)
103
              //automatic with these compilers
104
105
           #pragma code lowVector=0x18
     void LowVector(void) {_asm goto LowISR _endasm}
106
     #pragma code // Return to default code section
108
       #endif
109
111 //HighISR
       #if defined(HI_TECH_C)
112
           void interrupt HighISR(void)
113
114
       #elif defined(__SDCC__)
           void HighISR(void) __interrupt(1) //ML for sdcc
116
           #pragma interruptlow HighISR
117
118
           void HighISR(void)
       #endif
119
120
121
         //insert here code for high level interrupt, if any
       #if !defined(__SDCC__) && !defined(HI_TECH_C)
124
              //automatic with these compilers
     #pragma code highVector=0x8
125
     void HighVector(void) {_asm goto HighISR _endasm}
126
     #pragma code // Return to default code section
127
       #endif
128
129
130 #endif
131
132 const char* message; //pointer to message to display on LCD
133
134 /**
_{135} * Init the relay. This function opens the sockets to the server and the client and
_{\rm 136} * initializes the components used for the cooperative scheduling.
```

```
_{137} * @return 0 if everything succeeded, a negative number otherwise:
           -) -1, if the server socket could not be open
139 *
              -) -2, if the client socket could not be open
140 */
int DHCPRelayInit() {
       // init the components
142
       currentComponent
                          = COMP1;
143
144
                                 = WAITING_FOR_MESSAGE;
145
       comp1
146
       comp2
                                 = SERVER_QUEUE_WAITING;
                                 = CLIENT_QUEUE_WAITING;
147
       comp3
148
       comp2_2
                                 = SEND_ARP_REQUEST;
149
       // open the sockets
150
       clientToServer
                                 = UDPOpen(DHCP_SERVER_PORT, NULL, DHCP_CLIENT_PORT);
       serverToClient
                                 = UDPOpen(DHCP_CLIENT_PORT, NULL, DHCP_SERVER_PORT);
152
153
154
       if (serverToClient == INVALID_UDP_SOCKET) {
           DisplayString(0, "Invalid Server ");
155
156
           return -1;
157
       if (clientToServer == INVALID_UDP_SOCKET) {
158
           DisplayString(16, "Invalid Client ");
159
           return -2;
160
161
       }
162
       // init the queues
163
164
       PacketListInit(&ServerMessages);
165
       PacketListInit(&ClientMessages);
166
       // init some boolean flags needed to organize the work
167
       serverTurn
                                = FALSE;
168
169
       IPAddressNotNull
                                = FALSE:
170
171
172
                                = FALSE;
173
       serverKnown
                                = FALSE;
174
175
       // set the server's IP address
176
       ServerInfo.IPAddr.Val =
177
178
               SERVER_IP_ADDR_BYTE1 |
               SERVER_IP_ADDR_BYTE2<<8ul |
179
180
               SERVER_IP_ADDR_BYTE3<<16ul |
               SERVER_IP_ADDR_BYTE4<<24ul;
181
182
       return 0;
183
184 }
185
186 /**
* Read a DHCP packet (if any) from a socket, and store it in the 'pkt'
_{\rm 188} * parameter. The function reads the DHCP header and store it for future
   * use. It performs some basic checks on the hardware type (which must be
_{\rm 190} * ETHERNET (== 1u)) and the hardware length (which must be == 6u). It does
   \star not validate the message type (which may be both 1u (BOOT_REQUEST) and
191
* 2u (BOOT_REPLY)) and the magic cookie.
_{\rm 193} * <code>@param</code> pkt Pointer to a packet storing the packet readfrom the network
   * @param socket Socket used to read the packet (if any)
194
195 * @return O If everything succeeded, a negative number otherwise:
196 *
              -) -1 if no packet is available on the selected socket, meaning
                 there are less then 241 bytes in its buffer;
              -) -2, wrong hardware type
198
199
              -) -3, wrong hardware length
              -) -4, pkt is null or the socket is invalid
200
201 */
202 static int GetPacket(PACKET_DATA* pkt, UDP_SOCKET socket) {
     // does the current socket have enough bytes ready to be read?
203
       if (UDPIsGetReady(socket) < 241u) {</pre>
204
          return -1;
206
```

```
// parameters validation check
207
        if (pkt != NULL && socket != INVALID_UDP_SOCKET) {
208
            BYTE
                             toBeDiscarded; // used to throw away unused fields
209
210
            DWORD
                             magicCookie;
            BOOTP_HEADER
                             Header; // packet header
211
                             Type = Ou; // MessageType
            BYTE
212
                             Option; // used to iterate over the DHCP options
213
            BYTE
                             Len; // length of the current option
214
            BYTE
            BYTE
                             i; // used to add 0 paddings \,
215
216
            UDPGetArray((BYTE*)&Header, sizeof(Header)); // get the header
217
218
            // validate hardware interface and message type
219
            if (Header.HardwareType != 1u) {
220
                return -2;
221
222
223
            if (Header.HardwareLen != 6u) {
                return -3;
225
226
227
228
229
            * read and discard the following unused fields:
            * - client hardware address
230
231
            \star - server host name
232
            * - boot filename
233
            for(i = 0; i < 64+128+(16-sizeof(MAC_ADDR)); i++) {</pre>
234
235
                UDPGet(&toBeDiscarded);
236
237
            // obtain magic cookie
238
            UDPGetArray((BYTE*)&magicCookie, sizeof(DWORD));
239
            // process options
240
            while (UDPGet(&Option) && Option != DHCP_END_OPTION) {
241
242
                UDPGet(&Len); // get the length
                switch (Option) {
243
                    case DHCP_MESSAGE_TYPE:
244
245
                         UDPGet(&Type); // get the message type
                         memcpy(&(pkt \rightarrow MessageType), &Type, sizeof(BYTE)); // copy Type
246
247
                         switch (Type) {
248
                             case DHCP_DISCOVER_MESSAGE:
                                 DisplayString(16, "DHCP Discover
                                                                      ");
249
250
                                 break:
                             case DHCP_REQUEST_MESSAGE:
251
                                 DisplayString(16, "DHCP Request
252
                                                                      ");
253
                                 break;
254
                             case DHCP_OFFER_MESSAGE:
                                 DisplayString(16, "DHCP Offer
                                                                       ");
255
256
                                 break;
                             case DHCP_ACK_MESSAGE:
257
                                 DisplayString(16, "DHCP ACK
                                                                      ");
258
                         }
260
261
                         break;
                     // get the accepted IP address
262
                    case DHCP_PARAM_REQUEST_IP_ADDRESS:
263
264
                         if (Len == 4u) {
                             UDPGetArray((BYTE*)&RequiredAddress, 4);
265
                             IPAddressNotNull = TRUE;
266
                             DisplayIPValue(RequiredAddress.Val);
267
                         }
268
269
                         break;
270
                // remove any unprocessed bytes
271
272
                while(Len) {
                    UDPGet(&i);
273
                    Len--;
274
276
```

```
// discard the rest of the buffer (it contains the 0 padding)
            if (Option == DHCP_END_OPTION) {
               UDPDiscard();
279
280
281
           \ensuremath{//} prepare the packet to be pushed
282
           memcpy(&(pkt -> Header), &Header, sizeof(BOOTP_HEADER));
           if (IPAddressNotNull == TRUE) {
284
               memcpy(&(pkt -> RequiredAddress), &RequiredAddress, sizeof(IP_ADDR));
285
               memcpy(&(pkt -> IPAddressNotNull), &IPAddressNotNull, sizeof(BOOL));
287
           IPAddressNotNull = FALSE; // turn off flag
288
           DisplayIPValue(pkt -> Header.ClientIP.Val);
289
           return 0:
290
       } else {
291
           return -4;
292
293
294 }
295
296 /**
   * Read a packet from the server socket, if any, and store it in
297
   * 'serverPacket'. The function reads the DHCP header and store it for future
298
    * use. It performs some basic checks on the hardware type (which must be
    \star ETHERNET (== 1u)) and the hardware length (which must be == 6u). It does
300
301
    \star not validate the message type (which may be both 1u (BOOT_REQUEST) and
    * 2u (BOOT_REPLY)) and the magic cookie.
    * @return 0 If everything succeeded, a negative number otherwise:
303
304
              -) -1 if no packet is available on the selected socket, meaning
305
                 there are less then 241 bytes in its buffer;
              -) -2, wrong hardware type
306
              -) -3, wrong hardware length
307
              -) -4, pkt is null or the socket is invalid
308
309
   */
310 static int GetServerPacket() {
       int res = GetPacket(&serverPacket, serverToClient);
311
312
       if (res == 0) {
           comp1 = PUSH_CLIENT_QUEUE;
313
           prevFromServer = TRUE;
314
315
       return res;
316
317 }
318
319 /**
_{\rm 320} * Read a packet from the client socket, if any, and store it in
    \star 'clientrPacket'. The function reads the DHCP header and store it for future
   \star use. It performs some basic checks on the hardware type (which must be
322
   \star ETHERNET (== 1u)) and the hardware length (which must be == 6u). It does
324
    * not validate the message type (which may be both 1u (BOOT_REQUEST) and
    \star 2u (BOOT_REPLY)) and the magic cookie.
325
    * @return 0 If everything succeeded, a negative number otherwise:
              -) -1 if no packet is available on the selected socket, meaning
327
                 there are less then 241 bytes in its buffer;
328
              -) -2, wrong hardware type
               -) -3, wrong hardware length
330
              -) -4, pkt is null or the socket is invalid
331
332
333 static int GetClientPacket() {
       int res = GetPacket(&clientPacket, clientToServer);
334
       if (res == 0) {
335
336
           comp1 = PUSH_SERVER_QUEUE;
337
       return res;
338
339
       //return GetPacket(&clientPacket, clientToServer);
340 }
341
342 /**
* Send a packet to the server, taking it from ServerMessages. This function assumes
^{344} * that queue to be not empty. The function copies the message in the socket's buffer
_{345} * iff there are at least 300bytes free. 300 bytes is the minimum size of a sent packet.
* @precondition ServerMessages is not empty
```

```
_{347} \star @precondition ServerInfo contains both the server's IP and MAC address. If the latter
_{\rm 348} * is not known, an ARP request should be made in order to get it.
349 */
350 static void SendToServer() {
       // check if the buffer has enough space
351
       if (UDPIsPutReady(clientToServer) >= 300u) {
352
           BYTE
                                i; // used to add the 0 padding
353
           UDP_SOCKET_INFO
                                *socket = &UDPSocketInfo[activeUDPSocket]; //get the current
354
        socket
           // pop the packet
           PACKET_DATA
                                pkt:
356
357
           PacketListPop(&pkt, &ServerMessages);
358
           DisplayString(0, "Send to Server ");
359
           // set socket info
360
           socket -> remoteNode.IPAddr.Val = ServerInfo.IPAddr.Val;
361
           for(i = 0; i < 6; i++) {</pre>
362
               socket -> remoteNode.MACAddr.v[i] = ServerInfo.MACAddr.v[i];
364
365
366
           // copy header DHCP
           UDPPutArray((BYTE*)&(pkt.Header.MessageType), sizeof(pkt.Header.MessageType));
367
368
           UDPPutArray((BYTE*)&(pkt.Header.HardwareType), sizeof(pkt.Header.HardwareType));
           UDPPutArray((BYTE*)&(pkt.Header.HardwareLen));
369
370
           UDPPutArray((BYTE*)&(pkt.Header.Hops), sizeof(pkt.Header.Hops));
           UDPPutArray((BYTE*)&(pkt.Header.TransactionID), sizeof(pkt.Header.TransactionID)
           UDPPutArray((BYTE*)&(pkt.Header.SecondsElapsed), sizeof(pkt.Header.
372
       SecondsElapsed));
           UDPPutArray((BYTE*)&(pkt.Header.BootpFlags), sizeof(pkt.Header.BootpFlags));
373
           UDPPutArray((BYTE*)&(pkt.Header.ClientIP), sizeof(pkt.Header.ClientIP));
374
           UDPPutArray((BYTE*)&(pkt.Header.YourIP), sizeof(pkt.Header.YourIP));
375
376
           UDPPutArray((BYTE*)&(pkt.Header.NextServerIP));
           UDPPutArray((BYTE*)&(AppConfig.MyIPAddr), sizeof(AppConfig.MyIPAddr)); // giaddr
377
           UDPPutArray((BYTE*)&(pkt.Header.ClientMAC), sizeof(pkt.Header.ClientMAC));
378
379
           // the other fields are set to zero
380
           for (i = 0; i < 202u; i++) {</pre>
381
               UDPPut(0);
382
383
384
385
           // put magic cookie as per RFC 1533.
           UDPPut (99);
386
387
           UDPPut (130);
           UDPPut (83);
           UDPPut (99);
389
390
391
           // put message type
           UDPPut (DHCP_MESSAGE_TYPE);
392
         UDPPut (DHCP_MESSAGE_TYPE_LEN);
393
         UDPPut (pkt.MessageType);
394
395
           // Option: Subnet Mask
396
         UDPPut (DHCP_SUBNET_MASK);
397
         UDPPut(sizeof(IP_ADDR));
398
         UDPPutArray((BYTE*)&AppConfig.MyMask, sizeof(IP_ADDR));
399
400
           // Option: Server identifier
401
           UDPPut (DHCP_SERVER_IDENTIFIER);
402
403
           UDPPut (sizeof(IP_ADDR));
           UDPPutArray((BYTE*)&ServerInfo.IPAddr.Val, sizeof(IP_ADDR));
404
405
406
           // Option: Router/Gateway address
           UDPPut (DHCP_ROUTER);
407
           UDPPut (sizeof (TP ADDR)):
408
           UDPPutArray((BYTE*)&ServerInfo.IPAddr.Val, sizeof(IP_ADDR));
409
410
           // if there is an IP address, add it
411
           if (pkt.IPAddressNotNull == TRUE) {
               UDPPut (DHCP_PARAM_REQUEST_IP_ADDRESS);
413
```

```
UDPPut (DHCP_PARAM_REQUEST_IP_ADDRESS_LEN);
414
                UDPPutArray((BYTE*)&pkt.RequiredAddress, sizeof(IP_ADDR));
415
                IPAddressNotNull = FALSE; // reset the global variable used as a flag
416
417
418
            UDPPut(DHCP_END_OPTION); // end the packet
419
420
            // add zero padding to ensure compatibility with old BOOTP relays that discard
421
            // packets smaller that 300 octets
422
423
            while(UDPTxCount < 300u) {</pre>
                UDPPut(0);
424
425
426
            UDPFlush(); // transmit
427
       }
428
429 }
430
431 /**
   * Send a packet to the client, taking it from ClientMessages. This function assumes * that queue to be not empty. The function copies the message in the socket's buffer
432
433
   \star iff there are at least 300bytes free. 300 bytes is the minimum size of a sent packet.
434
   * @precondition ClientMessages is not empty
435
436
437 static void SendToClient() {
438
       // check if the buffer has enough space
       if (UDPIsPutReady(serverToClient) >= 300u) {
439
           BYTE
                                 i; // used to add te 0 padding
440
           UDP SOCKET INFO
                                 *socket = &UDPSocketInfo[activeUDPSocket]; //get the current
441
         socket
           // pop the packet from the queue
442
            PACKET_DATA
443
                                pkt;
            PacketListPop(&pkt, &ClientMessages);
444
            DisplayString(0, "Send to Client ");
445
446
            // set socket info
447
            socket -> remoteNode.IPAddr.Val = BROADCAST;
448
            socket -> remotePort = DHCP_CLIENT_PORT;
449
            for(i = 0; i < 6u; i++){ // copy client's MAC address (and take it from CHADDR)
450
                socket -> remoteNode.MACAddr.v[i] = pkt.Header.ClientMAC.v[i];
451
452
453
454
            // copy header DHCP
            UDPPutArray((BYTE*)&(pkt.Header.MessageType), sizeof(pkt.Header.MessageType));
455
456
            UDPPutArray((BYTE*)&(pkt.Header.HardwareType), sizeof(pkt.Header.HardwareType));
            UDPPutArray((BYTE*)&(pkt.Header.HardwareLen), sizeof(pkt.Header.HardwareLen));
457
            UDPPutArray((BYTE*)&(pkt.Header.Hops), sizeof(pkt.Header.Hops));
458
            UDPPutArray((BYTE*)&(pkt.Header.TransactionID), sizeof(pkt.Header.TransactionID)
459
       );
            UDPPutArray((BYTE*)&(pkt.Header.SecondsElapsed), sizeof(pkt.Header.
460
       SecondsElapsed));
            UDPPutArray((BYTE*)&(pkt.Header.BootpFlags), sizeof(pkt.Header.BootpFlags));
461
            UDPPutArray((BYTE*)&(pkt.Header.ClientIP));
462
            UDPPutArray((BYTE*)&(pkt.Header.YourIP), sizeof(pkt.Header.YourIP));
463
            UDPPutArray((BYTE*)&(pkt.Header.NextServerIP), sizeof(pkt.Header.NextServerIP));
464
            UDPPutArray((BYTE*)&(AppConfig.MyIPAddr), sizeof(AppConfig.MyIPAddr)); // giaddr
465
            UDPPutArray((BYTE*)&(pkt.Header.ClientMAC), sizeof(pkt.Header.ClientMAC));
466
467
            // the other fields are set to zero
468
            for (i = 0; i < 202u; i++) {
469
470
                UDPPut(0);
471
472
473
            // put magic cookie 0x63538263, little endian
            UDPPut (0x63):
474
         UDPPut (0x82):
475
         UDPPut (0x53);
476
         UDPPut (0x63);
477
478
            // put message type
           UDPPut (DHCP_MESSAGE_TYPE);
480
```

```
UDPPut (DHCP_MESSAGE_TYPE_LEN);
          UDPPut (pkt.MessageType);
483
484
            UDPPut (DHCP_END_OPTION); // end packet
485
            // add zero padding to ensure compatibility with old BOOTP relays that discard
486
            // packets smaller that 300 octets
            while (UDPTxCount < 300u) {</pre>
488
                UDPPut(0);
489
491
492
            UDPFlush(); // transmit
493
494 }
495
496 /** Schedule the first paralel component.
   \star This compoennt is responsible for getting the packets from the network and pushing
497
    \star in the right queue (depending if a packet comes from the server or from the client).
498
499
500 static void Component1() {
       switch(comp1) {
501
502
            // root
           case WAITING_FOR_MESSAGE:
503
                if (prevFromClient == TRUE) {
504
                    comp1 = PUSH_SERVER_QUEUE;
505
                } else {
506
507
                    GetServerPacket();
508
                    GetClientPacket();
                    // serve the server, but serve the client at next round
509
                    if (prevFromServer == TRUE && comp1 == PUSH_SERVER_QUEUE) {
510
                        prevFromServer = FALSE;
511
                        prevFromClient = TRUE;
512
                        comp1 = PUSH_CLIENT_QUEUE;
513
                    }
514
                }
                break;
516
            case PUSH_CLIENT_QUEUE:
517
518
                if (PacketListPush(&ClientMessages, &serverPacket) == 0) {
                    // cross the transiction iff the push succeeded
519
                    comp1 = PUSH_CLIENT_QUEUE_T;
520
521
                } else {
                   break;
522
                }
            case PUSH_CLIENT_QUEUE_T:
                comp1 = WAITING_FOR_MESSAGE;
                break;
526
527
            // client branch
            case PUSH_SERVER_QUEUE:
528
                if (PacketListPush(&ServerMessages, &clientPacket) == 0) {
529
                    // cross the transiction iff the push succeeded
530
531
                    comp1 = PUSH_SERVER_QUEUE_T;
                } else {
532
                    break;
533
            case PUSH_SERVER_QUEUE_T:
535
               comp1 = WAITING_FOR_MESSAGE;
536
537
                break;
       }
538
539 }
540
541 /**
_{\rm 542} * Schedule the second parallel component.
    * This component is responsible for popping a packet coming from the client,
543
^{544} \star if any, and sending it to the server. It also makes an ARP request the very
_{\rm 545} _{\star} first time a transmission to the server is required. After its MAC address
^{546} * has been resolved, it sets the 'serverKnown' flag to TRUE and stops sending
* ARP requests.
549 static void Component2() {
```

```
switch (comp2) {
            case SERVER_QUEUE_WAITING:
551
                if (!PacketListIsEmpty(&ServerMessages)) {
552
                     // cross iff the queue is not empty
                     comp2 = SERVER_QUEUE_WAITING_T;
554
                 } else {
556
                    break;
557
            case SERVER_QUEUE_WAITING_T:
558
559
                comp2 = GET_SERVER_IP_ADDRESS;
                break:
560
            case GET_SERVER_IP_ADDRESS:
561
                switch (comp2_2) {
562
                     case IDENTIFY_SERVER:
563
564
                         if (serverKnown == TRUE) {
                             comp2 = IDENTIFY_SERVER_TO_TX;
565
                             \ensuremath{\mathsf{break}}; // immediately go to transmission if the server is known
566
567
                         } else {
                             comp2_2 = IDENTIFY_SERVER_TO_ARP;
568
569
                         }
                     case IDENTIFY_SERVER_TO_ARP:
570
                         if (N == FALSE) {
571
572
                             comp2_2 = SEND_ARP_REQUEST;
                             N = TRUE;
573
574
                         }
575
                     case SEND_ARP_REQUEST:
576
577
                         ARPResolve(&ServerInfo.IPAddr);
578
                         DisplayString(0, "Send ARP Request");
                         comp2_2 = SEND_ARP_REQUEST_T;
579
                     case SEND_ARP_REQUEST_T:
580
                         comp2_2 = PROCESS_ARP_ANSWER;
581
582
                         N = FALSE;
                         break;
583
                     case PROCESS_ARP_ANSWER:
584
                         if (ARPIsResolved(&ServerInfo.IPAddr, &ServerInfo.MACAddr) == TRUE)
585
                             DisplayString(0, "MACAddr Resolved");
586
587
                              serverKnown = TRUE;
                             comp2_2 = PROCESS_ARP_ANSWER_T;
588
                         } else {
589
590
                             if (N == FALSE) {
                                  comp2_2 = SEND_ARP_REQUEST;
591
592
                                  N = TRUE;
593
                             break;
595
                         }
596
                     case PROCESS_ARP_ANSWER_T:
                         if (N == FALSE) {
597
598
                             comp2_2 = SEND_ARP_REQUEST;
                              comp2 = TX_TO_SERVER;
                             N = TRUE:
600
601
                         break:
602
603
                if (comp2 != IDENTIFY_SERVER_TO_TX) {
604
                     \ensuremath{//} go through the transition if the server was known
605
606
                     break;
                }
607
            case IDENTIFY_SERVER_TO_TX:
608
                if (N == FALSE) {
609
                    N = TRUE;
610
                     comp2 = TX_TO_SERVER;
611
612
                break;
613
614
            case TX_TO_SERVER:
                SendToServer();
615
                comp2 = TX_TO_SERVER_T;
616
            case TX_TO_SERVER_T:
               N = FALSE;
618
```

```
comp2 = SERVER_QUEUE_WAITING;
619
621
622 }
623
624 /**
* Schedule the third parallel component.
626 * This component is responsible for popping a packet coming from the server,
_{\rm 627} * if any, and sending it to the client.
629 static void Component3() {
630
       switch (comp3) {
           case CLIENT_QUEUE_WAITING:
631
                if (!PacketListIsEmpty(&ClientMessages)) {
632
633
                   comp3 = CLIENT_QUEUE_WAITING_T;
                } else {
634
635
                   break;
               }
           case CLIENT_QUEUE_WAITING_T:
637
638
               if (N == FALSE) {
                   comp3 = TX_TO_CLIENT;
639
                    N = TRUE;
640
641
               break;
642
           case TX_TO_CLIENT:
643
644
               SendToClient();
               comp3 = TX_TO_CLIENT_T;
645
            case TX_TO_CLIENT_T:
646
647
               N = FALSE;
               comp3 = CLIENT_QUEUE_WAITING;
648
649
               break;
650
       }
651 }
652
653 /**
_{654} * Handle the scheduling of the DHCP relay, including the transitions
^{655} * among te different components. The scheduling is done iff the DHCP
_{656} _{\star} is actually enabled, meaning the flag 'AppConfig.Flags.bIsDHCPEnabled'
* is not zero.
658 */
659 static void DHCPRelayTask() {
660
       if (AppConfig.Flags.bIsDHCPEnabled) {
           switch(currentComponent) {
661
662
               case INIT:
                    if (DHCPRelayInit() == -1) {
663
                        UDPClose(serverToClient);
664
665
                        UDPClose(clientToServer);
666
                        currentComponent = INIT;
667
668
                    break;
                case COMP1:
669
                   Component1();
670
                    currentComponent = COMP2;
671
                   break;
672
673
                case COMP2:
                   Component2();
674
                    currentComponent = COMP3;
675
676
                   break;
                case COMP3:
677
678
                    Component3();
                    currentComponent = COMP1;
679
680
                    break:
681
       } else {
682
           DisplayString(0, "DHCP Not Enabled");
683
684
685 }
686
688 // Main application entry point.
```

```
689 //
691
692 #if defined(__18CXX) || defined(__SDCC__)
693 void main (void)
694 #else
695 int main (void)
696 #endif
697 {
698 static TICK t = 0;
699 TICK nt = 0; //TICK is DWORD, thus 32 bits
700 BYTE loopctr = 0; //ML Debugging
701 WORD lloopctr = 14; //ML Debugging
702
703 static DWORD dwLastIP = 0;
704
        // Initialize interrupts and application specific hardware
705
        InitializeBoard();
706
707
        // Initialize TimerO, and low priority interrupts, used as clock.
708
        TickInit();
709
710
711
        // Initialize Stack and application related variables in AppConfig.
        InitAppConfig();
712
713
714
        // Initialize core stack layers (MAC, ARP, TCP, UDP) and
        // application modules (HTTP, SNMP, etc.)
715
716
        StackInit();
717
        #ifdef UART DEBUG ON
718
719
            UARTConfig();
        #endif
720
721
        #ifdef USE_LCD
722
           LCDTaskInit();
723
724
        #endif
725
        // Initialize and display message on the LCD
726
727
        LCDInit();
        DelayMs(100);
728
        DisplayString (0, "Waiting client "); //first arg is start position on 32 pos LCD
729
730
        currentComponent = INIT;
731
732
        // Now that all items are initialized, begin the co-operative
733
        // multitasking loop. This infinite loop will continuously
734
735
        // execute all stack-related tasks, as well as your own
736
        // application's functions. Custom functions should be added
        // at the end of this loop.
737
738
        // Note that this is a "co-operative multi-tasking" mechanism // where every task performs its tasks (whether all in one shot
739
740
        // or part of it) and returns so that other tasks can do their
741
        // job.
742
743
        // If a task needs very long time to do its job, it must be broken
        // down into smaller pieces so that other tasks can have CPU time.
744
745
746
        while(1)
747
748
749
             // Blink LEDO (right most one) every second.
750
751
            nt = TickGetDiv256();
            if((nt - t) >= (DWORD)(TICK_SECOND/1024ul))
752
753
            {
754
                t = nt;
                LED0_IO ^= 1;
ClrWdt(); //Clear the watchdog
755
756
757
758
```

```
// This task performs normal stack task including checking
759
           // for incoming packet, type of packet and calling
760
            // appropriate stack entity to process it.
761
762
           StackTask();
763
           // This tasks invokes each of the core stack application tasks
764
765
                     StackApplications(); //all except dhcp, ping and arp
766
           // LCD task
767
768
           #ifdef USE_LCD
              LCDTask();
769
770
           #endif
771
            // Process application specific tasks here.
772
            #ifdef STACK_USE_DHCP_RELAY
              DHCPRelayTask();
774
775
            #endif
776
           // If the local IP address has changed (ex: due to DHCP lease change)
777
778
           // write the new IP address to the LCD display, UART, and Announce
           // service
779
           if (dwLastIP != AppConfig.MyIPAddr.Val)
780
781
               dwLastIP = AppConfig.MyIPAddr.Val;
782
                    #if defined(__SDCC__)
783
                        DisplayIPValue(dwLastIP); // must be a WORD: sdcc does not
784
                                                 // pass aggregates
785
786
787
                        DisplayIPValue(AppConfig.MyIPAddr);
                    #endif
788
789
       }//end of while(1)
790
791 }//end of main()
793 /**********
794 Function DisplayWORD:
writes a WORD in hexa on the position indicated by
796 pos.
797
    - pos=0 -> 1st line of the LCD
    - pos=16 -> 2nd line of the LCD
798
799
800
    __SDCC__ only: for debugging
801 ***
802 #if defined( SDCC )
803 void DisplayWORD(BYTE pos, WORD w) //WORD is a 16 bits unsigned
804 {
805
       BYTE WDigit[6]; //enough for a number < 65636: 5 digits + \0
806
       BYTE j;
       BYTE LCDPos=0; //write on first line of LCD
807
       unsigned radix=10; //type expected by sdcc's ultoa()
808
809
810
       LCDPos=pos;
       ultoa(w, WDigit, radix);
811
       for(j = 0; j < strlen((char*)WDigit); j++)</pre>
812
813
          LCDText[LCDPos++] = WDigit[j];
814
815
816
       if(LCDPos < 32u)
          LCDText[LCDPos] = 0;
817
       LCDUpdate();
818
819 }
820 / * 7
821 Function DisplayString:
822 Writes an IP address to string to the LCD display
823 starting at pos
824 ****
825 void DisplayString(BYTE pos, char* text)
826 {
     BYTE l= strlen(text)+1;
BYTE max= 32-pos;
```

```
strlcpy((char*)&LCDText[pos], text,(1<max)?1:max );</pre>
      LCDUpdate();
831 }
832 #endif
833
834 /*********
835 Function DisplayIPValue:
836 Writes an IP address to the LCD display
837 ****
839 #if defined(__SDCC__)
void DisplayIPValue(DWORD IPdw) // 32 bits
841 #else
void DisplayIPValue(IP_ADDR IPVal)
843 #endif
844 {
       BYTE IPDigit[4]; //enough for a number <256: 3 digits + \0
845
846
       BYTE i;
       BYTE j;
847
       BYTE LCDPos=16; //write on second line of LCD
848
849 #if defined(__SDCC__)
      unsigned int IP_field, radix=10; //type expected by sdcc's uitoa()
850
851 #endif
852
       for(i = 0; i < sizeof(IP_ADDR); i++) //sizeof(IP_ADDR) is 4</pre>
853
854
855 #if defined(__SDCC__)
          IP_field = (WORD) (IPdw>>(i*8))&0xff;
856
                                                     //ML
857
          uitoa(IP_field, IPDigit, radix);
858 #else
859
          uitoa((WORD)IPVal.v[i], IPDigit);
860 #endif
861
          for(j = 0; j < strlen((char*)IPDigit); j++)</pre>
862
863
        LCDText[LCDPos++] = IPDigit[j];
864
865
          if(i == sizeof(IP ADDR)-1)
866
867
         break;
          LCDText[LCDPos++] = '.';
868
869
870
       if(LCDPos < 32u)</pre>
871
872
          LCDText[LCDPos] = 0;
873
       LCDUpdate();
874 }
875
876
877 /********
878
    Function:
      static void InitializeBoard(void)
879
880
     Description:
881
       This routine initializes the hardware. It is a generic initialization
882
       routine for many of the Microchip development boards, using definitions
883
       in HardwareProfile.h to determine specific initialization.
884
885
886
     Precondition:
       None
887
888
     Parameters:
889
      None - None
890
891
     Returns:
892
893
      None
894
     Remarks:
895
896
      None
898 static void InitializeBoard(void)
```

```
899 {
     // LEDs
       LED0\_TRIS = 0; //LED0
901
     LED1_TRIS = 0; //LED1
LED2_TRIS = 0; //LED2
902
903
     LED3_TRIS = 0; //LED_LCD1

LED4_TRIS = 0; //LED_LCD2

LED5_TRIS = 0; //LED5=RELAY1

LED6_TRIS = 0; //LED7=RELAY2
904
905
906
907
908 #if (!defined(EXPLORER_16) &&!defined(OLIMEX_MAXI)) // Pin multiplexed with
    // a button on EXPLORER_16 and not used on OLIMEX_MAXI
909
910
     LED7\_TRIS = 0;
911 #endif
            LED_PUT(0x00); //turn off LED0 - LED2
912
     RELAY_PUT(0x00); //turn relays off to save power
913
914
     // Set clock to 25 {\rm MHz}
915
916
     // The primary oscillator runs at the speed of the 25 \mathrm{MHz} external quartz
       OSCTUNE = 0 \times 00;
917
918
     // Switch to primary oscillator mode,
919
           // regardless of if the config fuses tell us to start operating using
920
921
            // the the internal RC
     // The external clock must be running and must be 25MHz for the
922
923
     \ensuremath{//} Ethernet module and thus this Ethernet bootloader to operate.
        if(OSCCONbits.IDLEN) //IDLEN = 0x80; 0x02 selects the primary clock
924
       OSCCON = 0x82;
925
926
     else
927
       OSCCON = 0x02;
928
     // Enable Interrupts
929
     RCONbits.IPEN = 1;  // Enable interrupt priorities
930
           INTCONbits.GIEH = 1;
931
            INTCONbits.GIEL = 1;
932
933
934 }
935
936 /*********
937
    * Function: void InitAppConfig(void)
938
                        MPFSInit() is already called.
    * PreCondition:
939
940
    * Input:
                         None
941
942
    * Output:
                        Write/Read non-volatile config variables.
943
944
945 * Side Effects:
                        None
946
    * Overview:
947
                         None
948
    * Note:
                         None
949
950
952 static void InitAppConfig(void)
953 {
AppConfig.Flags.bIsDHCPEnabled = TRUE;
     AppConfig.Flags.bInConfigMode = TRUE;
955
956
957 //ML using sdcc (MPLAB has a trick to generate serial numbers)
958 // first 3 bytes indicate manufacturer; last 3 bytes are serial number
     AppConfig.MyMACAddr.v[0] = 0;
959
     AppConfig.MyMACAddr.v[1] = 0x04;
960
961
     AppConfig.MyMACAddr.v[2] = 0xA3;
     AppConfig.MyMACAddr.v[3] = 0x01;
962
     AppConfig.MyMACAddr.v[4] = 0x02;
963
964
     AppConfig.MyMACAddr.v[5] = 0x03;
965
966 //ML if you want to change, see TCPIPConfig.h
967 AppConfig.MyIPAddr.Val = MY_DEFAULT_IP_ADDR_BYTE1 |
       MY_DEFAULT_IP_ADDR_BYTE2<<8ul | MY_DEFAULT_IP_ADDR_BYTE3<<16ul |
968
```

```
MY_DEFAULT_IP_ADDR_BYTE4<<24ul;
969
     AppConfig.DefaultIPAddr.Val = AppConfig.MyIPAddr.Val;
970
     AppConfig.MyMask.Val = MY_DEFAULT_MASK_BYTE1 |
MY_DEFAULT_MASK_BYTE2<<8ul | MY_DEFAULT_MASK_BYTE3<<16ul |
971
972
                MY_DEFAULT_MASK_BYTE4<<24ul;
973
     AppConfig.DefaultMask.Val = AppConfig.MyMask.Val;
974
     AppConfig.MyGateway.Val = MY_DEFAULT_GATE_BYTE1 |
975
                MY_DEFAULT_GATE_BYTE2<<8ul | MY_DEFAULT_GATE_BYTE3<<16ul |
976
                MY_DEFAULT_GATE_BYTE4<<24ul;
977
978
     AppConfig.PrimaryDNSServer.Val = MY_DEFAULT_PRIMARY_DNS_BYTE1 |
                MY_DEFAULT_PRIMARY_DNS_BYTE2<<8ul |
979
                MY_DEFAULT_PRIMARY_DNS_BYTE3<<16ul |
980
                MY_DEFAULT_PRIMARY_DNS_BYTE4<<24ul;
981
     AppConfig.SecondaryDNSServer.Val = MY_DEFAULT_SECONDARY_DNS_BYTE1 |
982
                MY_DEFAULT_SECONDARY_DNS_BYTE2<<8ul |
983
                MY_DEFAULT_SECONDARY_DNS_BYTE3<<16ul
984
                MY_DEFAULT_SECONDARY_DNS_BYTE4<<24ul;
985
```

Listing 4: DHCPRelay.c