

# Algorithmic Design of On-chip mm-wave Slow-wave Quadrature Coupler in Silicon Technology

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**Abstract**—In this paper, we have presented an algorithmic design methodology for designing integrated millimeter-wave quadrature coupler based on slow-wave coupled line. The design methodology is verified by a 3-dB quadrature coupler in a commercially available CMOS technology. The designed CMOS coupler covers the whole E- to W-band and occupies only 0.0115 mm<sup>2</sup> silicon area. The consumed wafer area is 50% smaller compared to the conventional microstrip line based couplers. Measurement of the slow-wave 90° coupler shows a -3.5 dB through and -4.4 dB coupling at 90 GHz, and less than ±1-dB amplitude and ±4° phase errors from 55 to 110 GHz.

**Index Terms**—Coupler, coupled line, CMOS, E-band, millimeter-wave, slow-wave, quadrature, W-band, 3-dB coupling.

## I. INTRODUCTION

COMMUNICATION at millimeter-wave frequencies has drawn a lot of consideration due to the availability of wide fractional bandwidths and small antenna sizes. Recently, there has been increasing research and commercial development of integrated circuits for wireless communication at E- and W-band and automotive radar application at 77 GHz. Commercially available silicon technologies show potentiality to realize cost-effective, highly integrated systems for these applications [1]–[3]. Like in microwave design techniques, silicon IC designs at millimeter-wave frequencies also include transmission lines, splitters, combiners, baluns, and couplers [Floyd1], [Niknjad], [Mikko.RMixer]. Among these passive circuit elements, 3-dB coupler having 90° phase difference between the outputs is a very important building block. It can be used for I/Q modulator-demodulator [ISSCC.shimawaki], phase shifters [IBM.60G], triplers [Northop.tripler], and various mixer topologies [D.MWCL], [Rebeiz.mixer]. These couplers are usually realized by the design technique called Lange

coupler, invented by J. Lange [MKK.6]. The Lange couplers are popular due to its broadband and accurate quadrature performance. The coupler design techniques involve quarter wavelength lines at the design frequency and are usually realized with microstrip lines. One major drawback to microstrip lines on the standard silicon technologies is the low effective dielectric constant ( $\sim 4$ ) which is defined by the surrounding media (silicon dioxide). Therefore, the sizes of the couplers become very large.

Efforts have been given to miniaturize the size of silicon quadrature couplers [Floyd.IMS], [EuMIC.Ferrari]. In [Floyd.IMS], thin film microstrip line was used to achieve the required coupling coefficient and an aggressive meandering was implemented to reduce the size of the coupler. In [EuMIC.Ferrari], coupler based on coupled slow-wave coplanar waveguide (CS-CPW) was presented and required coupling was achieved by modifying the shielding ribbons. However, the work has considered BiCMOS technology which allowed the designers to use wider lines compared with a standard CMOS technology. Furthermore, none of the works did address the modeling methodology used for designing their proposed complex structures.

In this work, an algorithmic design technique for a compact mm-wave coupler is presented which is feasible for any silicon technology. The coupler design is based on the use of CS-CPW and approaches a complete new way to miniaturize the structure. The proposed structure is easy to model and scalable in length. The structure provides required coupling by placing metal slabs on coupled lines within the CS-CPW structure. Since the coupler structure is based on CS-CPW structure, the modeling of the coupler relies on the fast and reliable modeling technique proposed in [Dristy.EuMIC]. Furthermore, the behavior of a CS-CPW is analyzed and based on the analysis; a conclusion of defining a good initial value of the design parameters for the EM simulation is drawn. For a proof-of-concept, a 3-dB quadrature coupler was designed. The coupler exhibits excellent performance over the entire E- to W-band.

The paper is organized as follows. Section II describes briefly the modeling methodology of the CS-CPWs. A study on the behavior of CS-CPW is covered in section III. A mathematical model is derived in section IV from the behavior analysis to estimate the initial design parameters for the EM simulation. An algorithmic design of the coupler based on CS-CPW is presented in section V. Section VI includes the design detail of the 3-dB quadrature coupler. Measurement results are presented in section VII.

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CS-CPW\_basic.png

Fig. 1. 3-D view of a coupled slow-wave coplanar waveguide (CS-CPW).

## II. MODELING OF THE COUPLED SLOW-WAVE CO-PLANAR WAVEGUIDE

A coupled slow-wave co-planar waveguide (CS-CPW) is an efficient way of implementing a coupled line in silicon technology. The CS-CPW are basically the modified versions of slow-wave co-planar waveguides (S-CPWs) which are popular for their compactness [J.Long], [Ferrari2]. 3-D view of a fundamental CS-CPW is illustrated in Fig. 1. The CS-CPW structures are composed of two signal lines of widths  $W$  separated by a gap  $S$ , two side-ground lines with widths of  $GW$  located in a distance of  $SG$  from the signal lines, and slotted metal strips below the signal lines at a distance of  $D$ . Simulation of the complete CS-CPW structure as in Fig. 1 is computationally heavy and time-consuming. Therefore, an efficient modeling methodology for designing the CS-CPW is proposed in [D.EuMIC] and successfully verified in [D. TMTT]. In the modeling approach, the side-ground lines serve as well-defined ground return current paths for the even-mode signal propagation and divides the EM problem into four parts [D.EuMIC]. The EM problem is first divided into odd- and even-mode analysis and each mode is further decoupled into odd- and even-mode  $R$  &  $L$  and  $C$  &  $G$  analysis. Here the  $R$ ,  $L$ ,  $C$ , and  $G$  are the typical transmission line parameters (Telegraphers equation) [Pozar] which support the transverse electromagnetic (TEM) propagation. A graphical representation of the modeling method is illustrated in Fig. 2. The port definitions for the EM simulations of the odd- and even-mode analysis are also included in the Fig. 2. Four two-port  $S$ -parameter data sets are obtained from these four EM simulations. Using these data sets, the odd- and even-mode  $R$ ,  $L$ ,  $C$ , and  $G$  parameters are calculated by the formulas stated in [Einstad]. Once the  $R$ ,  $L$ ,  $C$ , and  $G$  parameters for both the modes are obtained, the other essential transmission



CMOS\_technology5.png

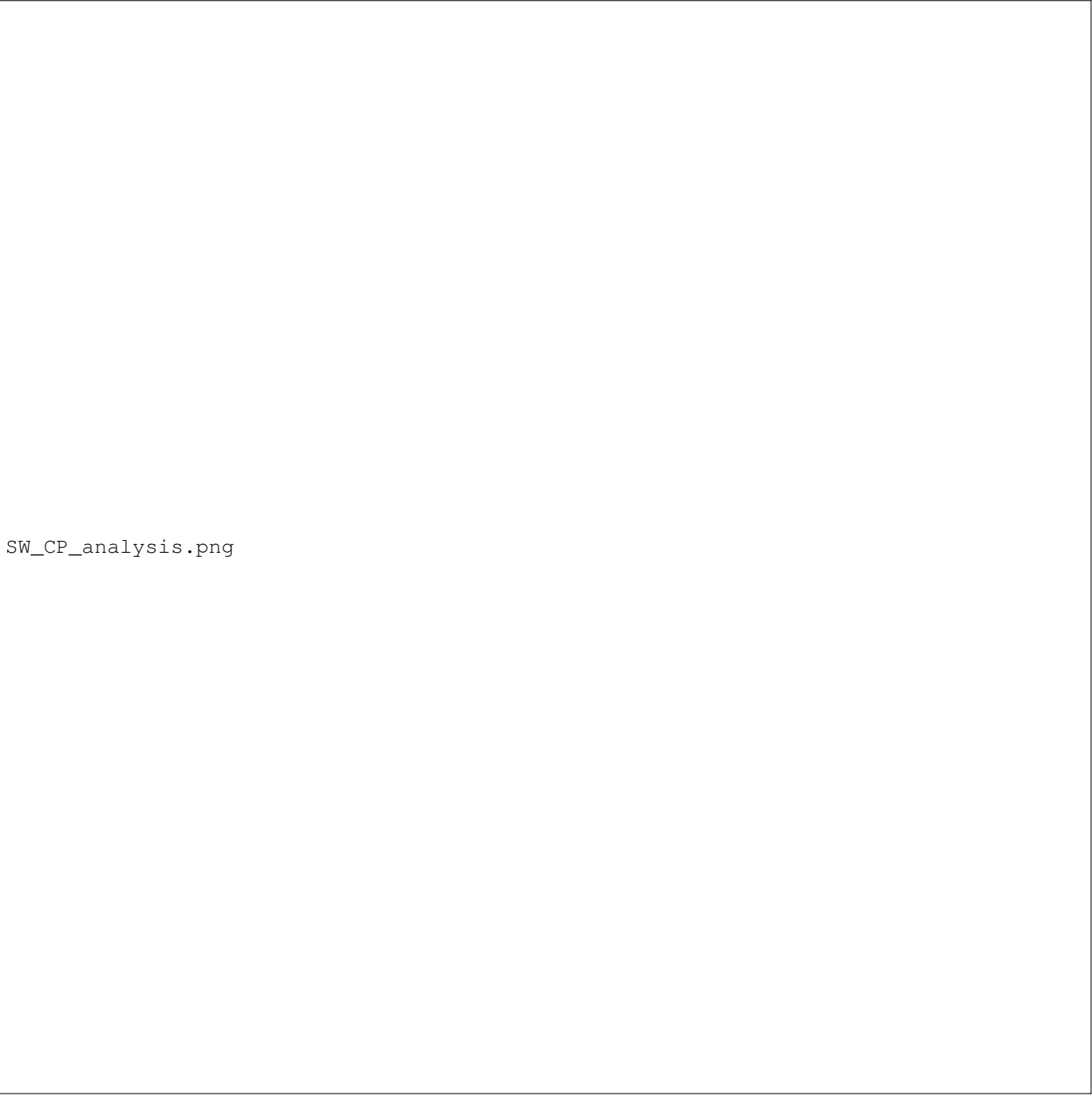
Fig. 3. A generic cross-sectional view of the applied CMOS technology and a CS-CPW structure.

line parameters can be calculated from [Pozar]. The necessary model parameters for a coupled transmission line are odd- and even-mode characteristic impedances ( $Z_{0o}$ ,  $Z_{0e}$ ), effective dielectric constants ( $\epsilon_{ro}$ ,  $\epsilon_{re}$ ), attenuation constants ( $\alpha_o$ ,  $\alpha_e$ ), and loss tangent ( $\tan\delta$ ). To design a scalable (in length) electrical model of the CS-CPW, a physical coupled line model (CLINP) supported by the simulator Keysight's ADS [ADS] is used. A first-order frequency-dependent loss model is considered in the line model.

## III. BEHAVIOR ANALYSIS OF COUPLED SLOW-WAVE CO-PLANAR WAVEGUIDES

In this section, we have analyzed the electrical behavior of the CS-CPW structure by EM simulations. We have studied the structure, mainly to derive a table showing the behavior of the structure at different geometric parameters. This table will help the designers to select the suitable initial values of the geometric parameters for the EM simulations in order to obtain a given design goal. Furthermore, the table will also be useful for the optimization iterations of the design. For this study, we had varied the geometric parameters  $S$ ,  $D$ ,  $W$ ,  $SG$ ,  $GW$  and observed the changes in odd- and even-mode inductances ( $L_{odd}$ ,  $L_{even}$ ), capacitances ( $C_{odd}$ ,  $C_{even}$ ), and impedances ( $Z_{0o}$ ,  $Z_{0e}$ ). While varying one parameter, the other parameters were kept constant. A commercially available standard 65-nm CMOS technology was used and we have considered a length of  $110 \mu\text{m}$  of CS-CPW for this analysis. The EM simulations were performed based on the modeling methodology described in the previous section. A generic cross-sectional view of the CMOS technology applied in this study is shown in Fig. 3.

At first we had created a CS-CPW structure with the arbitrary design variables and use its fundamental structure as



SW\_CP\_analysis.png

Fig. 2. Simplified representation of modeling a coupled slow-wave coplanar waveguide.

shown in Fig. 1 and simulated in a 2.5D EM simulator. Apart from the fundamental structure, we had also analyzed another structure where two metal layers were electrically connected with vias for the signal lines as shown in Fig. 3. The modified structure gave lower inductances and capacitances per unit length. The changes in  $L_{odd}$ ,  $L_{even}$ ,  $C_{odd}$ ,  $C_{even}$ ,  $Z_{0o}$ , and  $Z_{0e}$  for different geometric parameters of the CS-CPW are shown in Fig. 4. These variations are tabulated in Table I with the indicators showing increasing ( $\uparrow$ ), decreasing ( $\downarrow$ ), and no changing ( $\nabla$ ) states over the changes in geometric parameters.

This behavior analysis gives a reasonable first approxima-

tion of selecting the design variables for the EM simulation. For an example, consider a case where we want to obtain a ratio of 1:6 for the odd- and even-mode impedances. It is evident from Fig. 4 and Table I that all the geometric parameters except the  $SG$  and  $GW$  have the influence on both the odd- and even-mode impedances. Therefore, for an initial guess to obtain such a large ratio, we must choose a minimum possible gap  $S$  between the signal lines. Moderate values can be chosen for the  $W$  and  $D$  and since the even-mode return current paths are defined to the side-ground lines, the even-mode impedances can be controlled by varying the  $SG$  and  $GW$

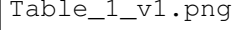



Fig. 5. Typical coupled-line coupler with its ports definition.

without affecting the odd-mode impedances. This property of CS-CPW gives a great design flexibility.

Alternatively, instead of speculate the initial design variables from the behavior of the structure, one can perform linear regressions to estimate the initial values for the design variables from number of simulation data. In the following section, a possible first degree linear fit model is described.

#### IV. MATHEMATICAL MODEL FOR ESTIMATING THE DESIGN PARAMETERS

Will be added later.

#### V. ALGORITHMIC DESIGN OF THE COUPLERS USING CS-CPW

In this section, we have described a step-by-step procedure to design a coupler using CS-CPW structure.

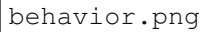
*Step 1):* At first, it is important to set the design goal. Depending on the coupler types, i.e. directional coupler with a certain coupling factor or 3-dB quadrature coupler the odd- and even-mode characteristic impedances are the key design parameters to be obtained. Fig. 5 shows a typical coupled-line coupler with its ports definition. It has been shown in the literature that the maximum coupling between the input port and the coupled port occurs at a frequency, when the lengths of the two identical parallel lines are quarter wavelengths at that frequency [R.Mongia]. Standard mathematical equations to calculate the required odd- and even-mode characteristic impedances for the coupled-line coupler are derived in [R. Mongia] shown in Eq. 1 and 2. Alternatively, an ideal coupled line model from the microwave circuit simulator Keysight's ADS [ADS] or AWR Microwave Office [MWO] can be used to simulate the required odd- and even-mode impedances.

$$Z_{0e} = Z_0 \sqrt{\frac{1+k}{1-k}} \quad (1)$$

$$Z_{0o} = Z_0 \sqrt{\frac{1-k}{1+k}} \quad (2)$$

*Step 2):* Once the design goal is determined, we have to make an initial guess of the geometric parameters for the EM simulation. A suitable initial guess of these parameters is critical since it will directly affect the total time required for the design task. As stated in the previous section that the behavior of the CS-CPW shown in Table I or the mathematical modeling approach described in the previous section can be used to predict the initial values of the geometric parameters.

*Step 3):* An efficient EM simulation methodology is important to simulate the CS-CPW structure as the structure has five design variables. As a result, a high number of iterations may require to achieve the given design goal. Therefore, the modeling methodology of CS-CPW structure explained in the section II is proposed for designing the couplers. Since the structure has been modeled using the transmission line parameters [Pozar], the proposed CS-CPW model is scalable in terms of the length. Hence for designing a quadrature coupler, we do not need to simulate at the quarter wavelength of the line but a fractional length of the line. This will further reduce the simulation time. On the other hand, it is also important to check the complete coupler performances, i.e. through, coupling, isolation, and directivity at the quarter wavelength of the line. However, if we simulate the quarter wavelength of the line, we will not be able to extract the reliable line parameters due to the resonance effect. Therefore, the scalable line parameters obtained from the EM simulations are inserted to a built-in physical coupled line model (CLINP) from the



behavior.png

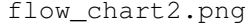
Fig. 4. Effect of the gap  $S$ , shielding distance  $D$ , signal width  $W$ , ground width  $GW$ , and signal to ground gap  $SG$  over the odd- and even-mode inductances, capacitances, and characteristic impedances.

microwave circuit simulator ADS [ADS] for the complete coupler simulations.

*Step 4):* For this CS-CPW based coupler design approach, few iterations may require to obtain the given design goal although the behavior table will help significantly to reduce the number of iterations. If the design goal is not achievable with the fundamental structure of the CS-CPW even after few iterations then the structure can be modified based on the requirement. For an instance, in our design example (described in the next section) we have increased the coupling

by modifying the signal lines of the CS-CPW for obtaining an aggressive odd-mode slow-wave effect.

*Step 5):* Finally, depending on the coupler applications, the signal lines can be folded in a similar fashion as in the Lange coupler to place the outputs or inputs on the same side. To finish the design, a complete coupler needs to be drawn in agreement with the design rules for the selected silicon technology. A flow-chart is shown in Fig. 6 to depict the design flow.



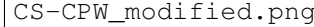
flow\_chart2.png

Fig. 6. Design flow of the couplers using CS-CPW.

## VI. E- TO W-BAND COUPLER DESIGN EXAMPLE

The proposed algorithm for designing CS-CPW based coupler was the basis of this design example. The design example had considered a 3-dB quadrature coupler which covers the entire E- and W-band. For a 3-dB quadrature coupler, the voltage coupling coefficient,  $k$  is calculated to be 0.708. Hence, by using the Eq. 1 and 2, the odd- and even-mode impedances become  $Z_{0o} = 20.67 \Omega$ , and  $Z_{0e} = 120.9 \Omega$  for a  $50 \Omega$  system. As mentioned in the design algorithm, the initial design parameters for the EM simulations can be estimated from the Table I or the mathematical model. In this work, we had experimented both types of approaches. The experimental data obtained during the behavior analysis of the CS-CPW were used for the mathematical modeling and the design rules from the technology were applied for the boundary conditions. The design parameters estimated from the both approaches and their possible outcomes are tabulated in Table II. Implementable physical dimensions from the estimated values were used for the corresponding EM simulations. Because of the required values of  $Z_{0o}$  and  $Z_{0e}$ , the CS-CPW structure shown in Fig. 3 was preferred in the initial design phase. A fractional line length of  $110 \mu\text{m}$  was considered for the EM simulations.

It can be seen from the Table II that the EM simulations using the design parameters obtained from the mathematical model had given better coupling coefficient. Therefore, these design parameters were used as initial values for the optimization iterations. However, EM simulations results using the values speculated from the behavioral table were also close to the design goal. The required coupling coefficient was achieved with single iteration; but the exact value of  $Z_{0o}$  and  $Z_{0e}$  could not be obtained. Note that this solution will sacrifice input match for coupling, as the port impedance is



CS-CPW\_modified.png

Fig. 7. Modified CS-CPW structure introduced for fulfilling the metal density requirements.

defined by,  $Z_0 = \sqrt{Z_{0o}Z_{0e}}$  and that is  $\sim 55 \Omega$ . Nevertheless, we had drawn the layout of the coupler structure with these geometric parameters in the CMOS technology. However, the layout experienced metal density errors. In order to fulfill the metal density requirement, we had placed all the metal layers down to the shielding metal plane under the even-mode ground paths as shown in Fig. 7. However, this modification had large impact on even-mode line parameters, as was expected (even-mode return current paths are defined to the side-ground lines). In contrast, the even-mode slow-wave effect was increased significantly which had reduced the required coupler length. Simulation data showing these effects are also included in the Table II.

Due to the technology specific design rules constrain, the required coupling coefficient cannot be achieved with the straight forward CS-CPW structure shown in Fig. 7. Therefore, we had applied an engineering approach to obtain the design goal by introducing a special structure. While approaching to this particular structure, two things were considered. First, it was obvious that there were little room left to tune the even-mode line parameters, and second, the odd-mode slow-wave effect was less. Hence, we had modified the top most metal layer of the signal lines and created slabs for gaining strong capacitive coupling in odd-mode propagation as shown in Fig. 8. The new CS-CPW structure had provided high odd-mode effective dielectric constant and significantly lowered the odd-mode impedance while the changes in even-mode parameters were less prominent. The odd- and even-mode line parameters for this modified structure are also included in the Table II. The structure was built such a way that the CS-CPW remains scalable. We had considered the structure block as a unit cell so that the complete coupler structure can be made by repeating

special\_structure2.png

Fig. 8. Top view of CS-CPW structure when the signal lines are modified in odd-mode signal propagation. The unit cell is repeated  $N$  times to cover the complete coupler length. The slow-wave grids are not shown here to avoid any ambiguity in the figure.

the number of required unit cells. In order to validate the scalability of the modified structure, we have simulated the structure with different number of unit cells and observe the effects on line parameters. It is understandable from Fig. 9 that the effect of repeating the unit cells on the line parameters are negligible hence the proposed structure is scalable.

Finally, the through port of the coupler was folded to its coupled port side for obtaining the in-phase and quadrature ports to the same side. The final geometric dimensions of the CS-CPW became  $W = 2.5 \mu\text{m}$ ,  $S = 1.5 \mu\text{m}$ ,  $D = 1.54 \mu\text{m}$  (Metal 3),  $SG = 19.75 \mu\text{m}$ ,  $GW = 5 \mu\text{m}$ . With these geometric dimensions the required metal density requirements needed for the circuit fabrication were fulfilled except for the metal 5 and 4. Therefore, extra metal plates on these two layers were placed and they had negligible effects on the line parameters. The final simulation structure in 3D view is shown in Fig. 9 and the corresponding complete coupler performances are shown in Fig. 10. From Fig. 10, it can be seen that the coupler has an insertion loss of -3.5 dB at the coupling and through ports, 18 dB of isolation, a less than  $2^\circ$  phase variations and a good matching over a wide bandwidth from 55 GHz to 110 GHz. The simulation results indicate that the coupler is critically-coupled even though the simulated coupling coefficient ( $k = 0.73$ ) should give over-coupled performance. However, the results are expected as the loss was compensated by the extra coupling.

## VII. MEASUREMENT RESULTS

The 3-dB quadrature coupler was fabricated in a 65-nm CMOS technology and the dimension is  $56 \text{ mm} \times 205 \text{ mm}$ . On-wafer  $S$ -parameter measurements were carried out for the

3D\_figure\_coupler\_v1.png

Fig. 9. 3D view of the final simulation structure for the coupler. The structure is  $110 \mu\text{m}$  long and includes four unit cells and the through port is folded to the coupled port side.

coupler\_simulation2.png

Fig. 10. Simulated  $S$ -parameters and phase difference between the ports for E- to W-band quadrature coupler based on CS-CPW structure.



micrograph\_v1.png

Fig. 11. Micrograph of the CS-CPW based coupler test structures for (a) through and (b) coupling measurements.

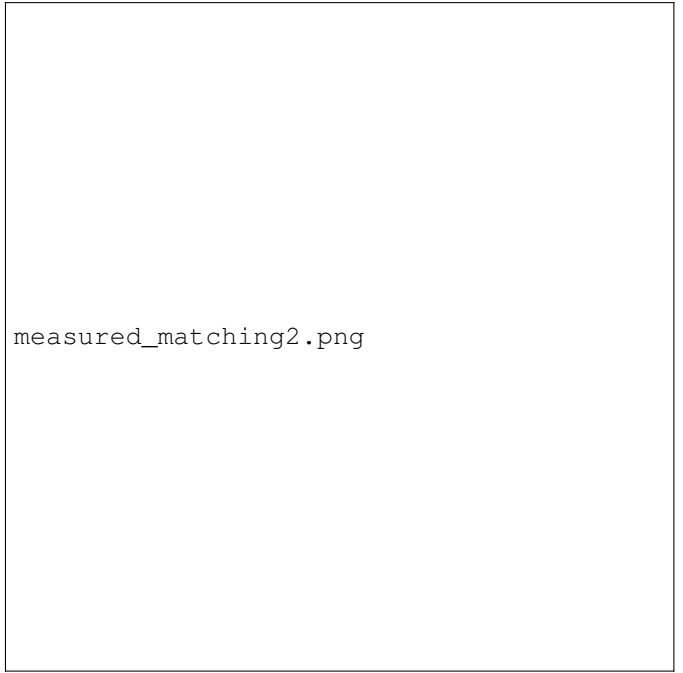


Measured\_deembed\_coupler2.png

Fig. 12. Measured (dotted) and simulated (solid) coupler performance.

coupler characterization. However, the coupled line coupler is a 4-port device and due to the unavailability of the 4-port VNA we had fabricated two exactly same coupler structures but with different port configurations for through and coupling measurement. For through measurement, the coupled and isolated ports were terminated to the on-chip  $50\ \Omega$  resistors and the input and through ports were connected to the probing pads. On the other hand, for coupling measurement the through and isolated port were terminated with  $50\ \Omega$  resistors and the input and coupled were connected to the probing pads. In order to facilitate the connection between a coupler port and a probing pad an extra piece of SW-CPW of length  $70\ \mu\text{m}$  was added. The effects of the pads and the lines were de-embedded from the measurements by pad-short-open [DEumic11] and L-2L [L.2L] procedure, respectively. The micrograph of the coupler test structures are shown in Fig. 11. Measured and simulated results for the coupler are shown in Fig. 12. Good correlation is observed between simulations and measurements. However, slight over-coupling is observed in the measurements and that is because of the realized odd- and even-mode impedances were different than the targeted. The measured coupler performance show an insertion loss of  $-3.5\ \text{dB}$  at the through port and  $-4.4\ \text{dB}$  at the coupled port at  $90\ \text{GHz}$ . A less than  $2^\circ$  phase variations,  $\pm 1\ \text{dB}$  amplitude error, and better than  $18\ \text{dB}$  return loss is measured over a wide bandwidth from  $55\ \text{GHz}$  to  $110\ \text{GHz}$ .

Furthermore, to see the coupler performance without the de-embedding we had matched the coupler and fabricated two more structures as for the other case. For the matching, we had modeled the pad and the SW-CPW through EM simulations, and terminated the isolated port with  $38\ \Omega$ . In this case, the length of the SW-CPW was  $100\ \mu\text{m}$ . The measured and



measured\_matching2.png

Fig. 13. Measured (dotted) and simulated (solid) performance of the matched coupler.

simulated matched coupler performances which include the pads and connecting transmission lines are shown in Fig. 13. Good agreement is again observed between measurements and simulations, especially the matching. However, the loss is bit higher than the simulation and this discrepancy may arise from the inaccurate pad modeling which is always difficult in mm-wave frequencies.



TABLE II  
EM SIMULATED ODD- AND EVEN-MODE LINE PARAMETERS @90 GHZ FOR DIFFERENT GEOMETRIC PARAMETERS

Simulation scenario	Design parameter					( $\Omega$ )	( $\Omega$ )					(dB/m)	(dB/m)	Complete coupler length ( $\mu$ m)
Estimated model from Table I	6	1.5	1.6	10	15	—	—	—	—	—	—	—	—	—
EM Simulated model	6	1.5	1.66	10	15	20.38	97	0.65	6	9.52	—	—	—	—
Mathematical model	3.53	1.5	2.04	4	11.8	20.7	121	0.71	—	—	—	—	—	—
EM Simulated model	3.5	1.5	2.04	4	12	23.4	130.5	0.69	5.1	6.3	1600	615	333	—
EM simulation iteration:1	4	1.5	2.04	4	15	22.9	133	0.7	5.5	5.56	1860	600	340	—
EM simulation iteration:2*	4	1.5	2.04	4	15	22.9	86.25	0.58	5.5	13.25	1860	670	275	—
EM simulation iteration:3*	4	1.5	2.04	4	20	22.9	91.68	0.6	5.5	14.8	1860	700	260	—
EM simulation iteration:4 <sup>†</sup>	4	1.5	2.04	4	20	12.7	93.45	0.76	23.8	15.35	4760	800	180	—
EM simulation iteration:5 <sup>†</sup>	2.5	1.5	2.04	4	20	15.76	109	0.75	18.6	13.1	4760	865	200	—
EM simulation iteration:6 <sup>†</sup>	2.5	1.5	1.57	4	20	16.73	105.7	0.73	17.5	13.6	4600	1240	205	—

TABLE III  
STATE-OF- THE-ART PERFORMANCE OF MM-WAVE 3-DB QUADRATURE COUPLERS ON SILICON TECHNOLOGY

Ref	Topology	Technology	Frequency (GHz)	Insertion loss (dB)	Coupling (dB)	Return loss (dB)	$\pm 1$ dB BW (GHz)	$\pm 5$ BW (GHz)	Size(mm <sup>2</sup> )
<b>This work</b>	<b>CP-SW</b>	<b>65-nm CMOS</b>	<b>90</b>	<b>3.5</b>	<b>4.4</b>	<b>&gt; 18</b>	<b>55</b>	<b>&gt; 60</b>	<b>0.0115</b>
[Floyd.IMS]	Lange coupler	130-nm BiCMOS	60	4	5	20	—	21*	0.048
[Floyd.IMS]	Lange coupler/meandering	130-nm BiCMOS	60	4	5	15	—	19*	0.0192
[D.Titz]	MBLC	130-nm BiCMOS	70	—	5.4	8.5	5	9	0.239
[D.Titz]	QLQC	130-nm BiCMOS	62	—	4.1	12.7	4	10.5	0.029
[EC.CPW]	EC-CPW	90-nm CMOS	62	4.8	5.5	22	$\sim 6.5$	$\sim 9$	0.102
[Ferrari.Thesis]	CP-SW	55-nm BiCMOS	60	3.3 <sup>+</sup>	3.5 <sup>+</sup>	29 <sup>+</sup>	—	—	.069

TABLE I  
MAPPING TABLE TO OBSERVE THE VARIATION OF , , , , AND VS. GEOMETRICAL PARAMETER CHANGES

Geometrical parameter	Output					
	??	??	??	??	??	??
W $\uparrow$	$\downarrow$	$\downarrow$	$\uparrow$	$\uparrow$	$\downarrow$	$\downarrow$
S $\uparrow$	$\downarrow$	$\uparrow$	$\uparrow$	$\downarrow$	$\downarrow$	$\uparrow$
D $\uparrow$	$\uparrow$	$\downarrow$	$\downarrow$	$\downarrow$	$\uparrow$	$\uparrow$
SG $\uparrow$	$\uparrow$	$\downarrow$	$\downarrow$	$\downarrow$	$\uparrow$	$\downarrow$
GA $\uparrow$	$\downarrow$	$\uparrow$	$\uparrow$	$\uparrow$	$\downarrow$	$\downarrow$

## VIII. CONCLUSION

An algorithmic design methodology was developed for mm-wave CS-CPW based silicon couplers. Besides, an experiment on the behavior of the CS-CPW structure had performed and a design map has been created for improving the design efficiency. Based on the proposed design algorithm and the design map, a 3-dB quadrature coupler covering the whole E- to W-band has been successfully demonstrated in a 65-nm CMOS technology. The state-of-the-art results published for the 3-dB quadrature couplers realized on monolithic silicon technologies are shown in Table III. The presented results show excellent wideband coupler performances. A size reduction of about 50% compared to the conventional microstrip based quadrature couplers is achieved. Moreover, the good agreement between the simulation and measurement results indicates the validity of the applied modeling technique. The proposed CS-CPW based coupler is well suited for the design of compact mm-wave silicon radio front-ends.

## APPENDIX A

### PROOF OF THE FIRST ZONKLAR EQUATION

Appendix one text goes here.

## APPENDIX B

Appendix two text goes here.

## ACKNOWLEDGMENT

The authors would like to thank...

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