Short Notes

Computation of the Fast Walsh-Fourier Transform

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Abstract—The discrete, orthogonal Walsh functions can be generated by a multiplicative iteration equation. Using this iteration equation, an efficient Walsh transform computation algorithm is derived which is analogous to the Cooley-Tukey algorithm for the complex-exponential Fourier transform.

Index Terms—Algorithm, Cooley-Tukey, Hadamard-Fourier, orthogonal, transform, Walsh-Fourier.

OPTIMIZATION OF THE DISCRETE WALSH TRANSFORM

Several authors have treated the set of Walsh functions and the Walsh-Fourier transform [1]-[4], [7]. We shall discuss here the set of discrete, orthogonal Walsh functions and the derivation of a Walsh transform algorithm which is analogous to the Cooley-Tukey algorithm [5] for the complex-exponential Fourier transform.¹

The discrete Walsh functions are sampled versions of the continuous set. As discussed here, the discrete functions are assumed to be infinite in extent, and are periodic with period M, where M is an integral power of two. Thus a complete orthogonal set will have M distinct functions. We shall designate these functions wal (m, n). The complete set is represented over the range $m = 0, 1, 2, \cdots, M-1$ and $n = 0, 1, 2, \cdots, M-1$.

The first two discrete Walsh functions are defined as

wal
$$(0, n) = 1$$
 for $n = 0, 1, 2, \dots, M - 1$ wal $(1, n)$

The remainder of the set can be generated by an iterative equation. Various iteration equations have been used to generate the Walsh functions, but for our development it is convenient to introduce the multiplicative iterative equation (3):

$$wal(m, n) = wal([m/2], 2n) \cdot wal(m - 2[m/2], n),$$
 (3)

where [m/2] indicates the integer part of m/2. It is

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¹ At EASTCON, 1968, and at the IEEE Workshop of FFT Processing, October 6–8, 1968, J. E. Whelchel [9] presented the matrix derivation of the fast Fourier-Hadamard transform. The Hadamard [8], [10] and the discrete Walsh transforms are identical with the possible exception of a permutation in the order of functions.

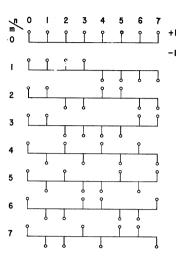


Fig. 1. The eight discrete Walsh functions of length 8.

shown in the Appendix that (1), (2), and (3) generate an orthogonal set.

Fig. 1 shows the eight discrete Walsh functions of length M=8 as generated by (1), (2), and (3). Note that the discrete Walsh functions as defined here are symmetric with respect to the argument (m, n). That is,

wal
$$(m, n) = \text{wal } (n, m)$$
.

Given an M-length real array f(n), we can define the Walsh transform as

$$F(m) = \sum_{n=0}^{M-1} f(n) \text{ wal } (m, n),$$

$$m = 0, 1, 2, \dots, M-1.$$
(4)

Similarly, the inverse transform is

$$f(n) = \frac{1}{M} \sum_{m=0}^{M-1} F(m) \text{ wal } (n, m),$$

$$n = 0, 1, 2, \dots, M-1.$$
(5)

Since the Walsh functions can have values of +1 and -1 only, computation of (4) or (5) requires no multiplications. Furthermore, using (3) we can derive a computation algorithm analogous to the Cooley-Tukey algorithm. This algorithm will require $M \log_2 M$ summations to compute a complete Walsh transform rather than M^2 as indicated by (4).

The derivation of the algorithm parallels the one given by Cooley and Tukey. Rather than present the general proof, we shall outline the derivation for the special case M=8. Anyone familiar with the derivation of the Cooley-Tukey algorithm should be able to extend the work presented here to the general case.

Let us replace the indices in (4) with a set which can have only values of 0 and 1. That is, let

$$m = 4j_2 + 2j_1 + j_0,$$
 $j_2, j_1, j_0 = 0$ or 1, (6)

$$n = 4k_2 + 2k_1 + k_0,$$
 $k_2, k_1, k_0 = 0$ or 1. (7)

Using these new indices, the notation wal (m, n) becomes

wal
$$(j_2, j_1, j_0; k_2, k_1, k_0)$$
.

Thus we rewrite (4) as

$$F(j_2, j_1, j_0) = \sum_{k_0=0}^{1} \sum_{k_1=0}^{1} \sum_{k_2=0}^{1} f(k_2, k_1, k_0)$$

$$\cdot \text{wal } (j_2, j_1, j_0; k_2, k_1, k_0).$$
(8)

Also, we can restate the iteration equation (3) using the new indices. Note that the numbers j_2 , j_1 , j_0 are essentially the digits of the binary representation of m. Therefore, dividing m by two and keeping the integer part is equivalent to shifting the binary representation of m one bit to the right and dropping the fractional bit. That is, if

$$m \leftrightarrow j_2 j_1 j_0$$

then

$$[m/2] \leftrightarrow 0j_2j_1$$
.

Similarly, multiplying n by two is equivalent to shifting the binary representation of n one bit to the left. If

$$n \leftrightarrow k_2 k_1 k_0$$

then

$$2n \leftrightarrow k_2 k_1 k_0 0$$
.

An 8-length Walsh function is periodic with period 8. Thus any index (such as 2n) can be evaluated modulo 8. This is equivalent to deleting any bits above the third bit and we have

$$2n \pmod{8} \leftrightarrow k_1 k_0 0.$$

Using these indices, (3) becomes

$$wal (j_2, j_1, j_0; k_2, k_1, k_0) = wal (0, j_2, j_1; k_1, k_0, 0) \cdot wal (0, 0, j_0; k_2, k_1, k_0).$$
(9)

Since j_0 can only be 0 or 1, wal $(0, 0, j_0; k_2, k_1, k_0)$ represents either wal (0, n) or wal (1, n). The function wal (0, n) is 1.0 for all n. The function wal (1, n) is 1.0 if $0 \le n < 4$ and wal (1, n) is -1.0 if $4 \le n \le 7$. Thus, from (7), wal (1, n) is +1.0 if $k_2 = 0$ and wal (1, n) is -1.0 if $k_2 = 1$. Therefore

wal
$$(0, 0, j_0; k_2, k_1, k_0) = (-1)^{j_0 k_2}$$
. (10)

We can use (9) to factor wal $(0, j_2, j_1; k_1, k_0, 0)$ to get

wal
$$(0, j_2, j_1; k_1, k_0, 0) = \text{wal } (0, 0, j_2; k_0, 0, 0)$$

·wal $(0, 0, j_1; k_1, k_0, 0)$. (11)

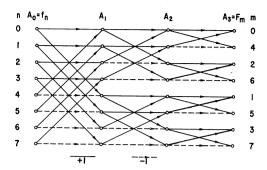


Fig. 2. Signal flow graph of 8-length discrete Walsh transforms. Multiplying factors are +1 and -1, as indicated by the solid and dotted branches, respectively.

Using (10) and (11) back in (9), we get the completely factored expression for the 8-length Walsh function

$$wal(j_2, j_1, j_0; k_2, k_1, k_0) = (-1)^{j_2k_0} (-1)^{j_1k_1} (-1)^{j_0k_2}.$$
 (12)

Using (12), (8) becomes

$$F(j_{2}, j_{1}, j_{0}) = \sum_{k_{0}=0}^{1} (-1)^{j_{2}k_{0}} \sum_{k_{1}=0}^{1} (-1)^{j_{1}k_{1}} \cdot \sum_{k_{0}=0}^{1} (-1)^{j_{0}k_{2}} f(k_{2}, k_{1}, k_{0}).$$

$$(13)$$

We can define

$$A_1(j_0, k_1, k_0) = \sum_{k_2=0}^{1} (-1)^{j_0 k_2} f(k_2, k_1, k_0).$$
 (14)

This A_1 array becomes the first result in a set of calculations to compute F(m). From A_1 we can compute an $A_2(j_0, j_1, k_0)$ array and so forth until the last sum (over k_0) has been evaluated. The final result A_3 (j_0, j_1, j_2) will contain all the coefficients of the 8-length Walsh transform. However, just as in the Cooley-Tukey algorithm, the order of the values will be in bit-reversed form.

Fig. 2 shows the signal flow graph for the 8-length Walsh transform. Its similarity to the Cooley-Tukey algorithm is obvious [6]. In fact, one can convert some Cooley-Tukey transform programs to the Walsh transform by setting all the trig values to 1.0, or by removing the steps which multiply the array values by the trig values. Also, the complex part of the Cooley-Tukey operations can be removed since only the Walsh transform is real. Since wal (n, m) = wal (m, n), the inverse Walsh transform is identical to the Walsh transform except that all the values are divided by M.

For the general case when $M=2^p$, the intermediate Walsh transform arrays are defined by

$$A_{l}(j_{0}, j_{1}, \cdots, j_{l-1}, k_{p-l-1}, \cdots, k_{0})$$

$$= \sum_{k_{p-l-0}}^{1} A_{l-1}(j_{0}, j_{1}, \cdots, j_{l-2}, k_{p-l}, k_{p-l-1}, \cdots, k_{0})$$

$$\cdot (-1)^{j_{l-1}k_{p-l}}, \qquad (15)$$

where $l = 1, 2, \dots, p$, and

$$A_0(k_{p-1}, k_{p-2}, \cdots, k_0) = f(k_{p-1}, k_{p-2}, \cdots, k_0).$$
 (16)

The general equation for the $M = 2^{p}$ -length discrete Walsh function is

wal
$$(j_{p-1}, j_{p-2}, \dots, j_0; k_{p-1}, k_{p-2}, \dots, k_0)$$

= $\prod_{i=0}^{p-1} (-1)^{j_{p-1-i}k_i}$. (17)

From (15) we can build a program for the general Mlength discrete Walsh transform. With (17) we can evaluate a Walsh function for any m and n. Also, (17) can be used to prove the orthogonality (see Appendix) and the symmetry of the set of discrete Walsh functions.

APPENDIX

The orthogonality of the discrete Walsh functions as generated by (1), (2), and (3) can be proved by using the general Walsh function (17). Equation (17) is derived from the iterative equation (3) and the definitions (1) and (2). The dot product of any two Mlength Walsh functions is as follows.

$$\phi(r_{p-1}, r_{p-2}, \cdots, r_0; j_{p-1}, j_{p-2}, \cdots, j_0)$$

$$= \sum_{k_{p-1}} \sum_{k_{p-2}} \cdots \sum_{k_0=0}^{1}$$
(18)

wal
$$(r_{p-1}, \dots, r_0; k_{p-1}, \dots, k_0)$$

·wal
$$(j_{p-1}, \dots, j_0; k_{p-1}, \dots, k_0)$$
 (19)

$$= \sum_{k_{p-1}} \sum_{k_{p-2}} \cdots \sum_{k_0=0}^{1} \prod_{i=0}^{p-1} \left\{ (-1)^{r_{p-1-i}k_i} (-1)^{j_{p-1-i}k_i} \right\}$$
 (20)

$$= \sum_{k_{p-1}} \sum_{k_{p-2}} \cdots \sum_{k_{0}=0}^{1} \prod_{i=0}^{p-1} (-1)^{k_{i}(r_{p-1-i}+j_{p-1-i})}$$
 (21)

$$= \prod_{i=0}^{p-1} \sum_{k_i=0}^{1} (-1)^{k_i(r_{p-1-2}+j_{p-1-i})}$$
 (22)

$$= \coprod_{i=0}^{p-1} \left\{ 1 + (-1)^{(r_{p-1-i}+j_{p-1-i})} \right\}.$$
 (23)

Suppose each $r_k = j_k = 0$ or 1. Then (23) becomes

$$\phi = \prod_{i=0}^{p-1} 2 = 2^p = M.$$

Suppose at least one $r_k \neq j_k$. Then at least one term in the product in (23) is 0 and $\phi = 0$. In terms of the decimal indices, this means that

 $\phi(m, l)$

$$= \sum_{n=0}^{M-1} \operatorname{wal}(m, n) \cdot \operatorname{wal}(l, n) = M \quad \text{for } m = l$$

$$= 0 \quad \text{for } m \neq l$$
(24)

Thus (1), (2), and (3) generate an orthogonal set.

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Threshold Logic Design of Pulse-Type Sequential Networks

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Abstract-A modification of the conventional design method for pulse-type sequential logic is developed in which threshold gates are used in all of the combinational portions of the circuit. The canonical configuration, memory element employed, tabular design aids, etc., found in the conventional method were changed as necessary in order to use more effectively the unique properties possessed by threshold elements. A theorem providing insight for threshold gate realization is given and a classification of sequential functions is discussed in order to facilitate the understanding and use of the new process. Finally, examples are presented showing a significant advantage of the present method in both gate and transistor economy over conventional design.

Index Terms-Logical design, realizability, sequence classification, sequential networks, switching circuit synthesis, threshold logic.

I. Introduction

Results of extensive research have been reported concerning the application of threshold logic principles in the design of combinatorial logic circuits [1]-[5]. In these papers, attention has been focused on general properties of threshold functions, on techniques for determining whether a given Boolean function is realizable by means of a single gate, and on various applications such as the development of learning

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