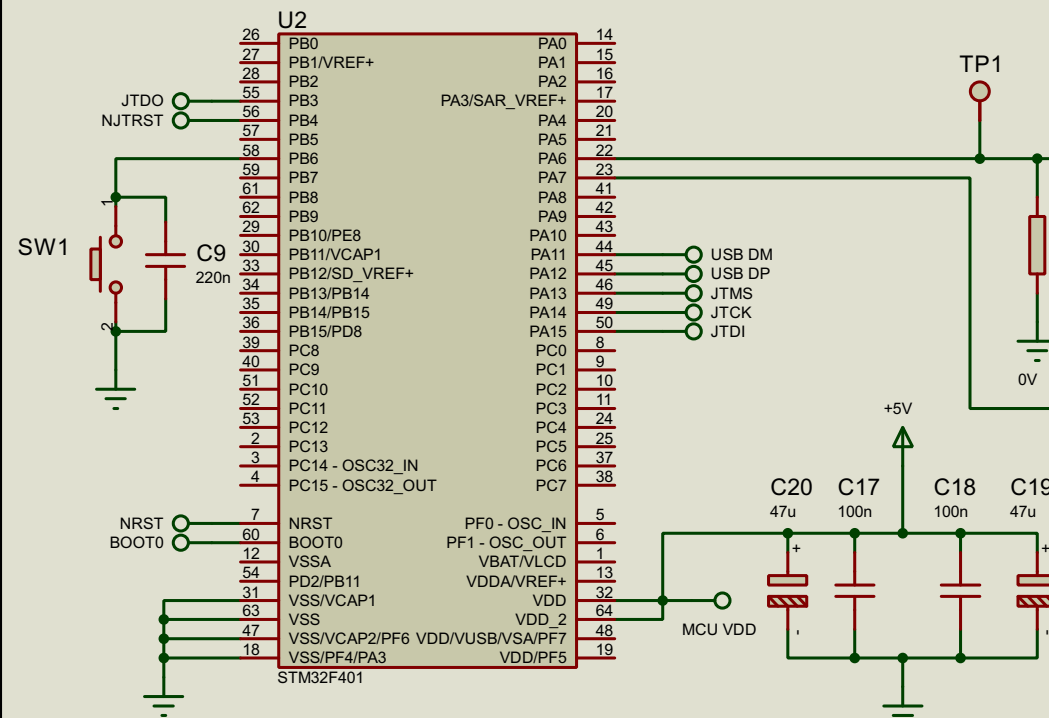


OUTPUT 3 - FBIS 5kHz

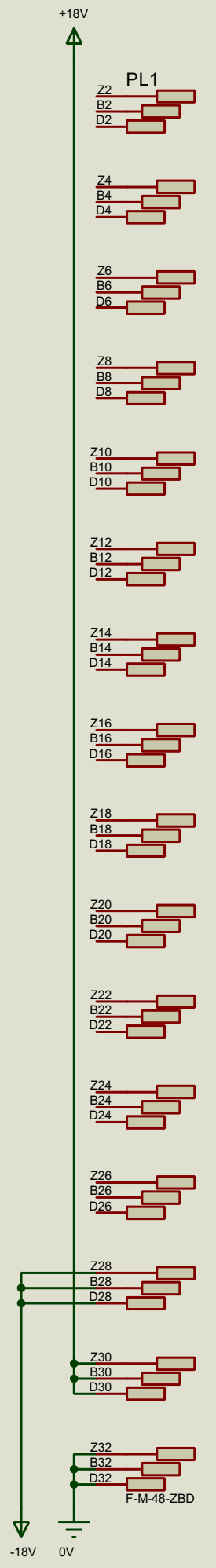
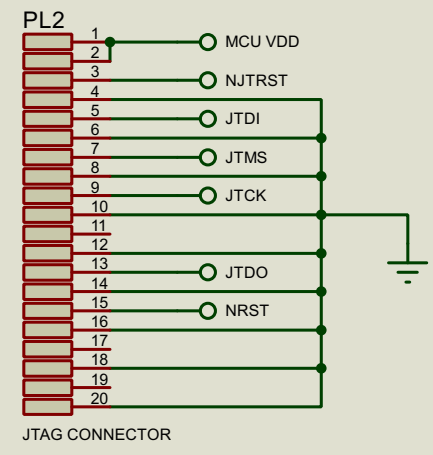
The circuit diagram for Output 3 - FBIS 5kHz shows a 555 timer (U3) configured as an astable multivibrator. The VCC pin (8) is connected to +5V through a 6.8k resistor (R10). The DC pin (7) is connected to +5V through a 10k resistor (R9). The TR pin (2) is connected to GND through a 10nF capacitor (C15). The TH pin (6) is connected to +5V through a 10nF capacitor (C14). The Q pin (3) is connected to GND through a 100nF capacitor (C21) and to a switch (SW2). The Q pin (3) is also connected to a test point (TP3) and a 10k resistor (R7) to GND. The Q pin (3) is connected to the base of a BS170 MOSFET (Q3). The gate of Q3 is connected to +5V through a 56R resistor (R3). The drain of Q3 is connected to +5V through a 100nF capacitor (C3) and to an LED (LED3) through a 2/6/7 resistor (TX3). The source of Q3 is connected to GND. The circuit is powered by a +5V supply and grounded at 0V.

PUT 4 - MANUAL



<div>CCFE</div> <div>CULHAM CENTRE</div> <div>FUSION ENERGY</div> <div>FOR</div>										
				P		Original	C. Meadowcroft			
				Iss	Date	Status / Modification	Drawn	Checked	Approved	Mod No
<div>@DOCNO</div>				ISSUE	<div>TITLE</div> <div>G1 Timing Card</div> <div>Main</div>					
Quality Class:				Sheet 1 of 2						

C:\Users\cmeadowc\OneDrive - UKAEA\Projects\PCBs\G1 Timing Card\Rev C\G1 Timing card.pdsprj



CCFE CULHAM CENTRE FOR FUSION ENERGY		P		Original		C. Meadowcroft			
		Iss	Date	Status / Modification		Drawn	Checked	Approved	Mod No
@DOCNO		ISSUE C		TITLE G1 Timing Card Connectors					
Quality Class:	Sheet 2 of 2	A3							