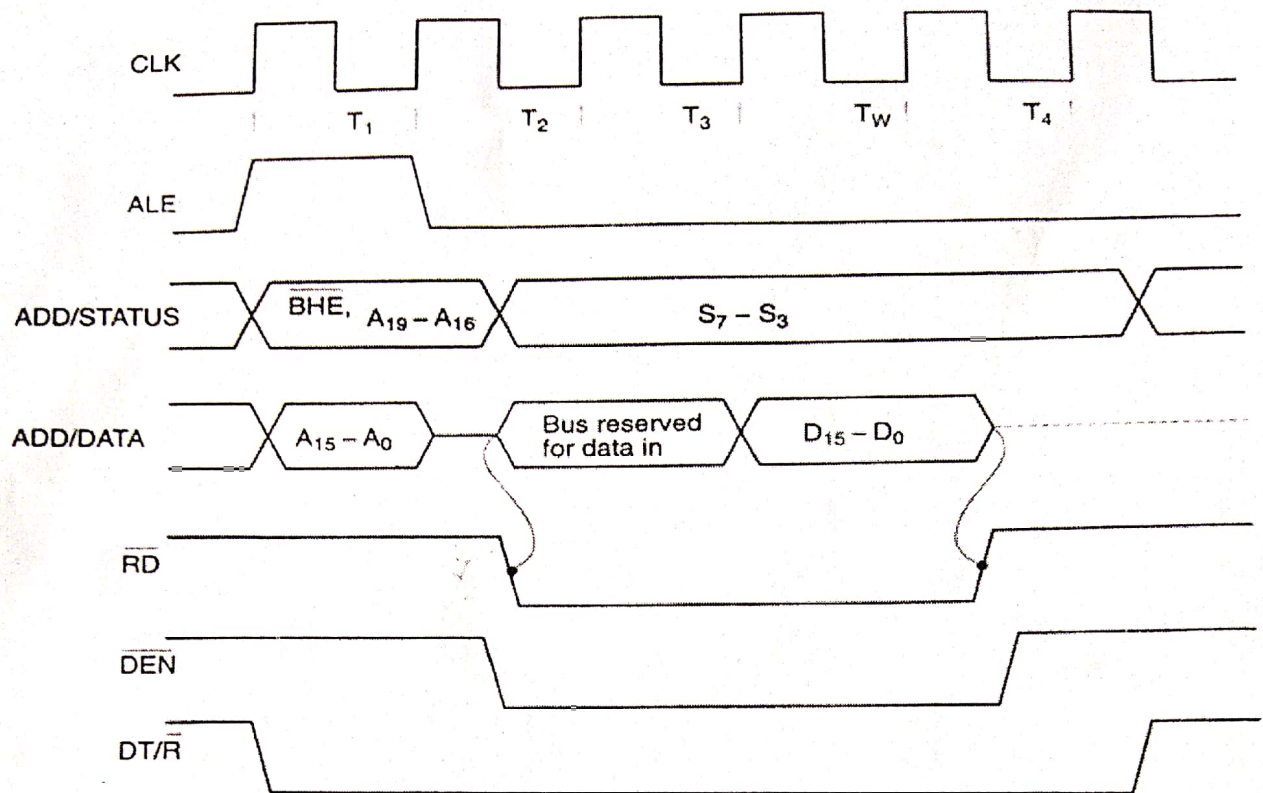


Timing Diagram Read Memory Cycle



Timing Diagram

Write Memory Cycle

