ECE 3300L Digital Design Ckt Verolig
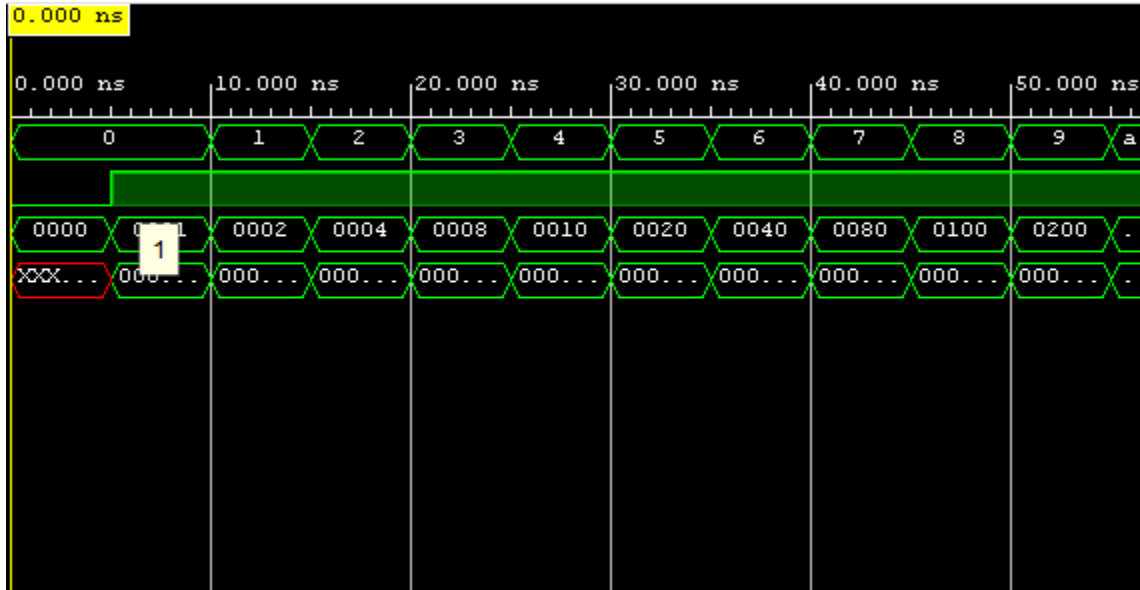
Experiment #2
Seven Segment Display

Section #1

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Abstract

For this experiment I built a system based in the Verilog language. The project programs the board to use 5 switches, one on/off switch and 4 switches to represent a binary counter. For the program a decoder (4x16) is implemented and the testbench for said code is also printed.

Testbench:

Video Link:
https://youtube.com/shorts/y2vKZZfsuFU?si=JUY4c6wyy7ehZa8b