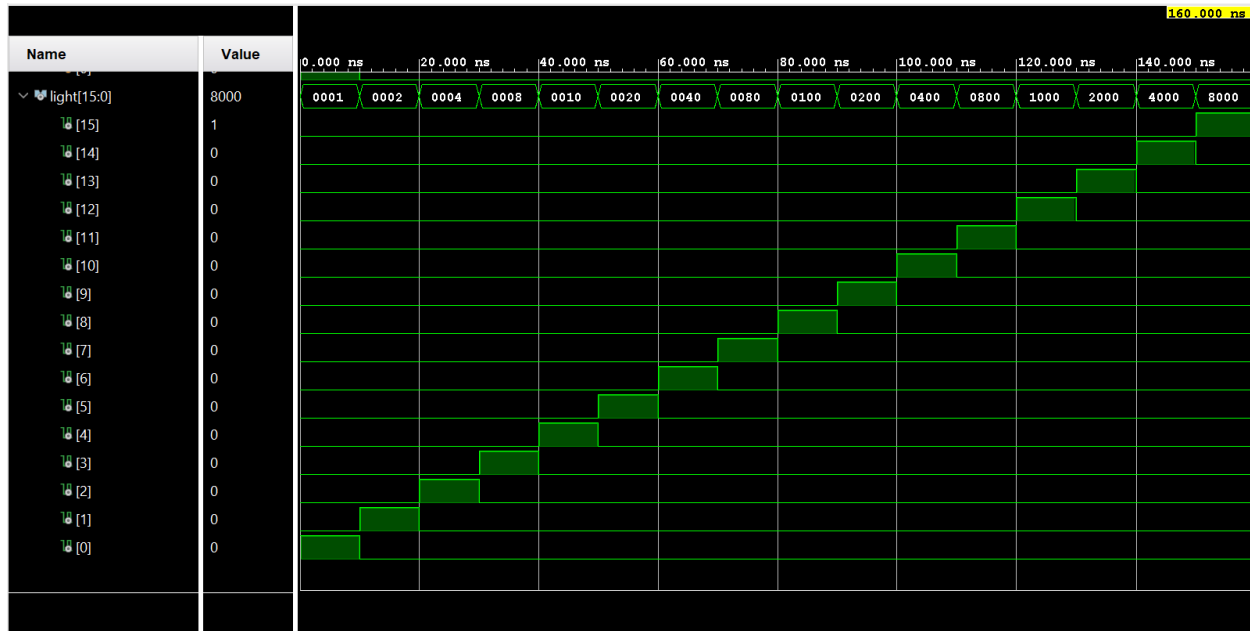
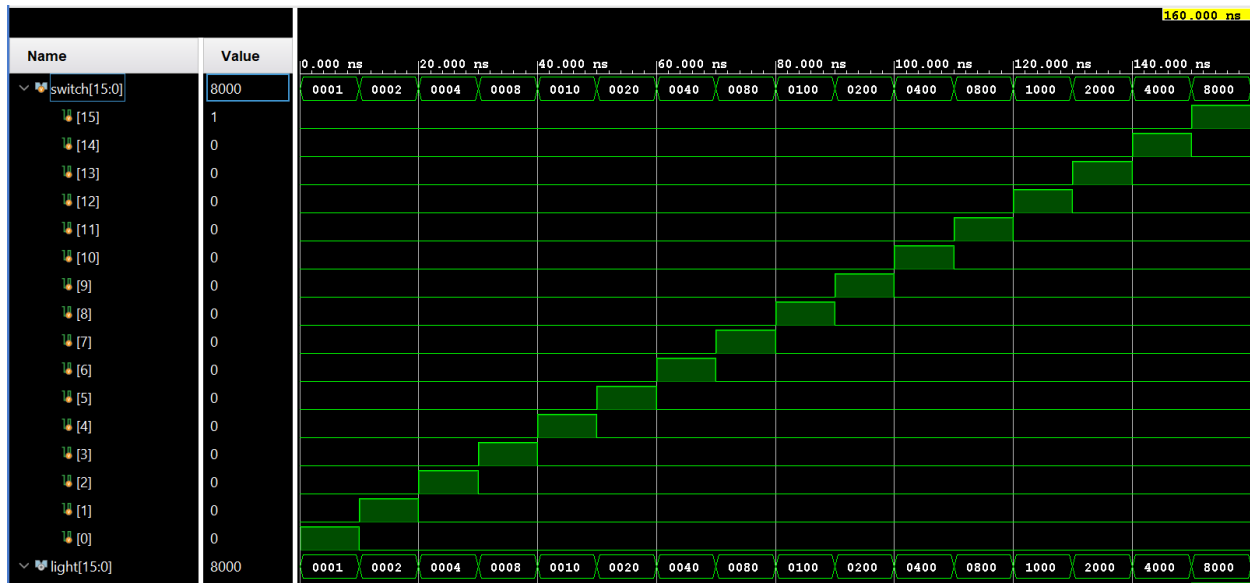


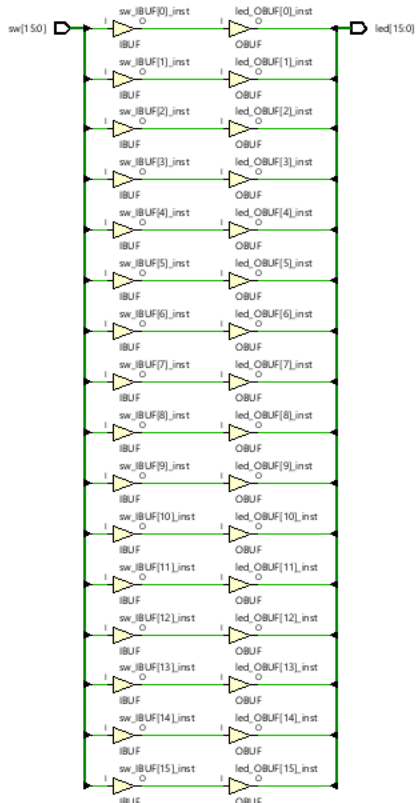
Nathaniel Garcia and Mikael Parsemyan

ECE 3300L

Group G

Lab 1:





Video Link: <https://www.youtube.com/watch?v=ntfzV6Z2OiY>

Reflection:

In this lab, we learned how to utilize Verilog to code our Artix-7 board and make the LEDs on the board correspond with their switches. We learned the importance of the constraints of the xdc code to make sure that the program aligns with the hardware that is being utilized. Then make sure that the hardware is properly connected for the code to be downloaded into the board. It is also important to synthesize the program to put all the pieces together but before synthesizing you should simulate to properly review whether the program functions correctly. One of the most important parts is to make sure the names in the Verilog code correspond with what is placed on the xdc constraint.