ECE 3300L Digital Design Ckt Verolig
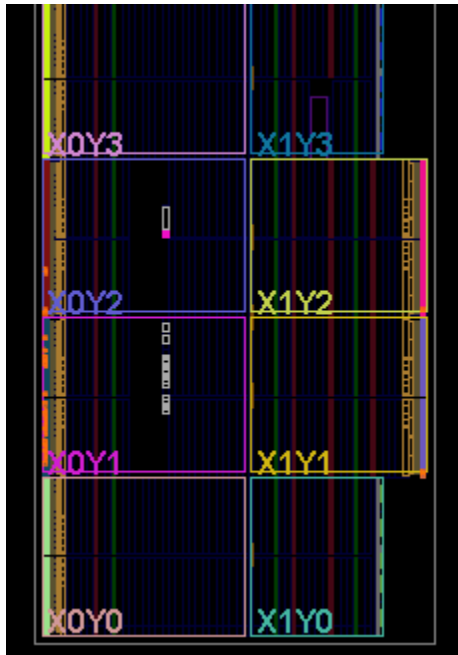

Experiment #1
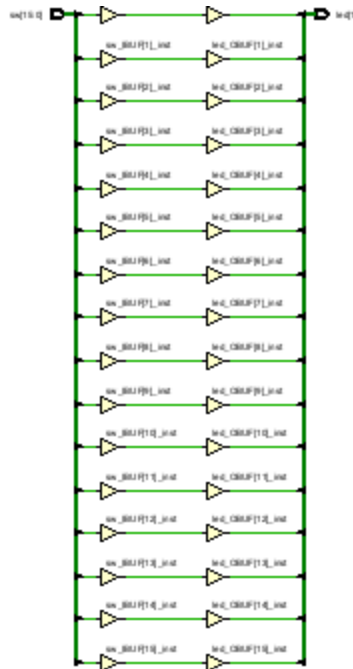## Switch ⇔ LED Interface

Section #1

By: Malachai Emery

Abstract

For this experiment I familiarized myself with the Nexys A7-100T board and set the switches to the corresponding LEDs. This allowed me to learn about programming the board and mapping I/O. To accomplish this I wrote a verilog module and assigned the pins with a .xdc file. The lab was very easy once you are able to understand Vivado.

Synthesis Device:

Synthesis Schematic:

Video Link:
 https://youtube.com/shorts/wuA7oFuzNaI?si=NR8C9TlmQxy6TTeJ