

Multi-qubit gates

With only single-qubit gates, we could never produce **entangled** states like $| \Psi \rangle = \frac{1}{\sqrt{2}} (| 00 \rangle + | 11 \rangle)$ since $(U \otimes V)| 0 \rangle \otimes | 0 \rangle = U| 0 \rangle \otimes V| 0 \rangle$

Multi-qubit gates are needed to generate entanglement and access the full power of QC

Recall:

A state of n bits is a vector in \mathbb{C}^{2^n}

An n -qubit gate is thus a unitary $U: \mathbb{C}^{2^n} \rightarrow \mathbb{C}^{2^n}$

Ex. Important multi-qubit unitaries include

$$CNOT = \begin{array}{c} \text{---} \\ \text{---} \end{array} = \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 \end{bmatrix}$$

Intuitive view
($x, y, z \in \mathbb{R}$)

$$CZ = \begin{array}{c} \text{---} \\ \text{---} \end{array} = \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & -1 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & -1 \end{bmatrix}$$

$$\begin{aligned} CNOT| x \rangle | y \rangle &= | x \rangle | xy \rangle \\ CZ| x \rangle | y \rangle &= (-1)^{xy} | x \rangle | y \rangle \end{aligned}$$

$$SWAP = \begin{array}{c} \text{---} \\ \text{---} \end{array} = \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \end{bmatrix}$$

(also written $\begin{array}{c} \text{---} \\ \text{---} \end{array}$)

$$\begin{aligned} SWAP| x \rangle | y \rangle &= | y \rangle | x \rangle \\ TOFF| x \rangle | y \rangle | z \rangle &= | x \rangle | y \rangle | z \oplus xy \rangle \end{aligned}$$

$$Toffoli = \begin{array}{c} \text{---} \\ \text{---} \\ \text{---} \end{array} = \begin{bmatrix} 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 \\ 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 \end{bmatrix}$$

Controlled gates

CNOT, CZ, Toffoli are examples of **controlled** gates. Controlled gates apply a unitary U on the **target(s)** if and only if the computational basis state of the **control** is $|1\rangle$

Ex.

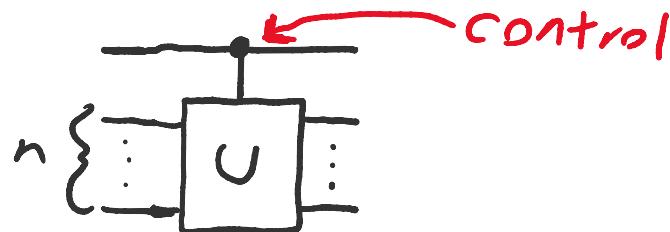
Given an n -qubit unitary U , the controlled- U gate is an $n+1$ -qubit gate that sends

$$|0\rangle|\psi\rangle \mapsto |0\rangle|\psi\rangle$$

$$|1\rangle|\psi\rangle \mapsto |1\rangle(U|\psi\rangle)$$

$$(\alpha|0\rangle + \beta|1\rangle)|\psi\rangle \mapsto \alpha|0\rangle|\psi\rangle + \beta|1\rangle(U|\psi\rangle)$$

The controlled- U gate is written



The CNOT gate is a **controlled-X** gate

CZ gate is a **controlled-Z** gate

Ex. The CNOT gate is entangling

$$\text{Let } |\Psi\rangle = |+\rangle|0\rangle = \frac{1}{\sqrt{2}}(|00\rangle + |10\rangle)$$

$$\begin{aligned} \text{Then } \text{CNOT}|\Psi\rangle &= \frac{1}{\sqrt{2}}(\text{CNOT}|00\rangle + \text{CNOT}|10\rangle) \\ &= \frac{1}{\sqrt{2}}(|00\rangle + |11\rangle) \\ &= |\Phi\rangle \end{aligned}$$

Matrix of a controlled gate

Given a unitary U , the controlled- U gate can be written as

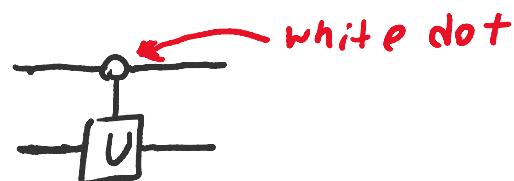
$$CU = |0\rangle\langle 0| \otimes I + |1\rangle\langle 1| \otimes U$$

Ex. // CNOT

$$\begin{aligned} CX &= |0\rangle\langle 0| \otimes I + |1\rangle\langle 1| \otimes X \\ &= \begin{bmatrix} 1 & 0 \\ 0 & 0 \end{bmatrix} \otimes I + \begin{bmatrix} 0 & 0 \\ 0 & 1 \end{bmatrix} \otimes X \\ &= \begin{bmatrix} I & 0 \\ 0 & 0 \end{bmatrix} + \begin{bmatrix} 0 & 0 \\ 0 & X \end{bmatrix} = \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 0 \end{bmatrix} \end{aligned}$$

(Negative controls)

We sometimes want to apply a gate if and only if the control is in the **0** state. we call this a **negative control** and denote it by.



Ex.

Negating the control on a CNOT flips the target bit if and only if the control is 0

$$\begin{array}{c} a \xrightarrow{\oplus} a \\ b \xrightarrow{\oplus} b \oplus (1 \oplus a) \end{array}$$

Fact

A quantum circuit diagram showing a controlled- U gate equivalent to a sequence of CNOT gates. The left side shows a control line with a white dot and a target line with a box labeled U . The right side shows two CNOT gates: one with control X and target X , and another with control U and target I .

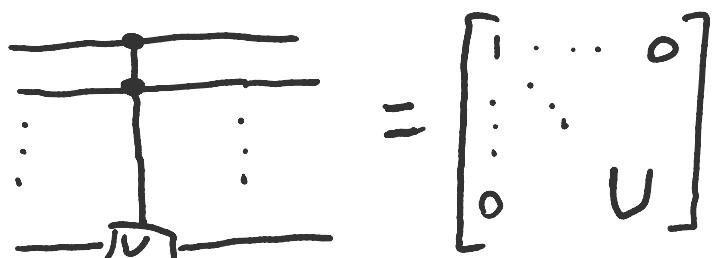
Multiply-controlled gates

Observe that the Toffoli gate is a controlled-CNOT gate:

$$\text{Toffoli}: |0\rangle|y\rangle|z\rangle \mapsto |0\rangle|y\rangle|z\rangle$$

$$|1\rangle|y\rangle|z\rangle \mapsto |1\rangle|y\rangle|z\rangle \otimes y = |1\rangle(\text{CNOT}|y\rangle|z\rangle)$$

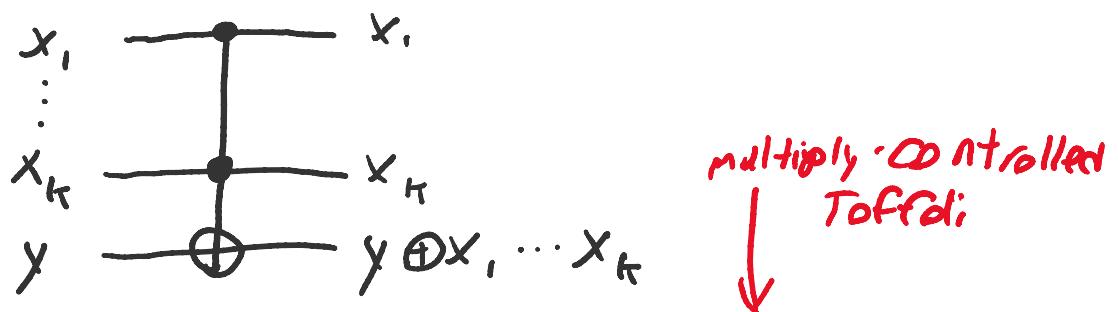
Controlled-controlled gates like the Toffoli gate are called multi-controlled gates. We draw each control with a dot:



$\curvearrowleft U$ is applied if and only if each control is 1

(Multiply-controlled Toffolis)

An important multiply-controlled gate is the multiply-controlled X gate:



Such a gate is often called an MCT gate and computes the product of its k controls

$$|x_1 \dots x_k\rangle|y\rangle \mapsto |x_1 \dots x_k\rangle|y \underset{\substack{\text{product} \\ \text{concatenation}}} \oplus (x_1 \dots x_k)\rangle$$

It's confusing,
I know

Construction of controlled gates

The construction of (multi-)controlled gates is another fundamental problem in Quantum compilation.

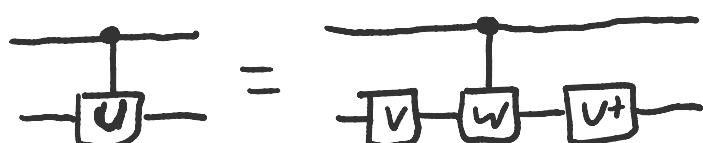
Barenco et.al

Elementary gates for quantum Computation lays the foundation for this problem

Our first goal is to show that multi-controlled gates may be implemented using only CNOT & single-qubit gates

(Basic Construction of controlled gates)

Observe that if $U = V^* W V$, then



In particular, the rhs sends

$$|0\rangle|\psi\rangle \mapsto |0\rangle(V^*V|\psi\rangle) = |0\rangle|\psi\rangle$$

$$|1\rangle|\psi\rangle \mapsto |0\rangle(V^*WV|\psi\rangle) = |0\rangle(V|\psi\rangle)$$

Ex.

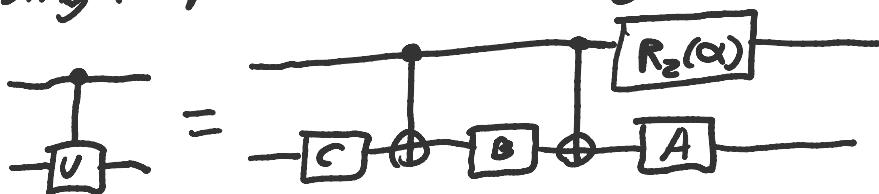
Recall that $X = HZH$. We can construct the controlled-X (CNOT) gate as



Universal 1-control gate construction

Prop.

For any 1-qubit unitary U , CU can be constructed from single-qubit and CNOT gates. Specifically,



For some α, A, B, C satisfying $ABC = I$ and $U = e^{i\alpha} A \otimes B \otimes C$

Pf.

Observe that the above circuit sends

$$\begin{aligned} |0\rangle|1\rangle &\mapsto (R_z(\alpha)|0\rangle) \otimes (ABC|1\rangle) = e^{-i\frac{\alpha}{2}}|0\rangle|1\rangle \\ |1\rangle|0\rangle &\mapsto (R_z(\alpha)|1\rangle) \otimes (A \otimes B \otimes C|0\rangle) = e^{i\frac{\alpha}{2}}|1\rangle|0\rangle \end{aligned}$$

∴ equal up to a global phase of $e^{i\frac{\alpha}{2}}$

Now let $U = e^{i\alpha} R_z(\beta) R_y(\gamma) R_z(\delta)$. Then

$$A = R_z(\beta) R_y(\gamma/2), \quad B = R_y(-\delta/2) R_z(-\frac{\beta+\delta}{2}), \quad C = R_z(\frac{\delta-\beta}{2})$$

satisfies the above constraints

Ex.

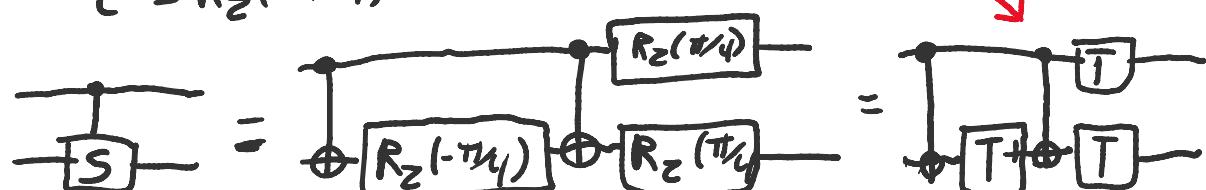
$$\begin{aligned} \text{Note that } S &= \begin{bmatrix} 0 & i \\ i & 0 \end{bmatrix} = e^{i\frac{\pi}{4}} \begin{bmatrix} e^{-i\frac{\pi}{4}} & 0 \\ 0 & e^{i\frac{\pi}{4}} \end{bmatrix} \\ &= e^{i\frac{\pi}{4}} R_z(0) R_y(0) R_z(\pi/2) \end{aligned}$$

$$\text{Setting } A = R_z(0) R_y(0)$$

$$B = R_y(0) R_z(-\pi/4)$$

$$C = R_z(\pi/4)$$

We get



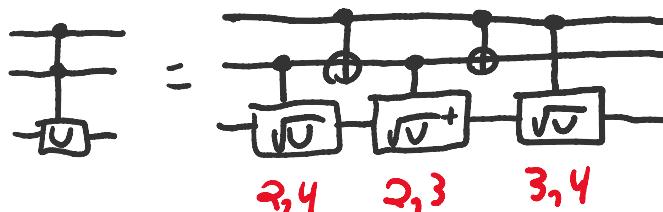
up to global phase $e^{i\pi/8}$
correct global phase

Construction of 2-control gates

Prop.

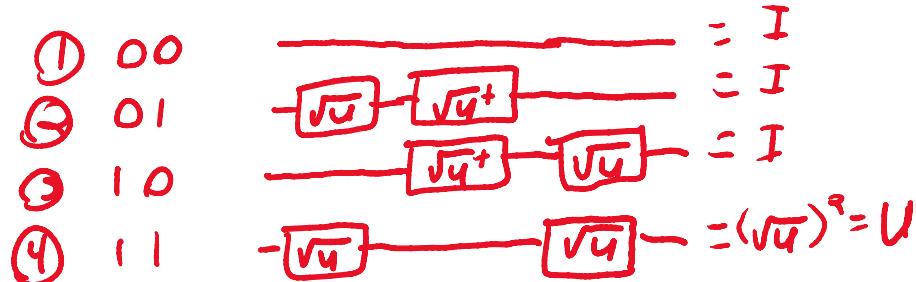
over SQ & CNOT

For any 1-qubit unitary V , CCV can be constructed as



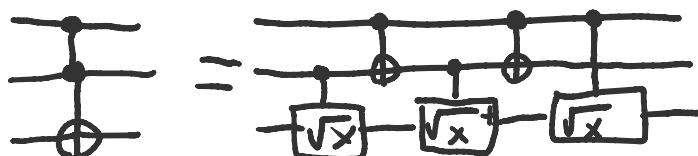
Pf.

We have 4 cases:

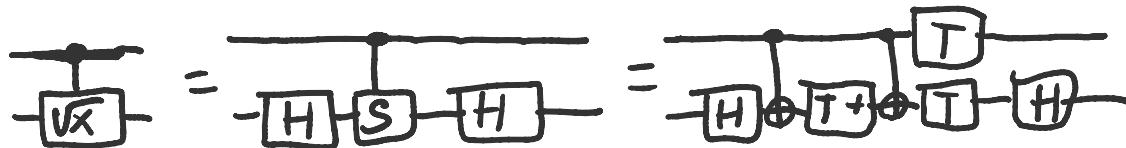


Ex.

Recall that the Toffoli gate is an X gate with two controls. In classical computing, the Toffoli gate **cannot be constructed (reversibly) by a circuit of < 3 bit gates**. Quantumly, by the above



Note also that $\sqrt{X} = S$ and $\sqrt{S} = T$, so



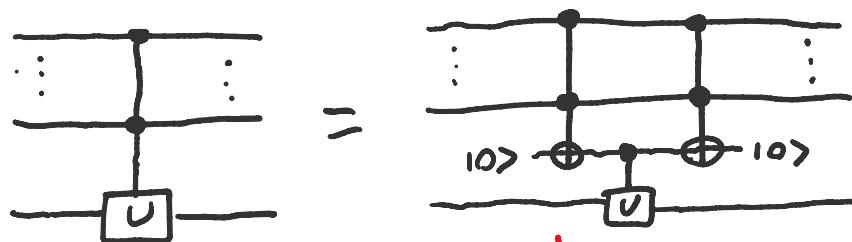
Thm. For the Toffoli gate,

- 5 + two-qubit gates is minimal
- 7 T-gates is minimal over $\{CNOT, H, T\}$

Construction of multi-controlled gates

Multiply-controlled gates are used in most **top-down** universal constructions (and often in algorithms). We now look at general techniques for decomposing into fewer controls.

(Decomposition with 1 ancilla)

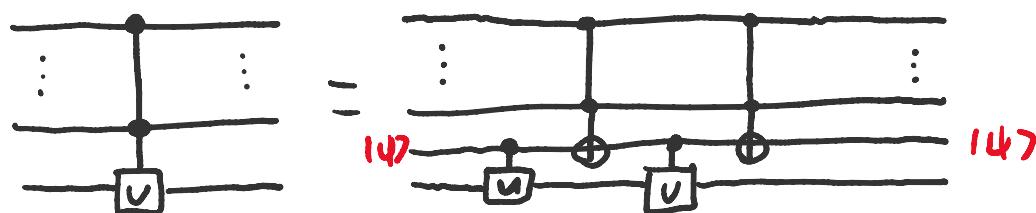


Note that $C^k U$ - k -controlled U "fires" if and only if all k -controls are 1.

$$x_1 = 1 \wedge x_2 = 1 \wedge \dots \wedge x_k = 1 \iff x_1 \cdot x_2 \cdots x_k = 1$$

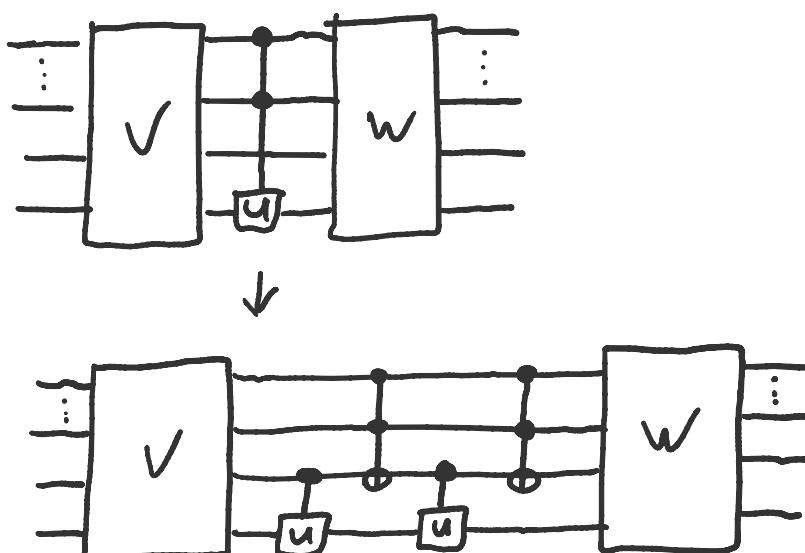
(Decomposition with 1 ancilla in an unknown state)

If $U^2 = I$, then



The ancilla here is called **dirty** and can in fact be a qubit used elsewhere in the circuit!

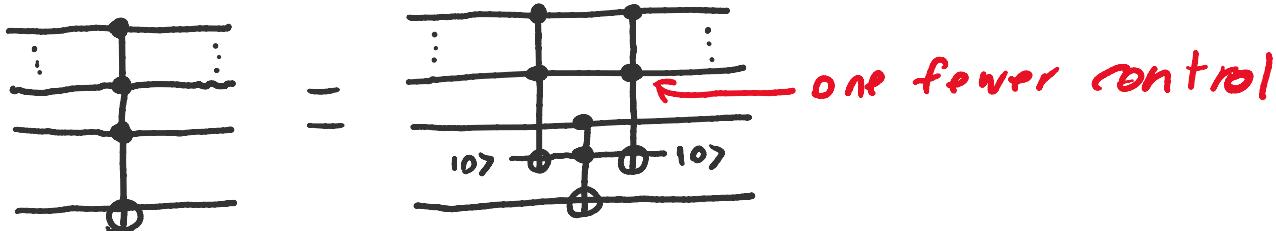
E.g.



Multi-controlled Toffoli gates

The previous constructions didn't reduce the number of controls, just shifted them to multi-controlled Toffoli (**MCT**) gates. To get things down to single-qubit and CNOT we need a construction which reduces controls

(**MCT** with linear clean ancillas)



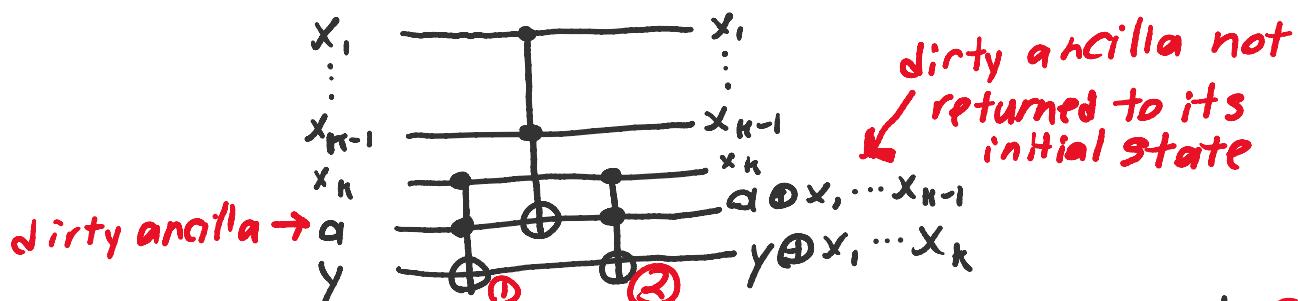
Prop.

A k -control Toffoli can be implemented with

- $k-2$ **Clean** ancillas
- $2(k-2) + 1$ Toffoli gates

(**MCT** with linear dirty ancillas)

A similar result holds for **dirty** ancillas, but its construction is slightly trickier. First observe:

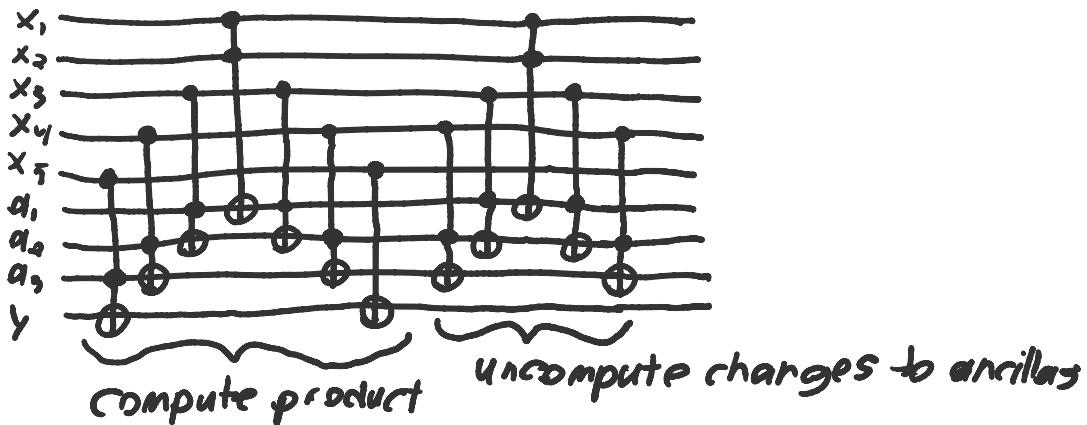


This is because at ① we have $y \oplus ax_k$, so at ②

$$y \oplus ax_k \oplus (a \oplus x_1 \cdots x_{k-1})x_k = y \oplus x_1 \cdots x_k$$

We can recursively apply this construction, giving $k-2$ dirty ancillas and $2(k-2) + 1$ Toffoli gates, but this will leave the $k-2$ dirty ancillas in an **unclean** state

To return the ancillas to their initial state, we need to **clean** them by reversing the intermediate $k-1$ control Toffoli computation. The circuit for 5 controls is



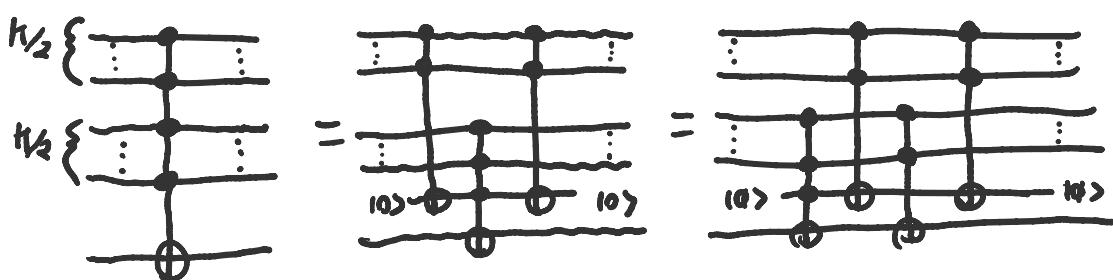
Prop.

A k -control MCT gate can be implemented with

- $k-2$ dirty ancillas
- $\alpha(k-2)+1 + \alpha(k-3)+1$ Toffoli gates

(MCT gate with a single ancilla)

Finally we show that an MCT gate may be implemented with just 1 ancilla (dirty or clean)



This decomposes a k -control MCT into $k/2$ and $k/2+1$ control MCT gates. We can then use the other $k/2-1$ bits as dirty ancillas to apply the previous linear simulation!

Prop. A k -control MCT gate can be implemented with

- 1 ancilla (dirty or clean)
- $O(n)$ Toffoli gates

A note about efficiency

In quantum computing, the constants matter. Physically realizable circuit depths double in **years**, and error-resistance means saving % on circuit depth can reduce time and space resources by **orders of magnitude**.

The Barenco et. al. single-ancilla construction was just recently beaten ^{* (Hines et al)} by Amy & Ross :)

With a single ancilla, the number of Toffoli gates to implement an MCT gate with the Barenco method scales roughly as

- $4(4^{k-2}) = 8k$ (dirty)
- $3(4^{k-2}) = 6k$ (clean)

Prep.

If X,Z basis changes (conjugation by Hadamard gates) are allowed, an MCT gate can be implemented with 1 ancilla and $\sim 4k$ CCX gates

