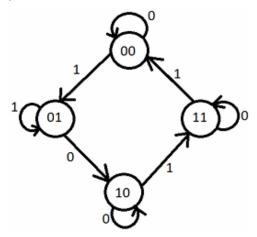
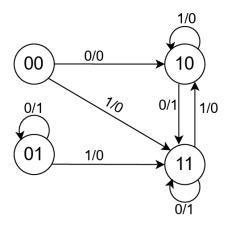
Assignment 4

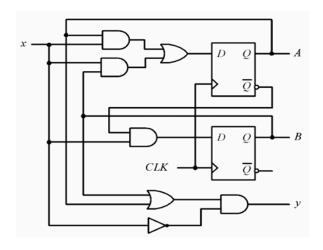
1. Given the state diagram as follows, get the sequential circuit using SR flipflop. (Ungraded)



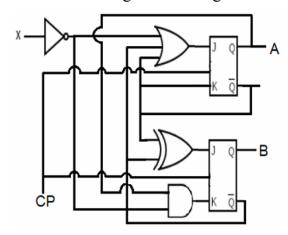
2. Given the state diagram as follows, get the sequential circuit using JK flipflop.



3. Draw the state diagram for the given circuit. (Ungraded)



4. Draw the state diagram for the given circuit.



- 5. Implement the following counter using T flip flop. (Ungraded)
 CSE110-> CSE111-> CSE220-> CSE221-> CSE331-> CSE221-> CSE321->
 CSE110
- 6. 3->4->6->10->12->13->15->3

Implement the given counter using JK flip-flop.

NB: For states not given in the question, please move to the initial state as per the question.

- 7. Implement 4 bit down counter using JK flip-flop
- 8. Implement the following counter using T FF:
 Green->Orange->Yellow->Red->Yellow->Orange->Yellow->Green
- 9. Implement the following counter using JK FF: (Ungraded) Green->Yellow->Red->Yellow->Green