

Question 1 [10 Points]

For the RISC-V assembly instructions below, what is the corresponding C/high level statement?

```
slli x30, x5, 3
add x30, x10, x30
slli x31, x6, 3
add x31, x11, x31
ld x5, 0(x30)
addi x12, x30, 16
ld x30, 0(x12)
add x30, x30, x5
sd x30, 0(x31)
```

Assume that the variables f, g, h, i, and j are assigned to registers x5, x6, x7, x28, and x29, respectively. Assume that the base address of the Arrays A and B are in registers x10 and x11, respectively.