```
1.x=x+y+2+0; Assume x,y,zanaqare stored
in registers X18-X21.
  Add X5, X18, X19 # X5 4x+Y
  Add ×5, ×5, ×20 # ×5 ←×+7+2
  A33 ×19, ×5, ×21 # ×19 < ×+4+2+2
                            Base addresses Of
2. int A[100], B[100];
   for(i=1; i<100; i++) {
                            A and B are in
       ACL] = ACL-1] + BCL];
                            X19, and X20. And L
                            15 in X21,
   Adde X5, X5,1 # X5 ←1
    Addi X6, X6, 100 # X6 ← 100
  100P: 10 X7,0(X19) # X7+A[1-1]
       1d x8, 8(x20) H x8 - BLi]
      Add Xq, X7, X8 # Xq + Aci-1]+Bci]
      Sd x9, 8 (X19) # A[L] + A[L-1]+B[L]
      Addix19, X19, 8 # For L+1 Of A
      Addi X20, X20, 8 # For L+1 Of B
      Addi x5, x5, 1 # L+ i+1
      bne X5,X6, loop # Compose with loop
      bea xo, xo, Exit
 Exit;
                      Assume that XIY and 2
      X=5Y+6Z;
3.
                      are in registers X19.
      Y=3x+7Y;
                      X20 and X21
      2 = 9x+11Y;
     SILIX5, X20, 2 # X5 - 4x7
     Add X5, X5, X20 # X5 + 54
```

```
SIIL X6, X21, 2 # X6 + 4*2
 Ada x6, x6, x21 # x6 4 5 * 2
                                 X=5Y+62
 Add x6, x6, x21 # x6 + 6 + 2
 Add x19, x19, x6 # X < 57+62
  SIIi \times 5, \times 19, 1 + \times 5 \leftarrow 2 \times x \leftarrow
 Add ×5, ×5, ×19 # ×5←3*×
 511L X6, X20, 3 # X6 + 8*Y
 SUB X6, X6, X20 # X6 4 7*Y
 A20, X20, X6 # Y4 3X+7Y2
 SIILX5, X19, 3 # X5 + 8*X
                                   Z=9xHIY
 Add X5, X5, X19 # X5 +9*X
 SIL X6, X20, 3 # X6 - 8*Y
 Add × 6, × 6, × 20 # × 6 ← 9 # Y
 499 x6/x6/x20 # x6 4 10*Y
 A22 X6, X6, X20 # X6 4 11 *Y
 429 x21, x21, x6 # 5 < 9x + 114
Encoding:
Add X5,X6,X7 - R TYPE
  XXXXXX | 00101 | 00110 | XXX | 00101 | XXXXXX
   57 Y62 Y61 J3 Yd OP
Addi X7, X6,10 - I TYPE
   0-01010 00110 XXX 00111 XXXXXX
    imm(12) x31 f3 xd OP(7)
```

