solve

**BRAC UNIVERSITY** 

**CSE 350** 

Quiz-3, Section 13

Fall 2024

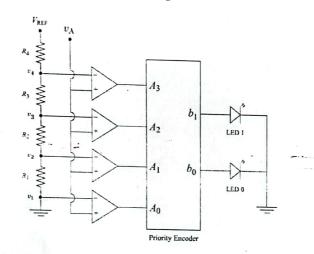
Marks: 20

Name:

ID:

1. The figure shows a 2-bit Flash ADC with VREF = 12V and its input ranged from 0V to 12 V.

Given: R1 = /2R, R2 = 3/2 R, R3 = 3/2 R, R4 = R/2. The two outputs of the ADC are connected to two LEDs. This circuit works as Active High Logic.



a. Determine the quantization range, corresponding digital output, and the states of the LED for the 2-bit Flash ADC. Make a table with quantization range, corresponding digital output, and the states of the LED.

[6]

b. Find the truth table of the Encoder and state the priority sequence of the Encoder.

[4]

$$I = \frac{12-0}{4R}$$

$$V_{1} = 0$$

$$V_{2} = V_{1} + I \times \frac{R}{2} = 0 + \frac{12}{4R} \times \frac{R}{2} = 1.5 \text{ V}$$

$$V_{3} = V_{2} + \frac{12}{4R} \times \frac{3R}{2} = 6 \text{ V}$$

$$V_{4} = V_{3} + \frac{12}{4R} \times \frac{3R}{2} = 10.5 \text{ V}$$

•	Range	6,60	L	LED LED	
/	0-1.5	00	र्भ	£	०२५
	1.5-6	01	to	J.	On
	6-10-5	10	07	ı	97
	10.5-12	11	on	0	m

1. 6)

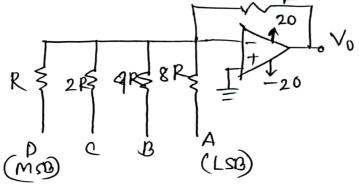
	A₃	A2	Αį	Aσ	1 6, 60	
	0	0	0	1	0 0	
	0	0	١	ı	01	
	Ò	1	l	T	10	
	ı	I	I	1	1-1	
prionity, A3 A2 A1 A0						

- 2. You want to design a 4-bit Flash ADC circuit. You need to use -5V as reference voltage, +20 V as positive power supply and -20 V as negative power supply.
  - a. Design the circuit to make the maximum output voltage 15 V.

[6]

b. What will be the step size of the circuit and Retput voltage for 0101, 1010.

[2+2]



For Max oulput, Imput 1111

Assume, R = 20 ks

2. b) For, 0001,  
step size = 
$$\sqrt{0} = -\frac{R_F}{R} \times \sqrt{R_F} \left(0 + 0 + 0 + \frac{1}{8}\right)$$
  
=  $-\frac{32 \times -5}{20} \times \left(\frac{1}{8}\right)$   
=  $1 \times \sqrt{0}$   
 $\sqrt{0}$  for 0101 =  $-\frac{32}{20} \times -5 \left(0 + \frac{1}{2} + 0 + \frac{1}{8}\right) = 5 \times \sqrt{0}$   
 $\sqrt{0}$  for, 1010 =  $-\frac{32}{20} \times -5 \left(1 + 0 + \frac{1}{4} + 0\right) = 10 \times \sqrt{0}$