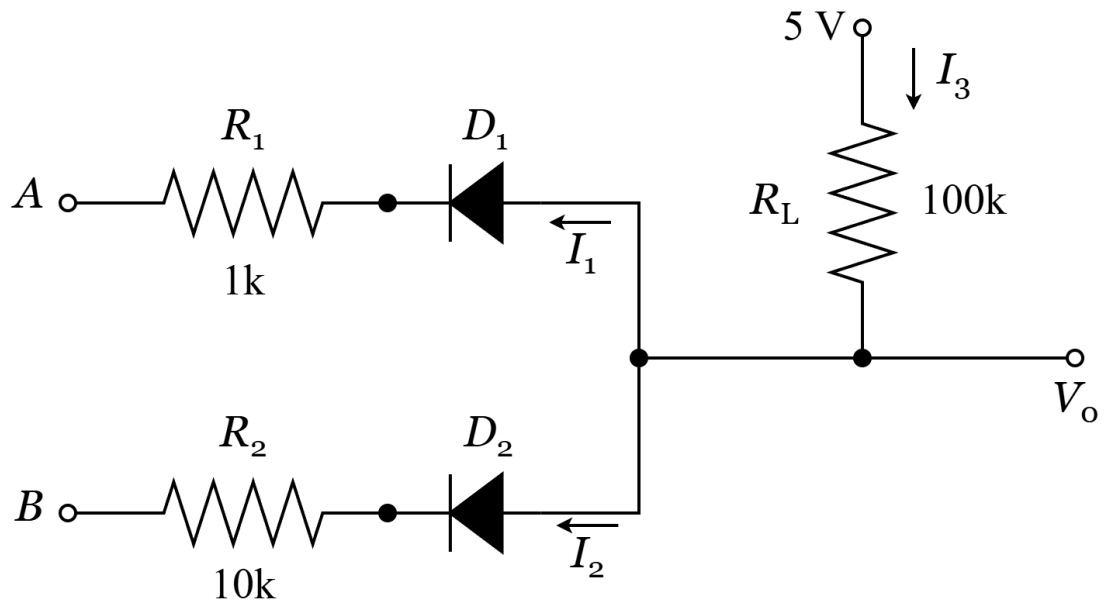


Assignment 1

For this diode logic AND gate,

1. Derive the output voltage for all input combinations.
2. Determine the power dissipation for all input combinations.

Complete the given truth table. Show all relevant calculations.



A (V)	B (V)	V_o (V)	Power Dissipation (mW)
0	0		
0	5		
5	0		
5	5		