

COMP 122/L: Computer Arch. and Assembly Language

Summer 2024

Instructor: Mahdi Ebrahimi (mahdi.ebrahimi@csun.edu)

Office: Online via Zoom (Zoom link is available in Canvas)

Section	Class Title	Dates & Times	Room	Class Dates
COMP 122-01 (10466)	COMP ARCH ASSEM (Lecture)	MoTuWeTh 9:00AM - 10:35AM	Online Class	May 24, 2024- Jul 09, 2024
COMP 122L-01 (10467)	COMP ARCH ASSEM (Laboratory)	MoTuWeTh 11:00AM - 12:35PM	Online Class	May 24, 2024- Jul 09, 2024

Communication:

Please use email rather than telephone voice mail for messages. Please keep emails short and focused, and use a clear subject line beginning with "COMP 122 Question". Many technical questions are better handled in person during lecture and lab rather than email since the class as a whole might benefit from the discussion.

You may email me (mahdi.ebrahimi@csun.edu) at any time; I will generally respond within 24 hours (during the academic days). Always include your name, course, and CSUN email address in your messages to me; an email address like meqwik@love.com leaves me clueless about who you are!

Course Description (from the catalog)

Introduction to computer architecture, assembly language programming system software, and computer applications. Number systems and data representation. Internal organization of a computer. Primitive instructions and operations. Assembly language.

Learning Objectives

A successful student will learn basic assembly programming skills, understand the fundamentals of digital logic design, and understand the interface between the two. The ARMv4 instruction set will be used for assembly, though the concepts are broadly applicable. In particular, successful students will be able to:

- Describe how modern computers represent numbers and interconvert between different numeric representations
- Perform common operations over computer-represented numbers, and design circuits which perform these operations
- Write programs in ARM assembly, including conditionals, loops, arrays, and functions
- Design and simplify combinatorial circuits with Boolean algebra and Karnaugh maps
- Design sequential circuits, including those implementing finite state machines
- Design a simplistic processor implementing a restricted assembly language
- Understand, from a high-level, the design of a high-level processor
- ...among many others

Prerequisite: Grade of "C" or better in [COMP 110/L](#); Grade of "C" or better in [MATH 103](#), [MATH 104](#), [MATH 105](#), [MATH 150A](#) or [MATH 255A](#), or a passing score on the [Mathematics Placement Test \(MPT\)](#) that satisfies prerequisites for MATH 150A or 255A; Lower division writing requirement.

Corequisite: COMP 122L.

Course Material:

Course material is available on Canvas (<https://canvas.csun.edu>). Labs, Assignments, Exams, and grades will be posted on canvas (<https://canvas.csun.edu>). Any questions about a homework/exam

grade should be addressed within **two** days of posting. After two days, all grades are final.

Textbook

No textbook is required. If you'd like a textbook for further study, two decent supplemental textbooks are:

1. Computer Organization and Design: The Hardware/Software Interface (David A. Patterson and John L. Hennessy); any edition from the past several years
2. Computer Systems Organization and Architecture (John D. Carpinelli)

Grading

You will receive a **single combined grade** for the lecture and lab. Your grade is based on the following components:

Component	Weight
Lab Assignments	40%
Exam 1	15%
Exam 2	20%
Exam 3	25%
Total	100%

The exact number of lab assignments has not been set, as this will depend somewhat on how the class progresses. Lab assignments are submitted through Canvas ([https:// canvas.csun.edu/](https://canvas.csun.edu/)). In the event that there is a problem with Canvas, you may email your assignment to me (mahdi.ebrahimi@csun.edu), though this should be considered a last resort.

Plus/minus grading is used, according to the scale below. The left column shows the minimum score necessary to receive the grade in the right column. The highest letter grade possible given the score is chosen; e.g., if you receive an 88.2, you'd receive a 'B+' for the course, which corresponds to being ≥ 86.5 .

If your score is \geqyou will receive...
92.5	A
89.5	A-
86.5	B+
82.5	B
79.5	B-
76.5	C+
72.5	C
69.5	C-
66.5	D+
62.5	D
59.5	D-
0	F

- **NOTE: Failure to take the Final Exam will result in a grade of “WU” which is equivalent to a grade of “F”**
- In fairness to all, I don't give make-up for any missed projects, homework, or exams.
- An incomplete (I) grade is given for genuine medical and other certified emergencies only; it is never given to catch up with missed assignments. Furthermore, to receive an Incomplete grade, you must have successfully completed at least two-thirds of the semester with a passing grade.

Late Policy:

Late assignments will be accepted without penalty if prior arrangements have been made or there is some sort of legitimate emergency (at my discretion). If an assignment is otherwise submitted late, it will be penalized according to the following scale:

If your assignment is late by <= this many days...	...it will be deducted by...
1	10%
2	30%
3	60%
4+	100%

To be clear, assignments that are submitted four or more days beyond the deadline will not receive credit.

Plagiarism and Academic Honesty

While collaboration is allowed on lab assignments, you are responsible for all of your own work. You may **not** take code from online sources and submit it as your own. No discussion whatsoever is allowed during exams, except with the instructor. Any violations can result in a failing grade for the assignment or potentially failing the course for egregious cases. A report will also be made to the Dean of Academic Affairs.

Students who repeatedly violate this policy across multiple courses may be suspended or even expelled.

Disabled Students

"If you have a disability and need accommodations, please register with the Disability Resources and Educational Services (DRES) office or the National Center on Deafness (NCOD). The DRES office is located in Bayramian Hall, room 110, and can be reached at (818) 677-2684. NCOD is located on Bertrand Street in Jeanne Chisholm Hall and can be reached at (818) 677-2611. If you would like to discuss your need for accommodations with me, please contact me to set up an appointment."

Changes to Syllabus

Changes may be needed to this syllabus and to the course plan. All such changes will be announced in class and will be announced via email. Students are responsible for this information.

Class Schedule and List of Topics (Subject to Change)

Exactly which topics are covered and when is subject to change.

Week	Dates	Topics
1	5/28 - 5/30	Number representation
2	6/3 - 6/6	Operations on binary values Exam 1 review
3	6/10 - 6/11	Exam 1: 6/10 @ 9:00 AM - 12:30 PM, Online via Canvas MIPS assembly: introduction and arithmetic
	6/12 - 6/13	MIPS assembly: conditionals and memory operations
4	6/17 - 6/18	MIPS assembly: Complex conditionals
	6/19 - 6/20	MIPS assembly: loops, arrays, functions Exam 2 review
5-6	6/24 - 6/25	Exam 2: 6/24 @ 9:00 AM - 12:30 PM, Online via Canvas
	7/1 - 7/2	Boolean logic and introductory combinatorial circuits
6	7/3 - 7/4	Simplifying circuits with Boolean algebra and K-maps
7	7/8 - 7/9	Exam 3 Review Exam 3: 7/9 @ 9:00 AM - 12:30 PM, Online via Canvas