

SPI 1

SCK1 - B14 (25)

SDI1 - B11 (22)

SDO1 - B13 (24)

SS1 - B15 (26)

SCK - a clock, controlled by Master

SDI - data input to Master (MISO)

SDO - data output from Master (MOSI)

SS - chip select output from Master (SS)

Write a library
MCP4922 pinOut

- spi.c & spi.h

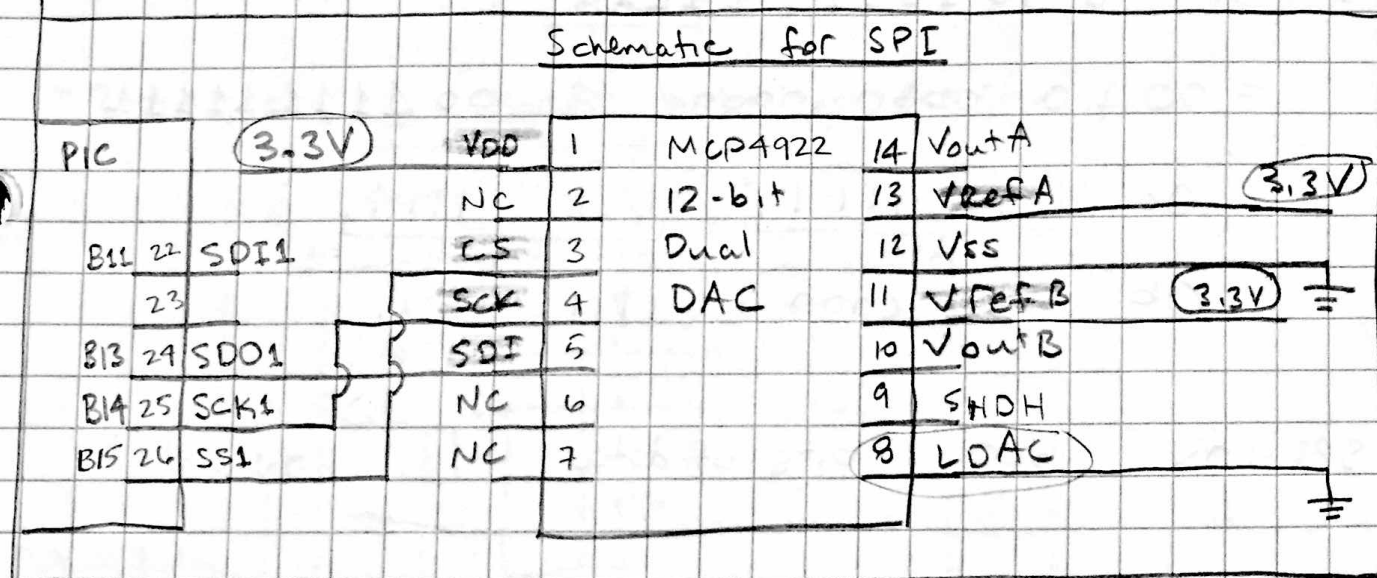
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DAC Output:

(1) 10 Hz sine wave VoutA

(2) 5 Hz triangle wave VoutB

update values 1000 times a second

Schematic for SPI

Data transfer rate

Clock rate

Signal rate

Update rate

SPI 1 :Pin Mapping

a = 0 or 1

V = 0 - 1023

t = a << 15

shifts bits to left

SPIRBF