HARYANA STATE BOARD OF TECHNICAL EDUCATION, PANCHKULA Result of Branch ELECTRICAL ENGINEERING (09) 3rd SEMESTER (Batch 2019) Examination held in June 2025

Notification No.: HSBTE/Re/004/09/Jun/2025								ReAppear														Name of Institute : Guru Daksh Government Polytechnic Hisar (Hisar (0											
Branc	h Name : ELEC	TRICAL ENGINEERIN	G															Date	of Pu	blicat	tion : 1	14 Jul	2025														Semester	: 3		
S.No.	Roll No	Student Name Father Name	180931 Electrical and Electronics Engineering Materials						180932 Non Conventional Sources of Energy				180933					180934 Electrical Engineering Design and Drawing-I							180935						180936 Electrical Workshop Practice - I		180	180937			Result			
		Mother Name Max Marks=>											Electronics-II				nd							Estimating and Costing in Electrica Engineering				al	Computer Applications In Electrical Engineering				Marks	Marks						
			Theory					Theory				Theory Practic				ctical	l Theory					Practical		Theory		Pr	Practical		Practical		Practical		/ Max	Max						
			Theory External	Assesment 5%	Grace Marks	Modaration	Total	Theory External	sesr	Grace Marks	Modaration	Total	Theory External) SE	Grace Marks Modaration	Total	Practical External	Total	Theory External	Assesment 5%	Grace Marks	Modaration		Practical External	Total	Theory External	Assesment 5%	Grace Marks	Modaration Total	Practical External	Total		Practical External	Total	Practical External	Total	Sessional Marks,	Marks Obtained /		
				5			100	100 /95	5			100	100 /95	5		100	50	50	100 /95	5		10	00	50		100 /95	5		10	50) 50	0	50	50	50	50				
1	190040900004	ANISH KHAN RAJU KHAN SHAHNAZ	22	3	-	8	33	31	3	-	8	42	10	0		10 R	32	32	44	0	-	- 4	14	36	36	33	0	-	- 33	3 35	5 3	5	38	38	32	32	169/325	Re-Appear	Re-Appear	

¹⁾ This result is subject to Alteration, Modification & fulfillment of eligibility condition 2) 'R' means Re-Appear, 'AB' means Absent 3) Total Grace of 5 marks in Maximum of 2 subjects

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Signature of Assistant Secretory