


HARYANA STATE BOARD OF TECHNICAL EDUCATION, PANCHKULA
Result of Branch ELECTRONICS AND COMMUNICATION ENGINEERING (10) 3^d SEMESTER (Batch 2022) Examination held in June 2024

Notification No. : HSBTE/Re/004/10/Jun/2024	ReAppear	Name of Institute : Guru Daksh Government Polytechnic Hisar (004)
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Branch Name : ELECTRONICS AND COMMUNICATION ENGINEERING	Date of Publication : 16 Aug 2024	Semester : 3
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S.No.	Roll No	Student Name Father Name Mother Name	220832/212832									220833									221031									221034														Result			
			Digital Electronics									Programming in C									Analog and Digital Communication									Electronic Devices and Circuits-II									Industrial/In-House Training - I						Electronic Design and Simulation		
			Theory					Practical				Theory					Practical				Theory					Practical				Theory					Practical				Practical						Practical		
			Theory Internal	Theory External	Grace Marks	Moderation	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Moderation	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Moderation	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Moderation	Total	Practical Internal	Practical External	Total	Practical Internal	Practical External	Total										
		Max Marks=>		40	60			100	40	60	100	40	60			100	40	60	100	40	60			100	40	60	100	40	60			100	40	60	100	40	60	100	400	1000							
1	220041000005	ANJALI DOHARE ROHITASHWA CHANDRA DOHARE SEEMA DOHARE	23	29	-	-	52	29	50	79	32	26	-	-	58	27	38	65	31	23	-	-	54	32	50	82	35	7	-	-	42 R	34	48	82	34	45	79	29	34	63	306	Re-Appear	Re-Appear				
2	220041000007	ANKIT SURAJ MAL MEENA DEVI	21	8	-	-	29 R	25	33	58	19	11	-	-	30 R	25	37	62	20	20	-	-	40	22	30	52	24	3	-	-	27 R	27	28	55	18	34	52	25	34	59	226	Re-Appear	Re-Appear				
3	220041000022	HARIOM RAMESH KUMAR SUNITA DEVI	21	13	-	-	34 R	25	44	69	25	24	-	-	49	25	48	73	20	5	-	-	25 R	22	40	62	25	2	-	-	27 R	28	41	69	27	54	81	34	52	86	252	Re-Appear	Re-Appear				
4	220041000030	MILAN MOHINDER SINGH BHATERI RANI	20	20	-	-	40	24	36	60	23	9	-	-	32 R	27	29	56	20	20	-	1	41	22	32	54	20	20	-	-	40	28	28	56	29	47	76	32	40	72	245	Re-Appear	Re-Appear				
5	220041000033	MOHIT TANEJA HARBANS TANEJA CHANCHAL TANEJA	22	21	-	-	43	26	45	71	29	37	-	-	66	29	46	75	25	6	-	-	31 R	26	43	69	24	5	-	-	29 R	25	44	69	30	51	81	30	46	76	266	Re-Appear	Re-Appear				
6	220041000037	NIZAM FAKHAN ASHA	20	7	-	-	27 R	28	20	48	22	24	-	-	46	26	29	55	20	9	-	-	29 R	25	36	61	25	6	-	-	31 R	26	25	51	24	32	56	24	35	59	240	Re-Appear	Re-Appear				

- 1) This result is subject to Alteration, Modification & fulfillment of eligibility condition
- 2) 'R' means Re-Appear, 'AB' means Absent
- 3) Grace Marks@5 marks in two subject


Signature of Assistant Secretary

HARYANA STATE BOARD OF TECHNICAL EDUCATION, PANCHKULA
Result of Branch ELECTRONICS AND COMMUNICATION ENGINEERING (10) 3rd SEMESTER (Batch 2022) Examination held in June 2024

Notification No. : HSBTE/Re/004/10/Jun/2024	ReAppear	Name of Institute : Guru Daksh Government Polytechnic Hisar (004)
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Branch Name : ELECTRONICS AND COMMUNICATION ENGINEERING	Date of Publication : 16 Aug 2024	Semester : 3
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S.No.	Roll No	Student Name Father Name Mother Name	220832/212832									220833									221031									221034												Sessional Total	Marks Obtained	Result			
			Digital Electronics									Programming in C									Analog and Digital Communication									Electronic Devices and Circuits-II									Industrial/In-House Training - I						Electronic Design and Simulation		
			Theory					Practical				Theory					Practical				Theory					Practical				Theory					Practical				Practical						Practical		
			Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External				Total		
		Max Marks==>		40	60			100	40	60	100	40	60			100	40	60	100	40	60			100	40	60	100	40	60			100	40	60	100	40	60	100	40	60	100	400	1000				
7	220041000041	PAWAN KUMAR RAMESH KUMAR POONAM DEVI	23	5	-	-	28 R	28	37	65	17	14	-	-	31 R	24	37	61	21	20	-	1	42	28	35	63	26	20	-	-	46	25	25	50	22	38	60	27	33	60	241	Re-Appear	Re-Appear				
8	220041000042	RAHUL UGAR SAIN GEETA DEVI	19	8	-	-	27 R	23	44	67	20	12	-	-	32 R	23	37	60	19	6	-	-	25 R	21	33	54	21	20	-	-	41	23	32	55	18	30	48	24	33	57	211	Re-Appear	Re-Appear				
9	220041000045	RAHUL SATYANARYAN SHARMA SATYANARYAN SHARMA SUSHILA SHARMA	17	20	-	-	37 R	24	35	59	18	44	-	-	62	30	30	60	25	20	-	1	46	28	35	63	21	20	-	-	41	29	48	77	20	30	50	29	29	58	241	Re-Appear	Re-Appear				
10	220041000048	RAMAN SHAMSHER SUMAN	20	21	-	-	41	26	35	61	23	27	-	-	50	27	33	60	25	10	-	-	35 R	27	35	62	30	12	-	-	42 R	31	38	69	23	30	53	23	32	55	255	Re-Appear	Re-Appear				
11	220041000052	SAHIL BALWAN SUNITA	20	7	-	-	27 R	26	42	68	25	8	-	-	33 R	24	32	56	25	6	-	-	31 R	30	35	65	20	3	-	-	23 R	25	37	62	23	36	59	23	50	73	241	Re-Appear	Re-Appear				
12	220041000053	SAHIL BHAL SINGH KAVITA	21	8	-	-	29 R	27	35	62	21	10	-	-	31 R	25	33	58	25	5	-	-	30 R	26	40	66	24	8	-	-	32 R	25	38	63	21	30	51	26	36	62	241	Re-Appear	Re-Appear				

- 1) This result is subject to Alteration, Modification & fulfillment of eligibility condition
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- 3) Grace Marks@5 marks in two subject


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HARYANA STATE BOARD OF TECHNICAL EDUCATION, PANCHKULA
Result of Branch ELECTRONICS AND COMMUNICATION ENGINEERING (10) 3^d SEMESTER (Batch 2022) Examination held in June 2024

Notification No. : HSBTE/Re/004/10/Jun/2024	ReAppear	Name of Institute : Guru Daksh Government Polytechnic Hisar (004)
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Branch Name : ELECTRONICS AND COMMUNICATION ENGINEERING	Date of Publication : 16 Aug 2024	Semester : 3
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S.No.	Roll No	Student Name Father Name Mother Name	220832/212832									220833									221031									221034												Sessional Total	Marks Obtained	Result			
			Digital Electronics									Programming in C									Analog and Digital Communication									Electronic Devices and Circuits-II									Industrial/In-House Training - I						Electronic Design and Simulation		
			Theory					Practical				Theory					Practical				Theory					Practical				Theory					Practical				Practical						Practical		
		Theory Internal	Theory External	Grace Marks	Moderation	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Moderation	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Moderation	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Moderation	Total	Practical Internal	Practical External	Total	Practical Internal	Practical External	Total											
		Max Marks=>	40	60			100	40	60	100	40	60			100	40	60	100	40	60			100	40	60	100	40	60			100	40	60	100	40	60	100	400	1000								
13	220041000058	SUNIL KUMAR JAWAHARLAL KHAJANI DEVI	20	22	-	-	42	24	42	66	26	36	-	-	62	25	47	72	25	20	-	-	45	26	42	68	25	20	-	-	45	27	42	69	21	44	65	26	34	60	245	594	594/1000				
14	220041000059	TAMANNA DUHAN RAM MEHAR RAJ BALA	26	20	-	-	46	31	45	76	31	24	-	-	55	33	53	86	26	13	-	-	39 R	32	40	72	27	20	-	-	47	32	40	72	31	54	85	32	48	80	301	Re-Appear	Re-Appear				
15	220041023002	ANKIT RAKESH KUMAR POONAM DEVI	24	24	-	-	48	29	42	71	27	33	-	-	60	27	29	56	27	UMC	-	-	UMC R	28	38	66	29	25	-	-	54	31	29	60	26	38	64	26	42	68	274	Re-Appear	Re-Appear				
16	220041023004	ANKUSH RAJESH MOHINI	24	16	-	-	40 R	29	51	80	28	12	-	-	40 R	26	29	55	25	4	-	-	29 R	26	38	64	26	5	-	-	31 R	31	31	62	26	38	64	26	38	64	267	Re-Appear	Re-Appear				
17	220041023005	BHATERI BALWAN SUSHILA	26	22	-	-	48	31	50	81	25	33	-	-	58	27	31	58	31	20	-	1	52	33	42	75	27	20	-	-	47	32	36	68	29	44	73	29	46	75	290	635	635/1000				
18	220041023006	CHETAN RAJENDER SAROJ	19	23	-	-	42	24	43	67	22	30	-	-	52	26	33	59	25	10	-	-	35 R	27	36	63	25	10	-	-	35 R	29	28	57	26	39	65	26	42	68	249	Re-Appear	Re-Appear				

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			Digital Electronics									Programming in C									Analog and Digital Communication									Electronic Devices and Circuits-II									Industrial/In-House Training - I			Electronic Design and Simulation								
			Theory					Practical				Theory					Practical				Theory					Practical				Theory					Practical				Practical			Practical								
			Theory Internal	Theory External	Grace Marks	Moderation	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Moderation	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Moderation	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Moderation	Total	Practical Internal	Practical External	Total	Practical Internal	Practical External	Total													
		Max Marks=>	40	60			100	40	60	100	40	60			100	40	60	100	40	60			100	40	60	100	40	60			100	40	60	100	40	60	100	40	60	100	400	1000								
19	220041023007	DEEPAK RANDHIR SINGH ANJU	31	25	-	-	56	36	48	84	28	20	-	-	48	27	52	79	26	20	-	-	46	28	38	66	27	30	-	-	57	31	29	60	25	36	61	25	40	65	284	622	622/1000							
20	220041023009	HIMANSHU SANDEEP SUNITA	28	9	-	-	37 R	32	52	84	22	13	-	-	35 R	31	27	58	25	20	-	-	45	26	42	68	26	22	-	-	48	30	30	60	29	36	65	29	40	69	278	Re-Appear	Re-Appear							
21	220041023012	MANISH VIRBHAN GUDDI DEVI	20	4	-	-	24 R	25	34	59	17	8	-	-	25 R	24	29	53	20	6	-	-	26 R	22	28	50	20	4	-	-	24 R	22	29	51	23	32	55	23	36	59	216	Re-Appear	Re-Appear							
22	220041023013	PANKAJ RAVINDER SUNITA	21	27	-	-	48	29	34	63	27	33	-	-	60	27	31	58	25	5	-	-	30 R	29	35	64	25	11	-	-	36 R	28	35	63	30	37	67	30	38	68	271	Re-Appear	Re-Appear							
23	220041023019	SACHIN RAJENDER DARSHNA	29	33	-	-	62	34	44	78	29	25	-	-	54	32	46	78	31	21	-	-	52	32	45	77	27	21	-	-	48	31	40	71	29	40	69	29	43	72	303	661	661/1000							
24	220041023020	SAHIL JAIBHAGWAN MEENA	24	20	-	-	44	29	40	69	24	12	-	-	36 R	28	29	57	25	UMC	-	-	UMC R	29	36	65	25	20	-	-	45	28	30	58	29	42	71	29	36	65	270	Re-Appear	Re-Appear							
<div>1) This result is subject to Alteration, Modification & fulfillment of eligibility condition</div> <div>2) 'R' means Re-Appear, 'AB' means Absent</div> <div>3) Grace Marks@5 marks in two subject</div> <div>4 of 5</div> <div>Ronia</div> <div>Signature of Assistant Secretary</div>																																																		

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S.No.	Roll No	Student Name Father Name Mother Name	220832/212832						220833						221031						221034											Result											
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			Theory			Practical			Theory			Practical			Theory			Practical			Theory			Practical			Practical						Practical										
			Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks				Modaration	Total	Practical Internal	Practical External	Total	Practical Internal	Practical External	Total			
		Max Marks=>		40	60			100	40	60	100	40	60			100	40	60	100	40	60			100	40	60	100	40	60			100	40	60	100	40	60	100	400	1000			
25	220041023022	SHUBHAM RAMNARAYAN KAVITA	10	2	-	-	12 R	30	34	64	12	31	-	-	43	28	32	60	16	7	-	-	23 R	18	32	50	13	9	-	-	22 R	30	33	63	16	32	48	16	31	47	189	Re-Appear	Re-Appear

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