HARYANA STATE BOARD OF TECHNICAL EDUCATION, PANCHKULA
Result of Branch ELECTRONICS AND COMMUNICATION ENGINEERING (10) 5th SEMESTER (Batch 2019) Examination held in June 2024

Notification No.: HSBTE/Re/004/10/Jun/2024 ReAppear ReAppear														nic Hisar (004)																								
Branch Name : ELECTRONICS AND COMMUNICATION ENGINEERING												Date of Publication : 16 Aug 2024 Semester : 5													neste	r : 5												
S.No.	Roll No	Student Name Father Name Mother Name	181051					181052					181053					181054					181055												Marks	Marks	Result	
			Computer Networks					Audio Video Systems					Digital Communication					Optical Fiber Communication					Microcontrollers						Electronics Design and Simulation Techniques			Indus	Industrial training					
			Theory					Theory Prac				tical	al Theory			Practical		ı	Theory			Prac	Practical		Theory			Practical		Practical		Practical		/ Max	Max			
			/ External	Marks	aration		/ External	Marks	aration		cal External		/ External	Marks	aration	al External		/ External	Marks	aration	ıration	al External		External	Marks	aration		al External		al External				Cal Externa		sional Marks	(s Obtained	
			Theor	Grace	Mod	Tota	Theor	Grace	Mod	Tota	Practi	Tota	Theor	Grace	Mode	Practi	Tota	Theor	Grace	Mod	Tota	Practi	Total	Theor	Grace	Mod	Tota	Practi	Total		Practi	Tota	Oracti	7 acu	- Ota	Ses	Mar	
		Max Marks=>	100			100	100			100	50	50	100		10	0 50	50	100	)		100	50	50	100			100	50	50	50		50	100	,	100			
1	190041020013	SACHIN VASHISHTH OMVEER VASHISHTH RAMVATI VASHISHTH		-	-	36	7	-	-	7 R	37	37	17	-   -	17 R	7 37	37	41	-	-	41	32	32	10	-	-	10 R	27	27	31		31	80		80	253/400	Re-Appear	Re-Appear

1) This result is subject to Alteration, Modification & fulfillment of eligibility condition 2) 'R' means Re-Appear, 'AB' means Absent 3) Grace Marks@5 marks in two subject

1 of 1

Signature of Assistant Secretory