


HARYANA STATE BOARD OF TECHNICAL EDUCATION, PANCHKULA
Result of Branch ELECTRICAL ENGINEERING (09) 3rd SEMESTER (Batch 2018) Examination held in June 2025

Notification No. : HSBTE/Re/004/09/Jun/2025	ReAppear	Name of Institute : Guru Daksh Government Polytechnic Hisar (004)
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Branch Name : ELECTRICAL ENGINEERING	Date of Publication : 14 Jul 2025	Semester : 3
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S.No.	Roll No	Student Name Father Name Mother Name	180931					180932					180933					180934					180935					180936		180937		Sessional Marks / Max Marks	Marks Obtained / Max Marks	Result						
			Electrical and Electronics Engineering Materials					Non Conventional Sources of Energy					Electronics-II					Electrical Engineering Design and Drawing-I					Estimating and Costing in Electrical Engineering					Electrical Workshop Practice - I		Computer Applications In Electrical Engineering										
			Theory					Theory					Theory			Practical		Theory			Practical		Theory			Practical		Practical		Practical										
			Theory External	Assesment 5%	Grace Marks	Modaration	Total	Theory External	Assesment 5%	Grace Marks	Modaration	Total	Theory External	Assesment 5%	Grace Marks	Modaration	Total	Practical External	Total	Theory External	Assesment 5%	Grace Marks	Modaration	Total	Practical External	Total	Theory External	Assesment 5%	Grace Marks	Modaration	Total				Practical External	Total	Practical External	Total		
		Max Marks=>	100 /95	5			100	100 /95	5			100	100 /95	5			100	50	50	100 /95	5			100	50	50	100 /95	5			100	50	50		50	50	50	50		
1	180040900009	AMIT SINGH AZAD SINGH SANTOSH DEVI	49	0	-	-	49	67	0	-	-	67	52	0	-	-	52	39	39	31	5	-	-	36	37	37	56	0	-	-	56	38	38	39	39	40	40	230/325	683/1075	683

- 1) This result is subject to Alteration, Modification & fulfillment of eligibility condition
- 2) 'R' means Re-Appear, 'AB' means Absent
- 3) Total Grace of 5 marks in Maximum of 2 subjects


Signature of Assistant Secretary