


HARYANA STATE BOARD OF TECHNICAL EDUCATION, PANCHKULA
Result of Branch ELECTRONICS AND COMMUNICATION ENGINEERING (10) 5^h SEMESTER (Batch 2019) Examination held in December 2024

Notification No. : HSBTE/Re/004/10/Dec/2024										ReAppear										Name of Institute : Guru Daksh Government Polytechnic Hisar (004)																	
Branch Name : ELECTRONICS AND COMMUNICATION ENGINEERING												Date of Publication : 14 Feb 2025										Semester : 5															
S.No.	Roll No	Student Name Father Name Mother Name	181051				181052					181053					181054					181055									Sessional Marks / Max Marks	Marks Obtained / Max Marks	Result				
			Computer Networks				Audio Video Systems					Digital Communication					Optical Fiber Communication					Microcontrollers					Electronics Design and Simulation Techniques							Industrial training			
			Theory				Theory			Practical		Theory			Practical		Theory			Practical		Theory			Practical		Practical							Practical			
			Theory External	Grace Marks	Modaration	Total	Theory External	Grace Marks	Modaration	Total	Practical External	Total	Theory External	Grace Marks	Modaration	Total	Practical External	Total	Theory External	Grace Marks	Modaration	Total	Practical External	Total	Theory External	Grace Marks	Modaration	Total	Practical External	Total				Practical External	Total		
		Max Marks=>	100			100	100			100	50	50	100			100	50	50	100			100	50	50	100			100	50	50	50	50		100	100		
1	190041000053	YOGESHDHAR DIWEDI SHIV NARYANA RAJ KUMARI	35	-	-	35	17	-	-	17 R	27	27	34	-	-	34	30	30	4	-	-	4 R	28	28	12	-	-	12 R	22	22	33	33	65	65	236/400	Re-Appear	Re-Appear

1) This result is subject to Alteration, Modification & fulfillment of eligibility condition
2) 'R' means Re-Appear, 'AB' means Absent
3) Total Grace of 5 marks in Maximum of 2 subjects


Signature of Assistant Secretary