

HARYANA STATE BOARD OF TECHNICAL EDUCATION, PANCHKULA

Result of Branch ELECTRONICS AND COMMUNICATION ENGINEERING (10) 3^d SEMESTER (Batch 2023) Examination held in December 2024

Notification No. : HSBTE/R/004/10/Dec/2024										Regular										Name of Institute : Guru Daksh Government Polytechnic Hisar (004)																							
Branch Name : ELECTRONICS AND COMMUNICATION ENGINEERING										Date of Publication : 14 Feb 2025										Semester : 3																							
S.No.	Roll No	Student Name Father Name Mother Name	220832/212832						220833						221031						221034									Sessional Total	Marks Obtained	Result											
			Digital Electronics						Programming in C						Analog and Digital Communication						Electronic Devices and Circuits-II						Industrial/In-House Training - I						Electronic Design and Simulation										
			Theory			Practical			Theory			Practical			Theory			Practical			Theory			Practical			Practical						Practical										
			Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks				Modaration	Total	Practical Internal	Practical External	Total	Practical Internal	Practical External	Total			
Max Marks=>			40	60			100	40	60	100	40	60			100	40	60	100	40	60			100	40	60	100	40	60			100	40	60	100	40	60	100	400	1000				
1	230041000001	ABHINAV SAINI NARESH KUMAR SHAKUNTLA DEVI	21	8	-	5	34 R	25	31	56	19	7	-	-	26 R	21	32	53	22	15	-	-	37 R	26	39	65	21	8	-	-	29 R	26	40	66	28	49	77	26	48	74	235	Re-Appear	Re-Appear
2	230041000004	AMAN SHASHI KAPOOR MONU	25	12	-	5	42 R	29	32	61	24	14	-	-	38 R	29	34	63	24	20	-	-	44	26	38	64	20	8	-	-	28 R	25	44	69	30	45	75	33	45	78	265	Re-Appear	Re-Appear
3	230041000005	AMAN KULDEEP POONAM	34	39	-	5	78	36	57	93	38	22	-	-	60	37	57	94	34	43	-	-	77	38	55	93	31	40	-	-	71	36	57	93	36	57	93	38	57	95	358	847	847/1000
4	230041000008	AMAN PARSAD TRILOKI PARSAD MANSA DEVI	25	27	-	5	57	29	38	67	31	28	-	-	59	31	39	70	28	46	-	-	74	32	41	73	18	36	-	-	54	28	36	64	29	46	75	28	45	73	279	666	666/1000
5	230041000009	ANJALI JAIBIR SINGH KAVITA	25	14	-	5	44 R	29	37	66	26	12	-	-	38 R	31	39	70	21	20	-	-	41	29	49	78	19	13	-	-	32 R	28	42	70	28	44	72	30	43	73	266	Re-Appear	Re-Appear
6	230041000011	ANKIT JAIBIR RAJPATI	21	11	-	5	37 R	24	32	56	24	11	-	-	35 R	30	34	64	20	20	-	-	40	28	37	65	17	7	-	-	24 R	27	36	63	29	49	78	28	48	76	248	Re-Appear	Re-Appear

1) This result is subject to Alteration, Modification & fulfillment of eligibility condition

2) 'R' means Re-Appear, 'AB' means Absent


3) Total Grace of 5 marks in Maximum of 2 subjects


Signature of Assistant Secretary

HARYANA STATE BOARD OF TECHNICAL EDUCATION, PANCHKULA
Result of Branch ELECTRONICS AND COMMUNICATION ENGINEERING (10) 3rd SEMESTER (Batch 2023) Examination held in December 2024

Notification No. : HSBTE/R/004/10/Dec/2024														Regular														Name of Institute : Guru Daksh Government Polytechnic Hisar (004)															
Branch Name : ELECTRONICS AND COMMUNICATION ENGINEERING														Date of Publication : 14 Feb 2025														Semester : 3															
S.No.	Roll No	Student Name Father Name Mother Name	220832/212832							220833							221031							221034											Marks Obtained	Result							
			Digital Electronics							Programming in C							Analog and Digital Communication							Electronic Devices and Circuits-II							Industrial/In-House Training - I						Electronic Design and Simulation						
			Theory					Practical		Theory					Practical		Theory					Practical		Theory					Practical		Practical						Practical						
			Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total			Practical Internal	Practical External	Total				
		Max Marks=>	40	60			100	40	60	100	40	60			100	40	60	100	40	60			100	40	60	100	40	60			100	40	60	100	40	60	100	400	1000				
7	230041000012	ANKUSH KULDEEP SUDESH	18	20	-	5	43	24	32	56	33	29	-	-	62	34	34	68	27	39	-	-	66	30	46	76	26	25	-	-	51	32	50	82	35	47	82	29	47	76	288	662	662/1000
8	230041000014	ANURAG SINGH RAVINDER SINGH KAVITA SINGH	21	24	-	5	50	23	49	72	22	20	-	-	42	27	49	76	25	35	-	-	60	29	42	71	26	34	-	-	60	28	45	73	25	50	75	20	49	69	246	648	648/1000
9	230041000016	ASHISH BHIM SINGH PREMA	19	12	-	5	36 R	24	34	58	29	9	-	-	38 R	32	36	68	22	20	-	-	42	30	34	64	25	9	-	-	34 R	28	48	76	32	47	79	36	47	83	277	Re-Appear	Re-Appear
10	230041000017	AVINASH PASWAN AVDHESH PASWAN NEELU DEVI	17	AB	-	-	17 R	20	32	52	22	AB	-	-	22 R	26	AB	26 R	22	AB	-	-	22 R	31	AB	31 R	19	AB	-	-	19 R	26	40	66	28	AB	28 R	26	AB	26 R	237	Re-Appear	Re-Appear
11	230041000018	DEEPAK KUMAR SATYAWAN SINGH POONAM	28	15	-	5	48	32	31	63	27	10	-	-	37 R	29	43	72	23	21	-	-	44	31	41	72	21	11	-	-	32 R	27	42	69	28	48	76	34	48	82	280	Re-Appear	Re-Appear
12	230041000020	DINESH PARKASH NEELAM	33	20	-	5	58	36	55	91	34	20	-	-	54	36	54	90	32	33	-	-	65	37	54	91	31	15	-	-	46 R	35	54	89	34	50	84	37	50	87	345	Re-Appear	Re-Appear

1) This result is subject to Alteration, Modification & fulfillment of eligibility condition
2) 'R' means Re-Appear, 'AB' means Absent
3) Total Grace of 5 marks in Maximum of 2 subjects


Signature of Assistant Secretary

HARYANA STATE BOARD OF TECHNICAL EDUCATION, PANCHKULA


Result of Branch ELECTRONICS AND COMMUNICATION ENGINEERING (10) 3^d SEMESTER (Batch 2023) Examination held in December 2024

Notification No. : HSBTE/R/004/10/Dec/2024										Regular										Name of Institute : Guru Daksh Government Polytechnic Hisar (004)																							
Branch Name : ELECTRONICS AND COMMUNICATION ENGINEERING										Date of Publication : 14 Feb 2025										Semester : 3																							
S.No.	Roll No	Student Name Father Name Mother Name	220832/212832						220833						221031						221034											Result											
			Digital Electronics						Programming in C						Analog and Digital Communication						Electronic Devices and Circuits-II						Industrial/In-House Training - I						Electronic Design and Simulation										
			Theory			Practical			Theory			Practical			Theory			Practical			Theory			Practical			Practical						Practical										
			Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks				Modaration	Total	Practical Internal	Practical External	Total	Practical Internal	Practical External	Total			
		Max Marks=>	40	60			100	40	60	100	40	60			100	40	60	100	40	60			100	40	60	100	40	60			100	40	60	100	40	60	100	400	1000				
13	230041000021	GARIMA BHAWAN SINGH HEMA DEVI	17	11	-	5	33 R	21	AB	21 R	28	20	-	-	48	33	AB	33 R	25	20	-	-	45	30	AB	30 R	22	12	-	-	34 R	28	AB	28 R	31	AB	31 R	20	AB	20 R	255	Re-Appear	Re-Appear
14	230041000022	GOVIND DHOOP SINGH SUNEHARI	17	9	-	5	31 R	21	32	53	23	9	-	-	32 R	24	36	60	22	15	-	-	37 R	30	38	68	16	9	-	-	25 R	25	34	59	26	37	63	26	30	56	230	Re-Appear	Re-Appear
15	230041000023	GOVIND KUMAR BALRAM LAKSHMI DEVI	25	31	-	5	61	28	32	60	27	20	-	-	47	29	49	78	25	22	-	-	47	31	41	72	25	11	-	-	36 R	27	48	75	24	39	63	20	38	58	261	Re-Appear	Re-Appear
16	230041000024	GULSHAN DASS SUNDER DASS PINKI DEVI	21	12	-	5	38 R	25	39	64	26	10	-	-	36 R	30	38	68	22	15	-	-	37 R	30	42	72	17	9	-	-	26 R	27	36	63	29	40	69	20	40	60	247	Re-Appear	Re-Appear
17	230041000025	GURU SEWAK SURENDER SAROJ	23	20	-	5	48	26	35	61	27	22	-	-	49	33	46	79	28	15	-	-	43 R	33	43	76	26	20	-	-	46	28	44	72	31	48	79	35	48	83	290	Re-Appear	Re-Appear
18	230041000028	KAPIL UMED SINGH SUSHIL	15	13	-	5	33 R	22	36	58	22	10	-	-	32 R	28	39	67	22	20	-	-	42	30	40	70	20	3	-	-	23 R	27	37	64	31	39	70	28	38	66	245	Re-Appear	Re-Appear

1) This result is subject to Alteration, Modification & fulfillment of eligibility condition

2) 'R' means Re-Appear, 'AB' means Absent

3) Total Grace of 5 marks in Maximum of 2 subjects


Signature of Assistant Secretary

HARYANA STATE BOARD OF TECHNICAL EDUCATION, PANCHKULA


Result of Branch ELECTRONICS AND COMMUNICATION ENGINEERING (10) 3^d SEMESTER (Batch 2023) Examination held in December 2024

Notification No. : HSBTE/R/004/10/Dec/2024										Regular										Name of Institute : Guru Daksh Government Polytechnic Hisar (004)																							
Branch Name : ELECTRONICS AND COMMUNICATION ENGINEERING										Date of Publication : 14 Feb 2025										Semester : 3																							
S.No.	Roll No	Student Name Father Name Mother Name	220832/212832							220833							221031							221034										Sessional Total	Marks Obtained	Result							
			Digital Electronics							Programming in C							Analog and Digital Communication							Electronic Devices and Circuits-II							Industrial/In-House Training - I						Electronic Design and Simulation						
			Theory					Practical		Theory					Practical		Theory					Practical		Theory					Practical		Practical						Practical						
			Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total			Practical Internal	Practical External	Total				
		Max Marks=>	40	60			100	40	60	100	40	60			100	40	60	100	40	60			100	40	60	100	40	60			100	40	60	100	40	60	100	400	1000				
19	230041000029	KARTIK VIJAY KUMAR KAVITA	28	12	-	5	45 R	31	44	75	29	20	-	-	49	33	49	82	28	20	-	-	48	31	46	77	24	11	-	-	35 R	30	48	78	33	45	78	36	44	80	303	Re-Appear	Re-Appear
20	230041000030	KARTIK RAJ RAJINDER SINGH VEENA	26	30	-	5	61	30	47	77	32	31	-	-	63	35	56	91	26	26	-	-	52	31	53	84	28	20	-	-	48	36	55	91	34	59	93	36	53	89	314	749	749/1000
21	230041000031	KHUSH MIGLANI MAHESH KUMAR GEETA	38	48	-	5	91	39	57	96	40	39	-	-	79	39	58	97	36	38	-	-	74	37	56	93	33	50	-	-	83	37	57	94	37	58	95	38	58	96	374	898	898/1000
22	230041000033	LALIT KUMAR NAR SINGH RAJ BALA	27	20	-	5	52	31	51	82	26	23	-	-	49	29	53	82	28	31	-	-	59	33	43	76	24	12	-	-	36 R	28	47	75	24	44	68	30	43	73	280	Re-Appear	Re-Appear
23	230041000036	MD DANISH MD JALALUDDIN RUKHSANA BEGUM	33	38	-	5	76	35	50	85	33	26	-	-	59	33	54	87	32	36	-	-	68	36	54	90	29	39	-	-	68	35	53	88	28	49	77	20	50	70	314	768	768/1000
24	230041000037	MIHIR RAJ RAJA BABU SHALINI DEVI	27	16	-	5	48	31	52	83	35	22	-	-	57	34	54	88	32	38	-	-	70	34	54	88	29	20	-	-	49	36	54	90	31	50	81	20	50	70	309	724	724/1000

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Signature of Assistant Secretary

HARYANA STATE BOARD OF TECHNICAL EDUCATION, PANCHKULA

Result of Branch ELECTRONICS AND COMMUNICATION ENGINEERING (10) 3^d SEMESTER (Batch 2023) Examination held in December 2024

Notification No. : HSBTE/R/004/10/Dec/2024										Regular										Name of Institute : Guru Daksh Government Polytechnic Hisar (004)																							
Branch Name : ELECTRONICS AND COMMUNICATION ENGINEERING										Date of Publication : 14 Feb 2025										Semester : 3																							
S.No.	Roll No	Student Name Father Name Mother Name	220832/212832						220833						221031						221034									Sessional Total	Marks Obtained	Result											
			Digital Electronics						Programming in C						Analog and Digital Communication						Electronic Devices and Circuits-II						Industrial/In-House Training - I						Electronic Design and Simulation										
			Theory			Practical			Theory			Practical			Theory			Practical			Theory			Practical			Practical						Practical										
			Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks				Modaration	Total	Practical Internal	Practical External	Total	Practical Internal	Practical External	Total			
		Max Marks=>	40	60			100	40	60	100	40	60			100	40	60	100	40	60			100	40	60	100	40	60			100	40	60	100	40	60	100	400	1000				
25	230041000038	MOHIT KULDEEP SHABINA	31	2	-	5	38 R	33	AB	33 R	27	6	-	-	33 R	32	AB	32 R	18	5	-	-	23 R	30	AB	30 R	12	6	-	-	18 R	29	AB	29 R	26	AB	26 R	20	AB	20 R	258	Re-Appear	Re-Appear
26	230041000039	MOHIT ANOOP NEELAM	32	27	-	5	64	35	53	88	32	20	-	-	52	37	51	88	31	33	-	-	64	34	52	86	28	30	-	-	58	36	50	86	35	50	85	34	50	84	334	755	755/1000
27	230041000040	MUSKAN VISHAVJEET BABITA	23	5	-	5	33 R	26	36	62	29	7	-	-	36 R	33	37	70	26	9	-	-	35 R	30	36	66	20	12	-	-	32 R	24	36	60	31	39	70	31	37	68	273	Re-Appear	Re-Appear
28	230041000042	NITIN RATAN PUSHPA	35	47	-	5	87	35	49	84	36	33	-	-	69	36	55	91	30	42	-	-	72	32	54	86	30	53	-	-	83	36	55	91	35	50	85	36	49	85	341	833	833/1000
29	230041000043	NITIN KRISHAN KUMAR SUNITA DEVI	17	14	-	5	36 R	22	34	56	17	12	-	-	29 R	24	AB	24 R	20	10	-	-	30 R	29	34	63	14	15	-	-	29 R	22	AB	22 R	26	AB	26 R	20	AB	20 R	211	Re-Appear	Re-Appear
30	230041000044	PARKASH PAWAN POONAMRANI	14	7	-	5	26 R	21	31	52	22	11	-	-	33 R	27	AB	27 R	22	5	-	-	27 R	31	31	62	17	9	-	-	26 R	20	AB	20 R	24	AB	24 R	20	38	58	218	Re-Appear	Re-Appear

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3) Total Grace of 5 marks in Maximum of 2 subjects

Ronia

Signature of Assistant Secretary

HARYANA STATE BOARD OF TECHNICAL EDUCATION, PANCHKULA


Result of Branch ELECTRONICS AND COMMUNICATION ENGINEERING (10) 3rd SEMESTER (Batch 2023) Examination held in December 2024

Notification No. : HSBTE/R/004/10/Dec/2024										Regular										Name of Institute : Guru Daksh Government Polytechnic Hisar (004)																							
Branch Name : ELECTRONICS AND COMMUNICATION ENGINEERING										Date of Publication : 14 Feb 2025										Semester : 3																							
S.No.	Roll No	Student Name Father Name Mother Name	220832/212832						220833						221031						221034											Result											
			Digital Electronics						Programming in C						Analog and Digital Communication						Electronic Devices and Circuits-II						Industrial/In-House Training - I						Electronic Design and Simulation										
			Theory			Practical			Theory			Practical			Theory			Practical			Theory			Practical			Practical						Practical										
			Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks				Modaration	Total	Practical Internal	Practical External	Total	Practical Internal	Practical External	Total			
		Max Marks=>	40	60			100	40	60	100	40	60			100	40	60	100	40	60			100	40	60	100	40	60			100	40	60	100	40	60	100	400	1000				
31	230041000045	PARVEEN KARBIR SUNITA	19	AB	-	-	19 R	23	36	59	24	AB	-	-	24 R	29	AB	29 R	20	AB	-	-	20 R	31	AB	31 R	16	AB	-	-	16 R	24	AB	24 R	27	AB	27 R	28	AB	28 R	241	Re-Appear	Re-Appear
32	230041000046	PARVEEN JAPAN SUNITA	21	6	-	5	32 R	26	44	70	23	9	-	-	32 R	26	45	71	19	5	-	-	24 R	30	39	69	19	14	-	-	33 R	26	43	69	29	46	75	20	46	66	239	Re-Appear	Re-Appear
33	230041000049	RAHUL RAMESH SHILA	21	10	-	5	36 R	25	40	65	21	9	-	-	30 R	24	43	67	22	15	-	-	37 R	31	46	77	25	14	-	-	39 R	30	41	71	32	48	80	35	49	84	266	Re-Appear	Re-Appear
34	230041000050	RAHUL RAJENDER RAMRATI	29	20	-	5	54	32	45	77	34	29	-	-	63	31	52	83	30	45	-	-	75	31	52	83	28	40	-	-	68	34	50	84	30	48	78	35	48	83	314	748	748/1000
35	230041000051	RAHUL SURENDER SHEELA DEVI	23	20	-	5	48	28	46	74	31	20	-	-	51	33	42	75	28	UMC	-	-	UMC R	30	37	67	24	10	-	-	34 R	24	41	65	33	51	84	28	50	78	282	Re-Appear	Re-Appear
36	230041000052	RAHUL SURESH PARVESH	22	30	-	5	57	27	46	73	25	20	-	-	45	29	43	72	24	10	-	-	34 R	30	41	71	23	24	-	-	47	27	41	68	32	50	82	35	50	85	274	Re-Appear	Re-Appear

1) This result is subject to Alteration, Modification & fulfillment of eligibility condition

2) 'R' means Re-Appear, 'AB' means Absent

3) Total Grace of 5 marks in Maximum of 2 subjects


Signature of Assistant Secretary

HARYANA STATE BOARD OF TECHNICAL EDUCATION, PANCHKULA


Result of Branch ELECTRONICS AND COMMUNICATION ENGINEERING (10) 3^d SEMESTER (Batch 2023) Examination held in December 2024

Notification No. : HSBTE/R/004/10/Dec/2024										Regular										Name of Institute : Guru Daksh Government Polytechnic Hisar (004)																							
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S.No.	Roll No	Student Name Father Name Mother Name	220832/212832						220833						221031						221034									Sessional Total	Marks Obtained	Result											
			Digital Electronics						Programming in C						Analog and Digital Communication						Electronic Devices and Circuits-II						Industrial/In-House Training - I						Electronic Design and Simulation										
			Theory			Practical			Theory			Practical			Theory			Practical			Theory			Practical			Practical						Practical										
			Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks				Modaration	Total	Practical Internal	Practical External	Total	Practical Internal	Practical External	Total			
		Max Marks=>	40	60			100	40	60	100	40	60			100	40	60	100	40	60			100	40	60	100	40	60			100	40	60	100	40	60	100	400	1000				
37	230041000053	RAVINDER KULDEEP SINGH BHAGWANTI	18	10	-	5	33 R	22	42	64	29	20	-	-	49	31	42	73	25	13	-	-	38 R	32	37	69	20	16	-	-	36 R	24	40	64	31	49	80	34	48	82	266	Re-Appear	Re-Appear
38	230041000054	ROHIT ANOOP NEELAM	32	21	-	5	58	34	54	88	35	21	-	-	56	36	52	88	29	30	-	-	59	34	52	86	27	36	-	-	63	34	52	86	36	48	84	36	48	84	333	752	752/1000
39	230041000056	SAHIL BALWAN SINGH SUMAN	19	8	-	5	32 R	24	33	57	23	11	-	-	34 R	28	36	64	26	3	-	-	29 R	31	40	71	21	20	-	-	41	26	35	61	25	40	65	20	39	59	243	Re-Appear	Re-Appear
40	230041000057	SAHIL NARESH MONIKA	17	11	-	5	33 R	22	47	69	27	8	-	-	35 R	30	45	75	26	20	-	-	46	32	42	74	23	12	-	-	35 R	28	44	72	30	46	76	36	45	81	271	Re-Appear	Re-Appear
41	230041000059	SOBIT BRIJASH KUMAR MANJU	26	13	-	5	44 R	30	34	64	20	10	-	-	30 R	20	40	60	21	14	-	-	35 R	31	33	64	19	11	-	-	30 R	21	38	59	24	39	63	20	39	59	232	Re-Appear	Re-Appear
42	230041000060	SUNIL RAMBHAGAT LEELAWATI	28	22	-	5	55	31	45	76	34	24	-	-	58	35	45	80	32	27	-	-	59	35	46	81	26	20	-	-	46	30	45	75	31	47	78	35	46	81	317	689	689/1000

1) This result is subject to Alteration, Modification & fulfillment of eligibility condition

2) 'R' means Re-Appear, 'AB' means Absent

3) Total Grace of 5 marks in Maximum of 2 subjects



Signature of Assistant Secretary

HARYANA STATE BOARD OF TECHNICAL EDUCATION, PANCHKULA

Result of Branch ELECTRONICS AND COMMUNICATION ENGINEERING (10) 3^d SEMESTER (Batch 2023) Examination held in December 2024

Notification No. : HSBTE/R/004/10/Dec/2024										Regular										Name of Institute : Guru Daksh Government Polytechnic Hisar (004)																							
Branch Name : ELECTRONICS AND COMMUNICATION ENGINEERING										Date of Publication : 14 Feb 2025										Semester : 3																							
S.No.	Roll No	Student Name Father Name Mother Name	220832/212832						220833						221031						221034									Sessional Total	Marks Obtained	Result											
			Digital Electronics						Programming in C						Analog and Digital Communication						Electronic Devices and Circuits-II						Industrial/In-House Training - I						Electronic Design and Simulation										
			Theory			Practical			Theory			Practical			Theory			Practical			Theory			Practical			Practical						Practical										
			Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Modaration	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks				Modaration	Total	Practical Internal	Practical External	Total	Practical Internal	Practical External	Total			
		Max Marks=>	40	60			100	40	60	100	40	60			100	40	60	100	40	60			100	40	60	100	40	60			100	40	60	100	40	60	100	400	1000				
43	230041000061	SURENDER RAM KUMAR SUMITRA	14	10	-	5	29 R	19	34	53	18	8	-	-	26 R	19	39	58	20	3	-	-	23 R	30	36	66	9	6	-	-	15 R	24	AB	24 R	25	AB	25 R	20	AB	20 R	198	Re-Appear	Re-Appear
44	230041000062	SUSHIL DHAN SINGH SHAKUNTLA	28	11	-	5	44 R	31	46	77	29	24	-	-	53	33	48	81	30	21	-	-	51	33	37	70	25	16	-	-	41 R	24	46	70	31	48	79	34	47	81	298	Re-Appear	Re-Appear
45	230041000063	VIKAS KARAMBIR RAMRATI	20	6	-	5	31 R	24	46	70	23	5	-	-	28 R	28	45	73	26	25	-	-	51	32	40	72	21	9	-	-	30 R	26	45	71	30	50	80	35	50	85	265	Re-Appear	Re-Appear
46	230041000064	VIKAS LAKHMI CHAND SURAJMUKHI	18	5	-	5	28 R	22	34	56	20	9	-	-	29 R	24	42	66	20	12	-	-	32 R	31	40	71	16	6	-	-	22 R	25	AB	25 R	29	AB	29 R	22	AB	22 R	227	Re-Appear	Re-Appear
47	230041000065	VIKASH RAJENDER MAYAWATI	22	10	-	5	37 R	25	46	71	32	13	-	-	45 R	33	50	83	29	11	-	-	40 R	31	45	76	26	14	-	-	40 R	30	50	80	33	49	82	36	49	85	297	Re-Appear	Re-Appear
48	230041000066	VIRENDER ROHTASH RAJBALA	14	6	-	5	25 R	20	35	55	16	9	-	-	25 R	20	42	62	18	4	-	-	22 R	30	30	60	8	FIS	-	-	8 R	FIS	AB	13 R	25	AB	25 R	FIS	AB	14 R	FIS	Re-Appear	Re-Appear

1) This result is subject to Alteration, Modification & fulfillment of eligibility condition

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3) Total Grace of 5 marks in Maximum of 2 subjects


Signature of Assistant Secretary

HARYANA STATE BOARD OF TECHNICAL EDUCATION, PANCHKULA

Result of Branch ELECTRONICS AND COMMUNICATION ENGINEERING (10) 3^d SEMESTER (Batch 2023) Examination held in December 2024

Notification No. : HSBTE/R/004/10/Dec/2024														Regular														Name of Institute : Guru Daksh Government Polytechnic Hisar (004)																											
Branch Name : ELECTRONICS AND COMMUNICATION ENGINEERING														Date of Publication : 14 Feb 2025														Semester : 3																											
S.No.	Roll No	Student Name Father Name Mother Name	220832/212832									220833									221031									221034																					Sessional Total	Marks Obtained	Result		
			Digital Electronics									Programming in C									Analog and Digital Communication									Electronic Devices and Circuits-II									Industrial/In-House Training - I						Electronic Design and Simulation										
			Theory					Practical				Theory					Practical				Theory					Practical				Theory					Practical				Practical						Practical										
			Theory Internal	Theory External	Grace Marks	Moderation	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Moderation	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Moderation	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Moderation	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Moderation	Total	Practical Internal	Practical External	Total													
		Max Marks=>	40	60			100	40	60	100	40	60			100	40	60	100	40	60			100	40	60	100	40	60			100	40	60	100	40	60			100	40	60	100	400	1000											
49	230041000068	YOGESH JAI SINGH SAROJ	14	1	-	5	20 R	21	35	56	18	9	-	-	27 R	20	42	62	18	4	-	-	22 R	29	31	60	8	FIS	-	-	8 R	FIS	AB	13 R	25	AB	25 R	FIS	AB	14 R	FIS	Re-Appear	Re-Appear												
50	230041024001	AKASH MUKHTYAR MANJEET	24	20	-	5	49	29	50	79	32	20	-	-	52	33	51	84	25	15	-	-	40 R	32	39	71	25	20	-	-	45	28	49	77	32	44	76	33	43	76	293	Re-Appear	Re-Appear												
51	230041024002	AMAN SATYA NARAYAN KAVITA	24	16	-	5	45	28	51	79	36	23	-	-	59	36	50	86	30	31	-	-	61	33	41	74	26	20	-	-	46	34	48	82	30	41	71	33	40	73	310	676	676/1000												
52	230041024003	ANIL KUMAR TEJVIR SINGH USHA	19	16	-	5	40	23	45	68	29	20	-	-	49	28	42	70	22	24	-	-	46	30	42	72	20	20	-	-	40	24	40	64	27	42	69	20	42	62	242	580	580/1000												
53	230041024005	DIPANSHU JANGRA RAKESH KUMAR PINKI DEVI	20	4	-	5	29 R	25	49	74	25	2	-	-	27 R	30	44	74	23	13	-	-	36 R	31	42	73	22	5	-	-	27 R	28	42	70	30	43	73	26	41	67	260	Re-Appear	Re-Appear												
54	230041024006	JITENDER ISHWAR MAMTA RANI	16	9	-	5	30 R	20	34	54	20	6	-	-	26 R	28	38	66	20	5	-	-	25 R	30	41	71	18	5	-	-	23 R	22	37	59	27	37	64	20	37	57	221	Re-Appear	Re-Appear												

1) This result is subject to Alteration, Modification & fulfillment of eligibility condition

2) 'R' means Re-Appear, 'AB' means Absent

3) Total Grace of 5 marks in Maximum of 2 subjects

Ronia

Signature of Assistant Secretary

HARYANA STATE BOARD OF TECHNICAL EDUCATION, PANCHKULA


Result of Branch ELECTRONICS AND COMMUNICATION ENGINEERING (10) 3^d SEMESTER (Batch 2023) Examination held in December 2024

Notification No. : HSBTE/R/004/10/Dec/2024										Regular										Name of Institute : Guru Daksh Government Polytechnic Hisar (004)																							
Branch Name : ELECTRONICS AND COMMUNICATION ENGINEERING															Date of Publication : 14 Feb 2025										Semester : 3																		
S.No.	Roll No	Student Name Father Name Mother Name	220832/212832						220833						221031						221034												Sessional Total	Marks Obtained	Result								
			Digital Electronics						Programming in C						Analog and Digital Communication						Electronic Devices and Circuits-II						Industrial/In-House Training - I			Electronic Design and Simulation													
			Theory			Practical			Theory			Practical			Theory			Practical			Theory			Practical			Practical			Practical													
			Theory Internal	Theory External	Grace Marks	Moderation	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Moderation	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Moderation	Total	Practical Internal	Practical External	Total	Theory Internal	Theory External	Grace Marks	Moderation	Total	Practical Internal				Practical External	Total	Practical Internal	Practical External	Total			
		Max Marks=>	40	60			100	40	60	100	40	60			100	40	60	100	40	60			100	40	60	100	40	60			100	40	60	100	40	60	100	40	60	100	400	1000	
55	230041024008	MOHIT SURENDER KUMAR BIRMATI	22	10	-	5	37 R	27	36	63	20	20	-	-	40	24	36	60	19	20	-	-	39 R	29	40	69	16	12	-	-	28 R	22	34	56	26	44	70	20	43	63	225	Re-Appear	Re-Appear
56	230041024010	SACHIN RAMNIWASH BABITA	26	25	-	5	56	31	55	86	31	20	-	-	51	34	51	85	32	20	-	-	52	33	50	83	32	24	-	-	56	34	50	84	35	49	84	26	48	74	314	711	711/1000
57	230041024012	SUHANI RAMPHAL KANTA DEVI	22	22	-	5	49	28	51	79	32	20	-	-	52	33	46	79	28	36	-	-	64	31	51	82	19	10	-	-	29 R	28	45	73	30	47	77	30	47	77	281	Re-Appear	Re-Appear
58	230041024013	SUNNY RAM BHAGAT BALA	18	2	-	5	25 R	23	32	55	17	6	-	-	23 R	19	35	54	21	2	-	-	23 R	23	38	61	14	2	-	-	16 R	27	34	61	27	37	64	20	AB	20 R	209	Re-Appear	Re-Appear
59	230041024014	SUPERSON SURENDER KUMAR SOMA	26	7	-	5	38 R	30	49	79	30	20	-	-	50	31	49	80	24	11	-	-	35 R	31	48	79	23	15	-	-	38 R	28	46	74	31	51	82	33	50	83	287	Re-Appear	Re-Appear

1) This result is subject to Alteration, Modification & fulfillment of eligibility condition

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3) Total Grace of 5 marks in Maximum of 2 subjects



Signature of Assistant Secretary