

HARYANA STATE BOARD OF TECHNICAL EDUCATION, PANCHKULA
Result of Branch ELECTRONICS AND COMMUNICATION ENGINEERING (10) 3^d SEMESTER (Batch 2019) Examination held in June 2024

Notification No. : HSBTE/Re/004/10/Jun/2024	ReAppear	Name of Institute : Guru Daksh Government Polytechnic Hisar (004)
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Branch Name : ELECTRONICS AND COMMUNICATION ENGINEERING	Date of Publication : 16 Aug 2024	Semester : 3
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S.No.	Roll No	Student Name Father Name Mother Name	180832						181031						181033						181034						181035						181036						Sessional Marks / Max Marks	Marks Obtained / Max Marks	Result						
			Digital Electronics						Electronic Device & Circuits						Principles of Communication Engineering						Computer Progammig using C						Network Filters and Transmission Lines						Electronic Instruments & Measurements														
			Theory				Practical		Theory				Practical		Theory				Practical		Theory				Practical		Theory				Practical		Theory				Practical										
			Theory External	Assesment 5%	Grace Marks	Modaration	Total	Practical External	Total	Theory External	Assesment 5%	Grace Marks	Modaration	Total	Practical External	Total	Theory External	Assesment 5%	Grace Marks	Modaration	Total	Practical External	Total	Theory External	Assesment 5%	Grace Marks	Modaration	Total	Practical External	Total	Theory External	Assesment 5%	Grace Marks	Modaration	Total	Practical External	Total										
		Max Marks=>	100 /95	5			100	50	50	100 /95	5			100	50	50	100 /95	5			100	50	50	100 /95	5			100	50	50	100 /95	5			100	50	50										
1	190041020013	SACHIN VASHISHTH OMVEER VASHISHTH RAMVATI VASHISHTH	33	0	-	-	33	35	35	8	0	-	-	8 R	43	43	32	4	-	8	44	44	44	5	0	-	-	5 R	40	40	21	4	-	8	33	37	37	17	0	-	-	17 R	36	36	210/325	Re-Appear	Re-Appear

- 1) This result is subject to Alteration, Modification & fulfillment of eligibility condition
- 2) 'R' means Re-Appear, 'AB' means Absent
- 3) Grace Marks@5 marks in two subject


Signature of Assistant Secretary