HARYANA STATE BOARD OF TECHNICAL EDUCATION, PANCHKULA

Result of Branch ELECTRONICS AND COMMUNICATION ENGINEERING (10) 1st Year (Batch 2021) Examination held in December 2024

Notification No. : HSBTE/Re/004/10/Dec/2024 | ReAppear | Name of Institute : Guru Daksh Government Polytechnic Hisar (004)

Branch Name : ELECTRONICS AND COMMUNICATION ENGINEERING | Date of Publication : 14 Feb 2025 | Semester : 1

S.No.	Roll No	Student Name / Father Name / Mother Name	180011 English Theory Internal	180011 English Theory External	180011 English Grace Marks	180011 English Modaration	180011 English Total	180011 English Practical External	180011 English Practical Total	180012 Applied Mathematics Theory Internal	180012 Applied Mathematics Theory External	180012 Applied Mathematics Grace Marks	180012 Applied Mathematics Modaration	180012 Applied Mathematics Total	180013 Applied Physics Theory Internal	180013 Applied Physics Theory External	180013 Applied Physics Grace Marks	180013 Applied Physics Modaration	180013 Applied Physics Total	180013 Applied Physics Practical External	180013 Applied Physics Practical Total	180015 Engineering Graphics Theory Internal	180015 Engineering Graphics Theory External	180015 Engineering Graphics Grace Marks	180015 Engineering Graphics Modaration	180015 Engineering Graphics Total	180015 Engineering Graphics Practical External	180015 Engineering Graphics Practical Total	180016 Environmental Studies Theory Internal	180016 Environmental Studies Theory External	180016 Environmental Studies Grace Marks	180016 Environmental Studies Modaration	180016 Environmental Studies Total	180016 Environmental Studies Practical External	180016 Environmental Studies Practical Total	180018 Information Technology Practical External	180018 Information Technology Practical Total	180019 Internet of Things & Artificial Intelligence Practical External	180019 Internet of Things & Artificial Intelligence Practical Total	180020 General Workshop Practice Practical External	180020 General Workshop Practice Practical Total	180814 Computer Fundamentals Theory Internal	180814 Computer Fundamentals Theory External	180814 Computer Fundamentals Grace Marks	180814 Computer Fundamentals Modaration	180814 Computer Fundamentals Total	180817 Fundamental of Electrical and Electronics Engineering Theory Internal	180817 Fundamental of Electrical and Electronics Engineering Theory External	180817 Fundamental of Electrical and Electronics Engineering Grace Marks	180817 Fundamental of Electrical and Electronics Engineering Modaration	180817 Fundamental of Electrical and Electronics Engineering Total	180817 Fundamental of Electrical and Electronics Engineering Practical External	180817 Fundamental of Electrical and Electronics Engineering Practical Total	Sessional Marks / Max Marks	Marks Obtained / Max Marks	Result
		Max Marks=>	40	60			100	50	50	40	60			100	40	60			100	50	50	40	60			100	25	25	40	60			100	50	50	50	50	100	100	50	50	40	60			100	40	60			100	50	50			
1	210041000025	LOKESH DINESH KUMAR DARSHNA	16	24	-	-	40	41	41	23	37	-	-	60	11	39	-	-	50	39	39	27	34	-	-	61	16	16	15	32	-	-	47	39	39	37	37	62	62	31	31	7	36	-	-	43	28	26	-	-	54	27	27	172/300	819/1425	819
2	210041000032	NAVEEN PHOOL KUMAR SUMAN DEVI	27	19	-	-	46	45	45	25	17	-	-	42	23	17	-	-	40	35	35	24	17	-	5	46	12	12	27	26	-	-	53	43	43	34	34	73	73	31	31	21	15	-	5	41	25	20	-	-	45	40	40	209/300	835/1425	835

1) This result is subject to Alteration, Modification & fulfillment of eligibility condition
2) 'R' means Re-Appear, 'AB' means Absent
3) Total Grace of 5 marks in Maximum of 2 subjects

Signature of Assistant Secretory