HARYANA STATE BOARD OF TECHNICAL EDUCATION, PANCHKULA
Result of Branch COMPUTER ENGINEERING (08) 3rd SEMESTER (Batch 2019) Examination held in June 2024

S.No.	Roll No	Student Name / Father Name / Mother Name	180831 Operating Systems (OS) Theory Ext	Assess 5%	Grace	Moder	Total	Prac Ext	Prac Total	180832 Digital Electronics Theory Ext	Assess 5%	Grace	Moder	Total	Prac Ext	Prac Total	180833 Multimedia Applications Theory Ext	Assess 5%	Grace	Moder	Total	Prac Ext	Prac Total	180835 Data Communication Theory Ext	Assess 5%	Grace	Moder	Total	180836 Programming in C Theory Ext	Assess 5%	Grace	Moder	Total	Prac Ext	Prac Total	Sessional Marks / Max	Marks Obtained / Max	Result
Max Marks=>			100/95	5			100	50	50	100/95	5			100	50	50	100/95	5			100	50	50	100/95	5			100	100/95	5			100	50	50			
1	190040800014	ASHISH / KARAN SINGH / POONAM DEVI	24	5	-	8	37	40	40	AB	0	-	-	0 R	38	38	47	5	-	8	60	35	35	33	5	-	8	46	AB	0	-	-	0 R	40	40	221/275	Re-Appear	Re-Appear
2	190040800056	PANKAJ KUMAR / DHARAMPAL SINGH / SAVITA DEVI	40	0	-	9	49	34	34	22	5	-	8	35	38	38	31	5	-	8	44	29	29	25	5	-	8	38	64	0	-	-	64	33	33	181/275	545/975	545
3	190040800071	RITESH KUMAR / ANIL KUMAR / SUNITA	AB	0	-	-	0 R	AB	0 R	AB	0	-	-	0 R	AB	0 R	20	5	-	8	33	34	34	8	0	-	-	8 R	9	0	-	-	9 R	AB	0 R	186/275	Re-Appear	Re-Appear
4	190040800075	SAHIL / BALJEET / RAJBALA	53	0	-	9	62	25	25	54	0	-	-	54	35	35	35	0	-	-	35	35	35	31	5	-	8	44	56	0	-	-	56	36	36	183/275	565/975	565
5	190040800082	SANJEET / SHYAM YADAV / KUSUM DEVI	50	0	-	-	50	34	34	32	5	-	8	45	27	27	37	0	-	-	37	34	34	56	0	-	-	56	38	0	-	-	38	35	35	192/275	548/975	548
6	190040800100	VIJESH KUMAR / SHIV SAGAR / MANITA DEVI	26	0	-	9	35	35	35	37	0	-	-	37	33	33	22	5	-	8	35	34	34	38	5	-	8	51	31	5	-	8	44	36	36	204/275	544/975	544

1) This result is subject to Alteration, Modification & fulfillment of eligibility condition
2) 'R' means Re-Appear, 'AB' means Absent
3) Grace Marks@5 marks in two subject

1 of 2

Signature of Assistant Secretory

HARYANA STATE BOARD OF TECHNICAL EDUCATION, PANCHKULA
Result of Branch COMPUTER ENGINEERING (08) 3rd SEMESTER (Batch 2019) Examination held in June 2024

Notification No. : HSBTE/Re/004/08/Jun/2024   ReAppear   Name of Institute : Guru Daksh Government Polytechnic Hisar (004)

Branch Name : COMPUTER ENGINEERING   Date of Publication : 16 Aug 2024   Semester : 3

S.No.	Roll No	Student Name / Father Name / Mother Name	180831 Operating Systems (OS) Theory Theory External	Assesment 5%	Grace Marks	Modaration	Total	Practical Practical External	Total	180832 Digital Electronics Theory Theory External	Assesment 5%	Grace Marks	Modaration	Total	Practical Practical External	Total	180833 Multimedia Applications Theory Theory External	Assesment 5%	Grace Marks	Modaration	Total	Practical Practical External	Total	180835 Data Communication Theory Theory External	Assesment 5%	Grace Marks	Modaration	Total	180836 Programming in C Theory Theory External	Assesment 5%	Grace Marks	Modaration	Total	Practical Practical External	Total	Sessional Marks / Max Marks	Marks Obtained / Max Marks	Result
		Max Marks=>	100 /95	5			100	50	50	100 /95	5			100	50	50	100 /95	5			100	50	50	100 /95	5			100	100 /95	5			100	50	50			
7	190040800110	VISHAL GOURAV RAMNATH GEETA DEVI	33	0	-	-	33	37	37	40	0	-	-	40	32	32	33	0	-	-	33	36	36	31	5	-	8	44	20	5	-	8	33	36	36	188/275	512/975	512

1) This result is subject to Alteration, Modification & fulfillment of eligibility condition
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3) Grace Marks@5 marks in two subject

Signature of Assistant Secretory