## HARYANA STATE BOARD OF TECHNICAL EDUCATION, PANCHKULA Result of Branch ELECTRICAL ENGINEERING (09) 5<sup>th</sup> SEMESTER (Batch 2019) Examination held in June 2025

Notific	ation No. : HSB	TE/Re/004/09/Jun/202	25					Re	Appe	ar																		1	Name of	Institute	: Guru D	aksh Gover	nment Polytech	nnic Hisar (004)	
Branch Name : ELECTRICAL ENGINEERING												Date of Publication : 14 Jul 2025 Semester :										r : 5													
S.No.		Student Name Father Name	180951						180952			180953						180954A/180954B/180954C						180955									Result		
		Mother Name	Electrical Machines-II				E	Electrical Pow			wer-I Programmable Logic Controllers And Microcontrollers					And	Instrumentation, Auto Electrical, Solor Panel Installation and Maintenance						Utilization of Electrical Energy			Industrial training		Minor Project Work		Marks	Marks				
			Theory Practical				al	Theory			Theory				Prac	ctical	Theory			Practical		Theory			Practical		Practical		/ Max	Max					
			Theory External	_   <u>~</u>   ä	Modaration	Practical External	Total	Theory External	Se S	Grace Marks Modaration		Total	Theory External	Grace Marks	Modaration	Total	Practical External	Total	Theory External	Grace Marks	Moderation	Total	Practical External	Total	Theory External	Grace Marks	Modaration	Total	Practical External	Total	Practical External	Total	Sessional Marks /	Marks Obtained /	
		Max Marks=>	100		10	0 5	0 5	0 10	0		100	100			100	50	50	100			100	50	50	100			100	100	100	50	50				
1	190040900004	ANISH KHAN RAJU KHAN SHAHNAZ	12	-	- 12 R	2 2	4 2	4 3	3 -	-	33	36	-	-	36	25	25	56	-	-	56	26	26	19	-	-	19 R	52	52	42	42	210/425	Re-Appear	Re-Appea	
2	190040900050	SAHIL RAMESH KUMAR SUMITRA DEVI	17	-	- 17		7 3	7 3	5 -	-	35	33	-	-	33	39	39	33	-	-	33	39	39	33	-	-	33	85	85	40	40	291/425	Re-Appear	Re-Appear	

<sup>1)</sup> This result is subject to Alteration, Modification & fulfillment of eligibility condition

2) 'R' means Re-Appear, 'AB' means Absent 3) Total Grace of 5 marks in Maximum of 2 subjects

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**Signature of Assistant Secretory**