Assignment # 5: Problem Set 3

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2/6/2013

Problem 1

Practice writing and using subroutines by writing a subroutine that calculates Z = (a0 * x0) - (a1 * x1).

The values of the four variables a0, a1, x0, x1 are to be passed to the subroutine in registers. The address of variable Z is to be passed to the subroutine in a register.

The "main" program is to call the subroutine two times, once for each of the following sets of data. You may not use "immediate" data all values are to be read from memory and results are to be stored in memory.

x0 and x1 are to be 32-bit integers. a0, a1, and z are to be stored using Q24.8 format.

First call: x0=200, x1=100, a0=5.25, a1=6.75. Store the answer at variable z1.

Second call: x0=300, x1=200, a0=3.5, a1=4.125. Store the answer at variable z2.

For convenience, define the arguments to be passed to the subroutine at the end of your code section, and the variables to be written in a data section. There should be eight "variables" in the code section two sets of four arguments. There should be two variables in the data section.

You can test this with the simulator, but version to be submitted must be executed in RAM on the Discovery board. Submit a printout of your source program, and a screen image of the debug window, with the results highlighted (circled) in a memory window.

Debugging Problem 1

The Assembly Language program for Problem 1 is named PS3-1.s, and can be found in the Source Program Section at the end of this document. For the first run, the expected value of z1 is 375_{10} . In Q24.8 format, this is 0x00017700. The value was expected to be stored at Memory Address 0x20003014. For the second run, the expected value of z2 is 225_{10} . In Q24.8 format, this is 0x0000E100. The value was expected to be stored at Memory Address 0x20003018.

After the program was written and simulated, it was executed in RAM on the Discovery board. Figures 1 and 2 show the debugging window after the program was executed in RAM on the board, with the values circled. Both figures indicate that the program for Problem 1 executed correctly because the results are correct.

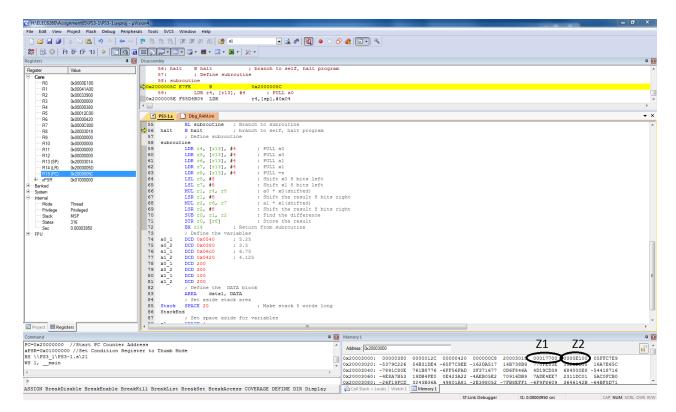


Figure 1: A full view of the debugging window for Problem 1. The values for z1 and z2 are circled. z1 is 0x00017700 and z2 is 0x0000E100. These values are correct, so one can see that the program executed correctly.

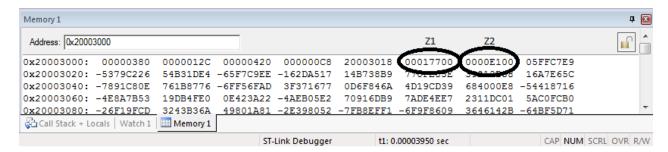


Figure 2: A view of the memory window for Problem 1 after the program was executed. As stated in Figure 1, the values for z1 and z2 are 0x00017700 and 0x0000E100, respectively. These are the expected values, and the occur at the expected locations, so one can conclude that the program written for Problem 1 executed correctly.

Problem 2

Given below is a C program that includes several functions calls.

- (a) Write the equivalent routines in ARM assembly language and test the program in the Keil debugger. Submit two screen captures of the debug window, showing the final result returned for each of the two functions called by the main program. The main program and the three "functions" should be implemented as separate routines/subroutines as listed (i.e. do not "merge" them to optimize the program.)
- (b) Enter and compile the C program, as given, and compare the "listing file" to your hand-written assembly language program. Alternatively, you may view the disassembled code in the debugger window to see the C and equivalent assembly language instructions. Turn off any optimizations that would normally be performed by the C compiler.

Problem 2, part (a)

The ARM assembly language program that emulates the above C program was written and named PS3-2.s. The program can be found in the Source Programs section at the end of this document. The program was tested in the Keil debugger. Figure 3 displays the memory on the board after f3 was called, and Figure 4 displays the memory on the board after f2 was called. Both figures display the expected values for the final result returned for each of the two functions called by the main program, so it can be observed that the hand-written program works correctly.

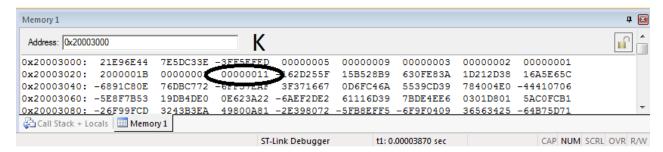


Figure 3: A view of the memory window after the main function in Problem 2 calls f3. The circled value is the value for k, 0x00000011, which corresponds to the expected result, 17_{10} . This means that the program executed f3 and f1 correctly.

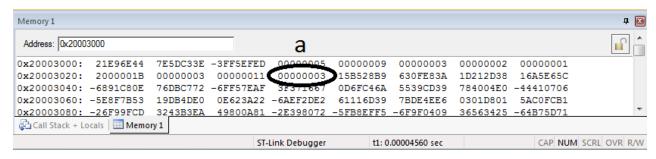


Figure 4: A view of the memory window after the main function in Problem 2 calls f2. The circle value is the value for a, 0x00000003, which corresponds to the expected result, 3. This means that the program executed f2 correctly.

Problem 2, part (b)

The C program listed above was entered and compiled, as given. All compiler optimizations were turned off. Then, it was debugged on the Discovery board so that the disassembled code could be used to compare the compiler output to my hand-written assembly language program. A section of the disassembled code was copied into PS3-2b.txt, which can be found in the Source Programs section at the end of this document.

The hand-written program was greater than 60 lines of assembly, while the disassembled code was just over 30 lines of assembly. This means that the C program, once disassembled, was approximately twice as fast as the hand-written program. The line length difference can be attributed, at least in part, to the way the C program treated certain register values as constants. Additionally, the hand-written code interacted with the stack value by value (STR r1, [r13, #-4]!), while the C program disassembled to use PUSH and POP. Also, the disassembled code was simply more elegant, even without any optimizations.

Conclusions

Both ARM assembly language programs written were functionally correct. Each program was executed on the Discovery board, and each program returned expected values. The C program, when disassembled, was more efficient than the hand-written program.

Source Programs

PS3-1.s

```
Brian Arnberg - ELEC6260
          Problem Set 3, Problem 1
   ;; Compute the following:
   ;; Z = (a0*x0) - (a1*x1)
  ;; Using a subroutine.
;; The 'main' will call the subroutine
       twice, each time passing all 5
       variables with registers.
   ;; Z will be passed as an ADDRESS \,
   ;; x0,x1 are 32bit integers
   ;; a0,a1,Z using Q24.8 format
   ; Define the CODE block
             AREA RESET, CODE, READONLY
18
              ; Begin
             ENTRY
   _main LDR r13, =StackEnd
21
                                             ; get a0_{-}1
             LDR r0, =a0_1
22
                                            ; r4 = a0_{-}1
             LDR r4, [r0]
23
             LDR r0, =x0_1
LDR r5, [r0]
                                            ; get x0_{-}1
                                            ; r5 = x0_{-1}
; get a1_1
25
             LDR r0, =a1_1
26
                                           ; r6 = a1_1
; get x1_1
; r7 = x1_1
; get address for z1
             LDR r6, [r0]
27
             LDR r0, =x1_1
28
             LDR r7, [r0]
29
             LDR r8, =z1
30
              ; Load variables into stack
31
             TR r8, [r13, \#-4]!; PUSH =z1 to stack STR r7, [r13, \#-4]!; PUSH x1 to stack STR r6, [r13, \#-4]!; PUSH a1 to stack STR r5, [r13, \#-4]!; PUSH a1 to stack STR r4, [r13, \#-4]!; PUSH a0 to stack STR r4, [r13, \#-4]!; PUSH a0 to stack
32
33
34
35
36
              ; Branch to sub-routine (for first run)
37
             BL subroutine ; Branch to subroutine
38
              ; Return from subroutine, get new values
39
             LDR r0, =a0_2 ; get a0_{-2}
LDR r4, [r0] ; r4 = a0_{-2}
40
                                           ; get x0_2
             LDR r0, =x0_2
42
             LDR r5, [r0]
                                            ; r5 = x0_{-2}
43
                                            ; get a1_2
; r6 = a1_2
             LDR r0, =a1_2
             LDR r6, [r0]
45
             LDR r0, =x1_2

LDR r7, [r0]

LDR r8, =z2
46
                                            ; get x0_2
                                       ; r7 = x1_2
; get address for z1
47
48
              ; Load variables into stack
49
             50
             STR r7, [r13, #-4]!; PUSH x1 to stack
STR r6, [r13, #-4]!; PUSH a1 to stack
STR r5, [r13, #-4]!; PUSH x0 to stack
STR r4, [r13, #-4]!; PUSH a0 to stack
STR r4, [r13, #-4]!; PUSH a0 to stack
51
53
54
             BL subroutine ; Branch to subroutine B halt ; branch to self, halt
55
   halt
             B halt
                                   ; branch to self, halt program
56
57
              ; Define subroutine
58
   subroutine
                                            ; PULL a0
             LDR r4, [r13], #4
59
             LDR r5, [r13], #4
LDR r6, [r13], #4
                                             ; PULL x0
60
                                             ; PULL a1
61
             LDR r7, [r13], #4
LDR r8, [r13], #4
LSL r5, #8
                                            ; PULL x1
62
                                            ; PULL =z
; Shift x0 8 bits left
63
64
                                            ; Shift x1 8 bits left
             LSL r7, #8
65
                                            ; a0 * x0(shifted)
; Shift the result 8 bits right
             MUL r1, r4, r5
66
             LSR r1, \#8
67
             MUL r2, r6, r7
LSR r2, #8
SUB r0, r1, r2
                                           ; a1 * x1(shifted)
                                            ; Shift the result 8 bits right ; Find the difference
69
70
                                           ; Store the result
             STR r0, [r8]
```

```
BX r14
                               ; Return from subroutine
              ; Define the variables
DCD 0x0540 ; 5.25
DCD 0x0380 ; 3.5
DCD 0x06c0 ; 6.75
DCD 0x0420 ; 4.125
73
   a0_{-}1
   a0_{-}2
75
   a1_{-}1
76
   a1_{-}2
               DCD 200
78
   x0_{-}1
   x0_{-2}
               DCD 300
79
               DCD 100
80
   x1_{-}1
               DCD 200
   x1_{-}2
81
               ; Define the DATA block
AREA data1, DATA
; Set aside stack area
82
83
84
               SPACE 20
   Stack
                                                   ; Make stack 5 words long
86
   StackEnd
               ; Set space aside for variables SPACE 4
87
   z1
               SPACE 4
89
   z2
               END
90
```

PS3-2.s

```
;; Brian Arnberg - ELEC6260
  Execute the C program
  ; ;
                                                      ;;
  ; ;
   ;;
                                                      ;;
   ; Define the CODE block
            AREA RESET, CODE
11
            ; Begin
            ENTRY
13
   _main LDR r13, =StackEnd
            ; int a
15
            \stackrel{\cdot}{\text{LDR}} r5, =aa
                                         ; Address of aa to r0
17
            ; k = 0;
            LDR r0, =kk
                                         ; Get Address of kk
18
            MOV r1, #0
STR r1, [r0]
                                         ; Set r1 to 0
; Store r1 (#0) to r0 ([kk])
19
20
             ; f3(3)
21
            \frac{\text{MOV}}{\text{r5}}, #3
22
                                         ; Put argument (#3) in r5
            STR r5, [r13, #-4]!
BL func3
                                         ; Push argument (#3) to stack
23
24
                                         ; POP argument off of stack
25
            ADD r13, #4
            ; a = f2(2)
26
            MOV r1, #2
27
            STR r1, [r13, \#-4]!
                                        ; Push argument (#2) to stack
28
            BL func2
29
            LDR r0, =aa
30
                                         ; Get address for aa
            LDR r1, [r13], #4; Get returned value from f2
STR r1, [r0]; Store returned value to aa
31
32
33
  halt
            B halt
                                ; branch to self, halt program
34
35
   ;; int f1(int x1, int x2) {
            return x1 + x2;
36
  ; ;
   ;; }
37
38
  func1
            LDR r0, [r13], #4 ; Take 1st argument from stack, then change sp LDR r1, [r13], #4 ; Take 2nd argument from stack, then change sp
39
40
            ADD r0, r0, r1 ; Add arg1 and arg2, then put in r0 STR r0, [r13, \#-4]! ; Store result to stack
41
42
            BX r14 ; Return from function
   ;; int f2(int x1) {
            return x1 + 1;
45
  ; ;
   ;; }
46
  func2
47
                                      ; Load argument from stack
            LDR r0, [r13]
48
                                ; Add 1 to the argument
; Store result to stack, save over previous value
49
            ADD r0, #1
            STR r0, [r13]
50
            BX r14 ; Return from function
51
   ;; void f3(int r) {
52
53
            int j;
  ; ;
            for (j=0; j<2; j++)
54
  ;;
55
                     k = k + f1(r+j, 5);
  ; ;
   :: }
56
57
  func3
            LDR r7, [r13]
                                         ; load argument from stack
58
                                         ; Put current return address in stack
            STR r14, [r13, \#-4]!
59
            EOR r5, r5, r5
                                        ; Use r5 for j, Clear all bits
60
            MOV r6, #2
CMP r5, r6
                                         ; Use for N
61
  loopf3
                              ; Compare j to 2
62
            BGE endf3
                                      ; if j \ge 2, branch to endf3
63
                                         ; Compute r + j
            ADD r0, r7, r5
64
            MOV r1, #5 ; Set r1 to 5
STR r5, [r13, #-4]! ; PUSH j to stack
STR r6, [r13, #-4]! ; PUSH N to stack
STR r7, [r13, #-4]! ; PUSH Argument to stack
STR r0, [r13, #-4]! ; PUSH first argument to f1 to stack
STR r1, [r13, #-4]! ; PUSH second argument to f1 to stack
65
66
67
68
69
            STR r1, [r13, \#-4]!
                                          ; PUSH second argument to f1 to stack
70
            BL func1
                             ; Call f1(r + j, 5)
71
            LDR r2, [r13], #4 ; Get returned value

LDR r7, [r13], #4 ; Get Argument from the LDR r6, [r13], #4 ; Get N
72
                                         ; Get Argument from stack
73
```

```
LDR r5, [r13], #4
                                    ; Get j
           LDR r10, =kk
LDR r3, [r10]
                                      ; Get address of kk
76
                                      ; Store current kk to r3
77
           ADD r2, r2, r3
                                     ; SUM k and result of the function call
78
                                     ; Store k
           STR r2, [r10]
ADD r5, r5, #1
79
80
                                      ; Increment j
           B loopf3
81
  endf3
82
           LDR r14, [r13], #4; Get return address from stack
83
           BX r14; Return from function
84
85
86
            ; Define the DATA block
           AREA data1, DATA
87
           ; Set aside stack area
           SPACE 40
                                      ; Make stack 10 words long
  Stack
89
  StackEnd
90
            ; Set space aside for variables
91
  kk
           SPACE 4
92
           SPACE 4
93
  aa
           END
```

PS3-2b.txt

```
14: int f1(int x1, int x2) {
0\,x\,200000\,228\quad 460\,2
                        MOV
                                           r2, r0
                   \mathtt{return} \ \mathtt{x1} \ + \ \mathtt{x2} \, ;
    15:
0x2000022A 1850
                         ADDS
                                           r0, r2, r1
    16: }
    17:
0x2000022C 4770
                         BX
    18: int f2(int x1) {
0x2000022E 4601 MOV
                                           {\bf r}{\bf 1} , {\bf r}{\bf 0}
    19:
                   return x1 + 1;
0x20000230 1C48
                    ADDS
                                           r0, r1,#1
    20: }
    21:
0 \times 20000232 4770
                         BX
                                           lr
    22: void f3(int r) {
                   int j;
0x20000234 B510
                         PUSH
                                           \{r4, lr\}
0 \times 20000236 4604
                         MOV
                                           r4, r0
    24:
                   for (j = 0; j < 2; j++)
0x20000238 2300
                         MOVS
                                           r3,\#0x00
0x2000023A E009
                         В
                                           0\,x\,20\,00\,02\,50
                             k = k + f1(r + j, 5);
    25:
0x2000023C 18E0
                         ADDS
                                           r0, r4, r3
0x2000023E 2105
                         MOVS
                                           r1, #0x05
                                           f1 (0x20000228)
0x20000240 F7FFFFF2
                         BL.W
                                           r1\ , [\ pc\ , \#40] \quad \  ; \ \ @0x20000270
0 \times 20000244 490A
                         LDR
0x20000246 6809
                         LDR
                                           r1, [r1, \#0x00]
0 \times 20000248 4408
                         ADD
                                          r0, r0, r1
0x2000024A 4909
                         LDR
                                           {\rm r1} \; , [\; {\rm pc} \; , \#36] \quad \; ; \quad @0{\rm x}20000270
0x2000024C 6008
                         STR
                                           r0 , [ r1 ,#0x00 ]
0x2000024E 1C5B
                         ADDS
                                           r3, r3, #1
0x20000250 2B02
                         CMP
                                           r3, #0x02
0 \times 20000252 DBF3
                                           0x2000023C
                         BLT
    26: }
    27:
0x20000254 BD10
                         POP
                                           \{r4, pc\}
    28: void main() {
     29:
                  int a;
0 \times 20000256 B500
                         PUSH
                                           \{lr\}
    30:
                   k = 0;
0x20000258 2000
                                           r0,\#0x00
                         MOVS
                                           {\rm r1\ , [\ pc\ , \#20]} \quad ; \quad @0{\rm x20000270}
0x2000025A 4905
                         LDR.
0x2000025C 6008
                         STR
                                           r0, [r1, \#0x00]
                  f3(3);
    31:
0x2000025E 2003
                         MOVS
                                           r0,\#0x03
0x20000260 F7FFFFE8 BL.W
                                           f3 (0x20000234)
                   a = f2(2);
    32:
0 \times 20000264 2002
                         MOVS
                                           r0, #0x02
                                           f2 (0x2000022E)
0x20000266 F7FFFFE2 BL.W
0x2000026A EE000A10 VMOV
                                           s0, r0
    33: }
```