





```
D:\u00e4verilog\u00adWeek2\u00adnand\u00adnand_tb.v - Notepad++
                                                                                              X
File Edit Search View Encoding Language Settings Tools Macro Run Plugins Window ?
                                                                                                      Х
                     📙 nand, v 🗵 📙 nand_tb, v 🗵 📙 and, v 🗵 📙 in v, v 🗵
       `timescale 1ns/1ns
      module nand tb();
        reg a;
        reg b;
        wire out1;
        nand1 n1(a,b,out1);
  8
  9
         initial begin
 10
          a = 0:
 11
          b = 0;
          #100 $finish;
 12
 13
        end
 14
 15
        always begin
16
          #1 a = ~a;
 17
          #1 b = ~b;
 18
        end
 19
        initial begin
          $display("Hello World");
 21
          $dumpfile("output.vcd");
 23
          $dumpvars(0);
 24
        end
       endmodule
```

Ln:1 Col:1 Pos:1

Windows (CR LF) UTF-8

INS

Verilog file

length: 334 lines: 25

